2-Wire Hall Effect Switch Datasheet



1. Features and Benefits

- Wide operating voltage range: from 2.7V to 24V
- Integrated self-diagnostic functions activating dedicated Safe Mode
- Reverse supply voltage protection
- Under-Voltage Lockout protection
- Thermal Protection
- HW component Qualified according to ISO26262-8:13 for use in safety critical systems.

2. Application Examples

- Automotive, Consumer and Industrial
- Brake light switch
- Window lifter
- Door lock
- Seatbelt buckle
- Seat positioning
- Transmission applications
- Electrical power steering

3. Ordering Information

Product Code	Temperature Code	Package Code	Option Code	Packing form Code
MLX92241	L	SE	AAA-0xx	RE
MLX92241	L	UA	AAA-0xx	BU

Table 1: Ordering information

Legend:

Temperature Code: L(-40°C to 150°C)

Package Code: UA=TO92-3L / SE=TSOT-3L

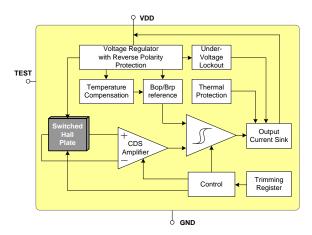
Option Code: AAA-0xx => 2 wire Hall Effect Switch

Packing Form: BU=Bulk | RE = Reel | CA = Ammopack delivery

Ordering example: MLX92241LSE-AAA-0xx-RE



4. Functional Diagram



5. General Description

The Melexis MLX92241 is based on the Melexis Hall-effect latest platform, designed in mixed signal submicron CMOS technology.

The device integrates a voltage regulator, Hall sensor with advanced offset cancellation system and a current sink-configured output driver and integrated capacitor all in a single package.

Based on the proven in use platform, the magnetic core is using an improved offset cancellation system allowing faster and more accurate processing while being temperature insensitive and stress independent. In addition a pre-programmable temperature coefficient is implemented to compensate the natural behavior of certain types of magnets becoming weaker with rise in temperature.

The included voltage regulator operates from 2.7 to 24V, hence covering a wide range of applications. With the built-in reverse voltage protection, a serial resistor or diode on the supply line is not required so that even remote sensors can be specified for low voltage operation down to 2.7V while being reverse voltage tolerant.

In an event of a drop below the minimum supply voltage during operation, the under-voltage lock-out protection will automatically freeze the device, preventing the electrical perturbation to affect the magnetic measurement circuitry. The output current state is therefore only updated based on a proper and accurate magnetic measurement result.

The two-wire interface not only saves one wire, but also allows implementation of diagnostic functions as reverse polarity connection and malfunction detection.

The on-chip thermal protection also switches off the output if the junction temperature increases above an abnormally high threshold. It will automatically recover once the temperature decreases below a safe value.

The MLX92241 is delivered in a Green and RoHS compliant Plastic Single-in-Line (TO-92) for through-hole mount, or in 3-pin Thin Small Outline Transistor (TSOT) for surface mount process.

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6. Glossary of Terms

Tesla Units for the magnetic flux density, 1 mT = 10 Gauss

TC Temperature Coefficient in ppm/°C

7. Absolute Maximum Ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

Parameter	Symbol	Value	Units
Supply Voltage (1, 2)	V_{DD}	+27	V
Supply Current (1, 2, 3)	I_{DD}	+20	mA
Supply Current (1, 3, 4)	I_{DD}	+50	mA
Reverse Supply Voltage (1, 2)	V_{DDREV}	-24	V
Reverse Supply Current (1, 2, 5)	I_{DDREV}	-20	mA
Reverse Supply Current (1, 4, 5)	I _{DDREV}	-50	mA
Maximum Junction Temperature (6)	T_J	+165	°C
Operating Temperature Range	T_A	-40 to 150	°C
Storage Temperature Range	T_s	-55 to 165	°C
ESD Sensitivity – HBM ⁽⁷⁾	-	3	kV
ESD Sensitivity – MM ⁽⁸⁾	-	400	V
ESD Sensitivity – CDM ⁽⁹⁾	-	1000	V
Magnetic Flux Density	В	Unlimited	mT

Table 2: Absolute maximum ratings

¹ The maximum junction temperature should not be exceeded

² For maximum 1 hour

³ Including current through protection device

⁴ For maximum 500ms

⁵ Through protection device

⁶ For 1000 hours.

⁷ Human Model according AEC-Q100-002 standard

⁸ Machine Model according AEC-Q100-003 standard

⁹ Charged Device Model according AEC-Q100-011 standard

2-Wire Hall Effect Switch Datasheet



8. General Electrical Specifications

DC Operating Parameters $V_{DD} = 2.7V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ ⁽¹⁾	Max	Units
OFF Supply Current (selectable by a dedicated bit)	I _{OFF}	V _{DD} = 3.5 to 24V	2 5	-	5 6.9	mA mA
ON Supply Current	I _{ON}	V _{DD} = 3.5 to 24V	12	-	17	mA
Reverse Supply current	I _{DDREV}	$V_{DD} = -16V$	-1	-	-	mA
Safe Mode Supply Current	I _{SAFE}		-	-	0.8	mA
Supply Current Rise/Fall Time ⁽²⁾	t_R/t_F	V_{DD} = 12V, C_{LOAD} =50pF to GND	0.1	0.3	1	μs
Power-On Time ^(3, 4)	t _{on}	V_{DD} = 5V, $dV_{DD}/dt > 2V/us$, activated output with >1mT overdrive	-	40	70	μs
Chopping Frequency	f_{CHOP}		-	350	-	kHz
Delay Time ^(2, 5)	t_{D}	Average over 1000 successive switching events @10kHz, Latch, B _{OP} set to 5mT, triangle wave magnetic field with B>±20mT	-	7.5	-	μs
Output Jitter (p-p) ^(2, 6)	t _{JITTER}	Over 1000 successive switching events @10kHz, Latch, B _{OP} set to 5mT, triangle wave magnetic field with B>±20mT	-	±3.5	-	μs
Maximum Switching Frequency (2, 7)	f_{SW}	Latch, B _{OP} set to 5mT, triangle wave magnetic field with B>±20mT	-	50	-	kHz
Under-voltage Lockout Threshold	V_{UVL}		-	-	2.7	V
Under-voltage Lockout Reaction time (2)	t_{UVL}		-	1	-	μs
Thermal Protection Activation	T_{PROT}	Junction Temperature	-	190 ⁽⁸⁾	-	°C
Thermal Protection Release	T_REL	Junction Temperature	-	180 ⁽⁸⁾	-	°C
UA Package Thermal Resistance	R_{THJA}	Single layer PCB, JEDEC standard test boards	-	200	-	°C/W
SE Package Thermal Resistance	R_{THJA}	Single layer PCB, JEDEC standard test boards	-	300	-	°C/W

Table 3: General Electrical parameters

¹ Typical values are defined at $T_A = 25\,^{\circ}\text{C}$ and $V_{DD} = 12\,\text{V}$

² Guaranteed by design and verified by characterization, not production tested

³ The Power-On Time represents the time from reaching V_{DD}=2.7V to the first refresh of the supply current state

⁴ Power-On Slew Rate is not critical for the proper device start-up

⁵ The Delay Time is the time from magnetic threshold reached to the start of the output switching

⁶ Output jitter is the unpredictable deviation of the Delay time.

⁷ Maximum switching frequency corresponds to the maximum frequency of the applied magnetic field which is detected without loss of pulses

⁸ T_{PROT} and T_{REL} are the corresponding junction temperature values

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9. Magnetic Specifications

9.1. MLX92241LUA-AAA-003

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Test Condition	Operating Point B _{OP} (mT)			Release Point B _{RP} (mT)			TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	5.9	8.5	10.9	3.5	5.5	7.9			Z-axis sensitive	
$T_J = 25^{\circ}C$	6.1	7.9	9.5	3.9	5.3	7	-1100 ⁽²⁾	3.3	Direct Switch	UA (TO92-3)
$T_J = 150$ °C	3.8	6.8	9.6	2.7	5	8.2			South pole	

9.2. MLX92241LUA-AAA-004

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40^{\circ}$ C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)			TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	6	9.2	12	5	7.2	10.5				
$T_J = 25^{\circ}C$	6	9.2	12	5	7.2	10.5	0 ⁽²⁾	6	Z-axis sensitive Inverted Switch	UA (TO92-3)
$T_J = 100$ °C	6	9.2	12	5	7.2	10.5	U	О	South pole	UA (1092-3)
$T_J = 150$ °C	5.7	9.2	12.5	4.6	7.2	11				

9.3. MLX92241LUA-AAA-005

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Test Condition	Орс	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)			TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	6	9.2	12	5	7.2	10.5				
$T_J = 25^{\circ}C$	6	9.2	12	5	7.2	10.5	0 ⁽²⁾	3.3	Z-axis sensitive Inverted Switch	UA (TO92-3)
$T_J = 100$ °C	6	9.2	12	5	7.2	10.5	U	5.5	South pole	UA (1092-3)
$T_J = 150$ °C	5.7	9.2	12.5	4.6	7.2	11				

$$\frac{B_{OPT2} - B_{OPT1}}{B_{OP25^{\circ}C} \times (T_2 - T_1)} * 10^6, ppm/^{\circ} C; T_1 = 25^{\circ}C; T_2 = 150^{\circ}C$$

¹ Typical values are defined at $T_A \!\!=\!\! +25^{\circ} C$ and $V_{DD} \!\!=\!\! 12V$

² Temperature coefficient is calculated using the following formula:

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9.4. MLX92241LUA-AAA-006

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Test Condition	Орс	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)			TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	5.9	8.5	10.9	3.5	5.5	7.9			Z-axis sensitive	
$T_J = 25^{\circ}C$	6.1	7.9	9.5	3.9	5.3	7	-1100 ⁽²⁾	6	Direct Switch	UA (TO92-3)
$T_J = 150^{\circ}C$	3.8	6.8	9.6	2.7	5	8.2			South pole	

9.5. MLX92241LUA-AAA-007

DC Operating Parameters V_{DD} = 3.5V to 24V, T_J = -40°C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)			TC (ppm/°C)	l _{off} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	3.6	6	8.4	1.5	3.6	6.1			Z-axis sensitive	
$T_J = 25^{\circ}C$	3.9	5.6	7.1	2	3.6	5.3	-1100 ⁽²⁾	3.3	Direct Switch	UA (TO92-3)
$T_J = 150$ °C	2	4.8	7.1	1.3	3.5	6.7			South pole	

9.6. MLX92241LSE-AAA-008⁽³⁾

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_{J} = -40$ °C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)	oint	Release Point B _{RP} (mT)			TC (ppm/°C)	I _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	12	17	22.8	11	15	21.4			Z-axis sensitive	
$T_J = 25^{\circ}C$	14.5	17	19.5	12.6	15	17.8	0 ⁽²⁾	6	Inverted Switch	SE (TSOT-3L)
$T_J = 150$ °C	12	17	22.8	11	15	21.4			South pole	

9.7. MLX92241LSE-AAA-011

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Test Condition		erating Po B _{OP} (mT)	oint	Release Point B _{RP} (mT)			TC (ppm/°C)	I _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	3.6	6.1	8.6	1.2	3.6	6.2			Z-axis sensitive	
$T_J = 25^{\circ}C$	3.6	5.6	7.8	1.0	3.4	5.7	-1100 ⁽²⁾	3.3	Direct Switch	SE (TSOT-3L)
$T_J = 150$ °C	2.5	4.7	7.2	0.7	3.1	5.6			South pole	

2 Temperature coefficient is calculated using the following formula:

$$\frac{B_{OPT2} - B_{OPT1}}{B_{OP25^{\circ}C} \times (T_2 - T_1)} *10^6, ppm/^{\circ} C; T_1 = 25^{\circ} C; T_2 = 150^{\circ} C$$

3 Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations

¹ Typical values are defined at T_A =+25°C and V_{DD} =12V

2-Wire Hall Effect Switch Datasheet



9.8. MLX92241LSE-AAA-012

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)	oint	Release Point B _{RP} (mT)		TC (ppm/°C)	I _{OFF} (mA)	Output Behavior Active Pole	Package Information	
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	3.6	6	8.4	1.5	3.6	6.1			Z-axis sensitive	
$T_J = 25^{\circ}C$	3.9	5.5	7.1	2	3.5	5.3	-1100 ⁽²⁾	6	6 Direct Switch	SE (TSOT-3L)
$T_J = 150^{\circ}C$	2	4.8	7.1	1.3	3.5	6.7			South pole	

9.9. MLX92241LSE-AAA-013

DC Operating Parameters V_{DD} = 3.5V to 24V, T_J = -40°C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)		int	TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	6	9.2	12	5	7.2	10.5			Z-axis sensitive Inverted Switch	
$T_J = 25^{\circ}C$	6	9.2	12	5	7.2	10.5	0 ⁽²⁾	6		SE (TSOT-3L)
$T_J = 150^{\circ}C$	5.7	9.2	12.5	4.6	7.2	11			South pole	

9.10. MLX92241LSE-AAA-014

DC Operating Parameters V_{DD} = 3.5V to 24V, T_I = -40°C to 165°C (unless otherwise specified)

- o o p c . a	operating farameters top cost to 111, 1, 10 to 100 to 100 to 1100 operation,											
Test Condition	Ор	erating Posting Postin		Release Point B _{RP} (mT)			TC (ppm/°C)	I _{OFF} (mA)	Output Behavior Active Pole	Package Information		
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾					
$T_J = -40$ °C	6	9.2	12	5	7.2	10.5			Z-axis sensitive Inverted Switch			
$T_J = 25^{\circ}C$	6	9.2	12	5	7.2	10.5	0 ⁽²⁾	3.3		SE (TSOT-3L)		
$T_J = 150^{\circ}C$	5.7	9.2	12.5	4.6	7.2	11			South pole			

$$\frac{B_{OPT2} - B_{OPT1}}{B_{OP25^{\circ}C} \times (T_2 - T_1)} * 10^6, ppm/^{\circ} C; T_1 = 25^{\circ}C; T_2 = 150^{\circ}C$$

¹ Typical values are defined at $T_A \!\!=\!\! +25\,^{\circ}\text{C}$ and $V_{\text{DD}} \!\!=\!\! 12\text{V}$

² Temperature coefficient is calculated using the following formula:

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9.11. MLX92241LSE-AAA-015⁽¹⁾

DC Operating Parameters $V_{DD} = 3.5V$ to 24V, $T_J = -40$ °C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)		TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information	
	Min	Typ ⁽²⁾	Max	Min	Typ ⁽²⁾	Max	Typ ⁽²⁾			
$T_J = -40$ °C	7.6	10.4	13.5	5.5	8.4	11.6			Z-axis sensitive	
$T_J = 25^{\circ}C$	6	9.2	12	5	7.2	10.5	-2000 ⁽³⁾	6	Direct Switch	SE (TSOT-3L)
$T_J = 150$ °C	4.5	6.9	9.5	3	5.6	8.3			South pole	

9.12. MLX92241LSE-AAA-016

DC Operating Parameters V_{DD} = 3.5V to 24V, T_J = -40°C to 165°C (unless otherwise specified)

Test Condition	Ор	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)		TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information	
	Min	Typ ⁽²⁾	Max	Min	Typ ⁽²⁾	Max	Typ ⁽²⁾			
$T_J = -40$ °C	3.6	6.1	8.6	1.1	3.5	6.2			Z-axis sensitive	
$T_J = 25^{\circ}C$	3.6	5.6	7.8	0.9	3.3	5.6	-1100 ⁽³⁾	6 Inverted Switch	Inverted Switch	SE (TSOT-3L)
$T_J = 150$ °C	2.5	4.7	7.2	0.7	3.1	5.6			South pole	

9.13. MLX92241LSE-AAA-017⁽¹⁾

DC Operating Parameters V_{DD} = 3.5V to 24V, T_J = -40°C to 165°C (unless otherwise specified)

Test Condition	Оро	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)		TC (ppm/°C)	l _{OFF} (mA)	Output Behavior Active Pole	Package Information	
	Min	Typ ⁽²⁾	Max	Min	Typ ⁽²⁾	Max	Typ ⁽²⁾			
$T_J = -40$ °C	7.6	10.4	13.5	5.5	8.4	11.6			Z-axis sensitive	
$T_J = 25^{\circ}C$	6	9.2	12	5	7.2	10.5	-2000 ⁽³⁾	3.3	Direct Switch	SE (TSOT-3L)
$T_J = 150^{\circ}C$	4.5	6.9	9.5	3	5.6	8.3			South pole	

$$\frac{B_{OPT2} - B_{OPT1}}{B_{OP25^{\circ}C} \times (T_2 - T_1)} * 10^6, ppm/^{\circ} C; T_1 = 25^{\circ}C; T_2 = 150^{\circ}C$$

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¹ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations

² Typical values are defined at T_A=+25°C and V_{DD}=12V

³ Temperature coefficient is calculated using the following formula:

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9.14. MLX92241LUA-AAA-028

DC Operating Parameters V_{DD} = 3.5V to 24V, T_I = -40°C to 165°C (unless otherwise specified)

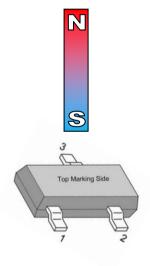
Test Condition	Ор	erating Po B _{OP} (mT)		Release Point B _{RP} (mT)			TC (ppm/°C)	I _{OFF} (mA)	Output Behavior Active Pole	Package Information
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾			
$T_J = -40$ °C	16.8	21.3	26.2	14.7	19.2	23.7			Z-axis sensitive	
$T_J = 25^{\circ}C$	17	20	23.2	15.2	18.0	21.0	-1100 ⁽²⁾	6	Direct Switch	UA (TO92-3)
$T_J = 150$ °C	13.3	17.1	21.4	12.0	15.6	20.2			South pole	



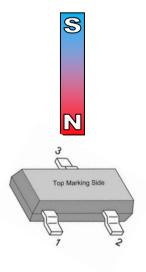
Z-axis Sensitive South Active Pole



Z-axis Sensitive North Active Pole



*Z-axis Sensitive*South Active Pole



Z-axis Sensitive North Active Pole

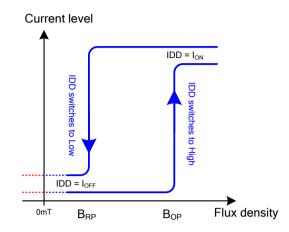
$$\frac{B_{OPT2} - B_{OPT1}}{B_{OP2S'C} \times (T_2 - T_1)} * 10^6, ppm/^{\circ} C; T_1 = 25^{\circ} C; T_2 = 150^{\circ} C$$

¹ Typical values are defined at T_A =+25°C and V_{DD} =12V

² Temperature coefficient is calculated using the following formula:



10. Magnetic Behavior



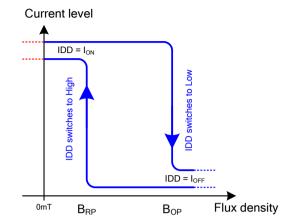


Fig.1 – Direct South Active Pole

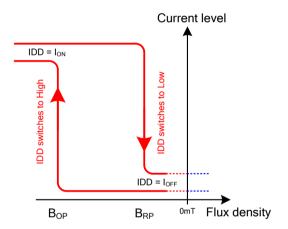


Fig.2 – Inverted South Active Pole

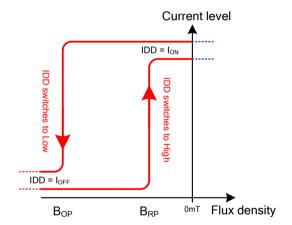


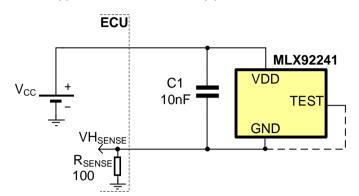
Fig.3 – Direct North Active Pole

Fig.4 – Inverted North Active Pole



11. Application Information

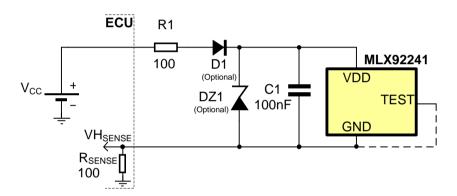
11.1. Typical Automotive Application Circuit



Notes:

- 1. For proper operation, a 10 to 100nF bypass capacitor should be placed as close as possible to the V_{DD} and ground (GND) pin.
- 2. The TEST pin is to be connected to GND or left open.

11.2. Automotive and Harsh, Noisy Environments Application Circuit



Notes:

- 1. For proper operation, a 10 to 100nF bypass capacitor should be placed as close as possible to the $\rm V_{\rm DD}$ and ground (GND) pin.
- 2. The device can tolerate positive voltages up to +28 (+32)V and negative voltages down to -24 (-30)V.

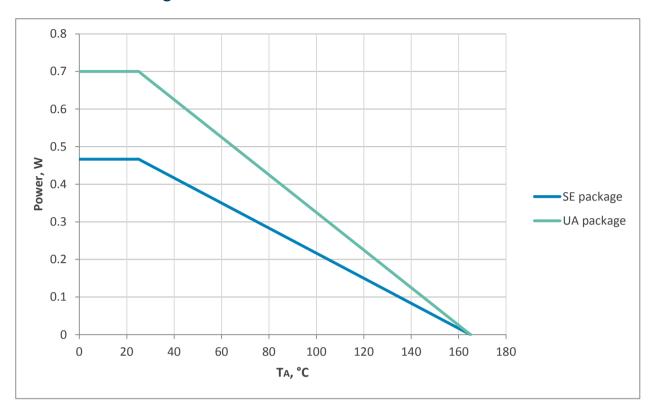
If bigger transients over the supply line are expected the usage of D1 and D21 (24...27V) is recommended.

The series resistor R1 is used to limit the current through DZ1 and to improve the EMC performance.

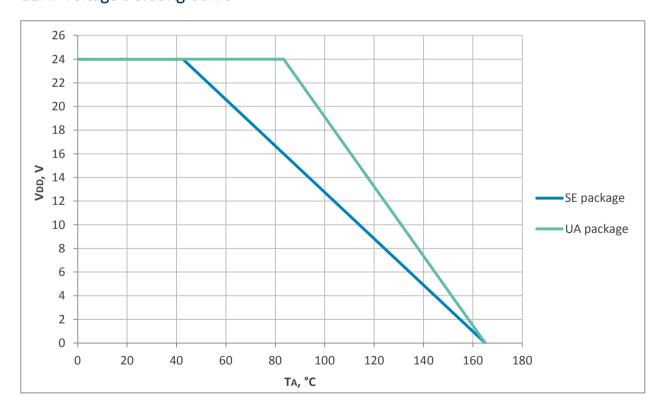
2-Wire Hall Effect Switch Datasheet



11.3. Power Derating Curve



11.4. Voltage Derating Curve



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12. Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD's (Surface Mount Devices)

IPC/JEDEC J-STD-020

Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)

EIA/JEDEC JESD22-A113

Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

EN60749-20

Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat

EIA/JEDEC JESD22-B106 and EN60749-15

Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD's (Through Hole Devices)

EN60749-15

Resistance to soldering temperature for through-hole mounted devices

Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

EIA/JEDEC JESD22-B102 and EN60749-21

Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis recommends reviewing on our web site the General Guidelines <u>soldering recommendation</u> (http://www.melexis.com/Quality_soldering.aspx) as well as http://www.melexis.com/Assets/Trim-and-form-recommendations-5565.aspx).

Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: http://www.melexis.com/quality.aspx

13. ESD Precautions

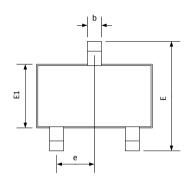
Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).

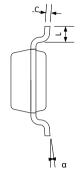
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.



14. Package Information

14.1. SE (TSOT-3L) Package Information





e1 D

- 1. All dimensions are in millimeters
- Outermost plastic extreme width does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.15mm per side.
- Outermost plastic extreme length does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.25mm per side.
- The lead width dimension does not include dambar protrusion.
 Allowable dambar protrusion shall be 0.07mm total in excess of the lead width dimension at maximum material condition.
- 5. Dimension is the length of terminal for soldering to a substrate
- 6. Formed lead shall be planar with respect to one another with 0.076mm at seating plane.

Marking:

TOP:

WWYY

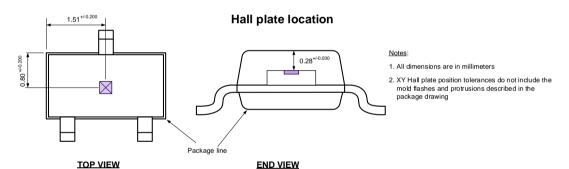
WW: Assembly week

YY: Assembly year

BOTTOM:

LLLL

LLLL: Last 4 digits from lot#



This table in mm

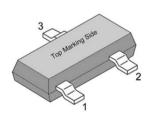
	Α	A1	A2	D	E	E1	L	b	С	е	e1	α
min	_	0.025	0.85	2.80	2.60	1.50	0.30	0.30	0.10	0.95	1.90	0°
max	1.00	0.10	0.90	3.00	3.00	1.70	0.50	0.45	0.20	BSC	BSC	8°

Notes:

- Dimension "D" and "E1" do not include mold flash or protrusions. Mold flash or protrusion shall not exceed 0.15mm on "D" and 0.25mm on "E" per side.
- Dimension "b" does not include dambar protrusion.

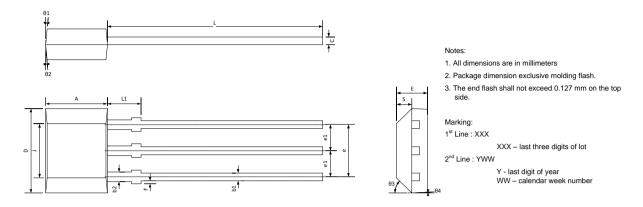
SE Pin №	Name	Туре	Function
1	VDD	Supply	Supply Voltage pin
2	TEST	1/0	For Melexis use only
3	GND	Ground	Ground pin



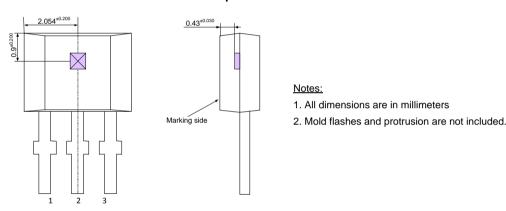




14.2. UA (TO92 - 3L) Package Information



Hall plate location



This table in mm

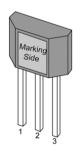
	A	D	E	F	J	L	L1	S	b1	b2	С	е	e1
min	2.80	3.90	1.40	0.00	2.51	14.0	0.90	0.63	0.35	0.43	0.35	2.51	1.24
max	3.20	4.30	1.60	0.20	2.72	15.0	1.10	0.84	0.44	0.52	0.44	2.57	1.30
	θ1	θ2	θ3	θ4									
min	7° REF	7°	45°	7°									
max	/ KEF	REF	REF	REF									

Notes:

- 1. Mold flashes and protrusion are not included.
- 2. Gate burrs shall not exceed 0.127mm on the top side.

Pin Nº	Name	Туре	Function
1	VDD	Supply	Supply Voltage pin
2	GND	Ground	Ground pin
3	TEST	1/0	For Melexis use only

Table 5: UA Package pinout



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15. Contact

For the latest version of this document, go to our website at www.melexis.com.

For additional information, please contact our Direct Sales team and get help for your specific needs:

Europe, Africa	Telephone: +32 13 67 04 95
	Email: sales_europe@melexis.com
Americas	Telephone: +1 603 223 2362
	Email : sales_usa@melexis.com
Asia	Email: sales_asia@melexis.com

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