

# ES5A THUR ES5J

## ES5A THUR ES5J Super-Fast Surface Mount Rectifiers

### General description

Super-Fast Surface Mount Rectifiers  
 Reverse Voltage: 50 to 600V  
 Forward Current: 5.0A  
 SMC/DO214AB package

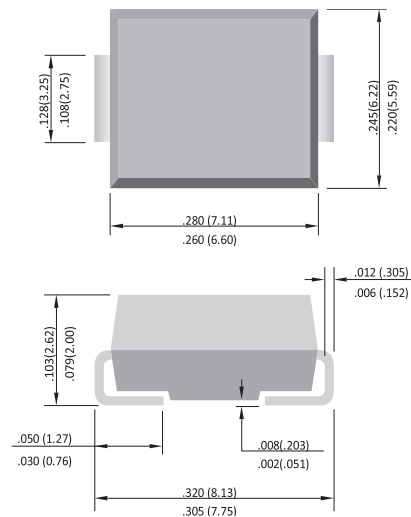
### FEATURES

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Superfast reverse recovery time
- Lead free in comply with EU RoHS 2011/65/EU directives

### MECHANICAL DATA

- Case : SMC
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.22g / 0.0077oz

### SMC/DO-214AB



Unit: inch (mm)

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## Maximum Ratings & Thermal Characteristics

(Ratings at 25°C ambient temperature unless otherwise specified.)

Parameter	Symbols	ES5A	ES5B	ES5C	ES5D	ES5E	ES5G	ES5J	Units
Marking Code	Mark	ES5A	ES5B	ES5C	ES5D	ES5E	ES5G	ES5J	N/A
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	50	100	150	200	300	400	600	V
Maximum RMS voltage	$V_{RMS}$	35	70	105	140	210	280	420	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	150	200	300	400	600	V
Maximum Average Forward Rectified Current at $T_c = 100^\circ\text{C}$	$I_{F(AV)}$	5							A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	$I_{FSM}$	120							A
Maximum Forward Voltage at 5 A	$V_F$	1				1.25		1.68	V
Maximum DC Reverse Current $T_a = 25^\circ\text{C}$ at Rated DC Blocking Voltage $T_a = 125^\circ\text{C}$	$I_R$	5 100							$\mu\text{A}$
Typical Junction Capacitance at $V_R=4\text{V}$ , $f=1\text{MHz}$	$C_j$	50							pF
Maximum Reverse Recovery Time <sup>(1)</sup>	$t_{rr}$	35							ns
Typical Thermal Resistance <sup>(2)</sup>	$R_{\theta JA}$ $R_{\theta JC}$	35 13							$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_j, T_{stg}$	-55 ~ +150							$^\circ\text{C}$

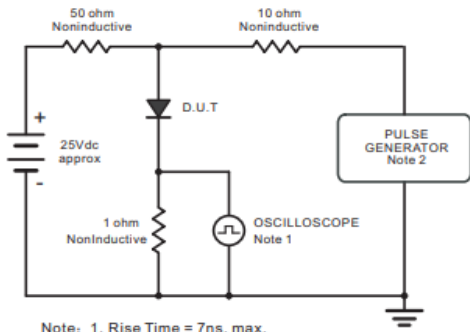
Note:1.Reverse recovery condition  $I_F=0.5\text{A}$ ,  $I_R=1.0\text{A}$ ,  $I_{rr}=0.25\text{A}$

2.Measured at 1MHz and applied reverse voltage of 4.0V D.C.

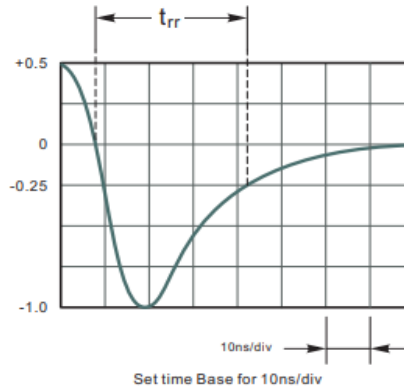
## RATING AND CHARACTERISTIC CURVES

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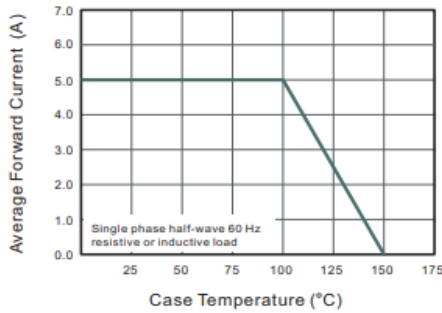
**Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram**



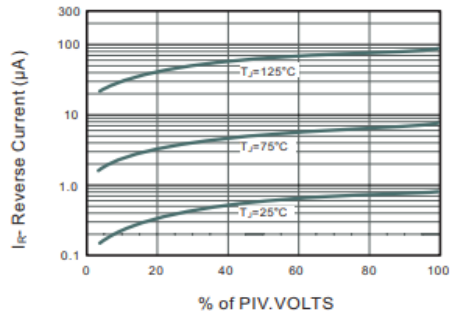
Note: 1. Rise Time = 7ns, max.  
Input Impedance = 1 megohm, 22pF.  
2. Rises Time = 10ns, max.  
Source Impedance = 50 ohms.



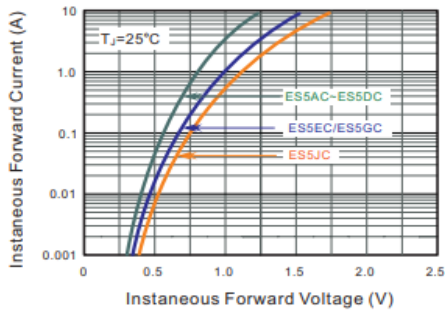
**Fig.2 Maximum Average Forward Current Rating**



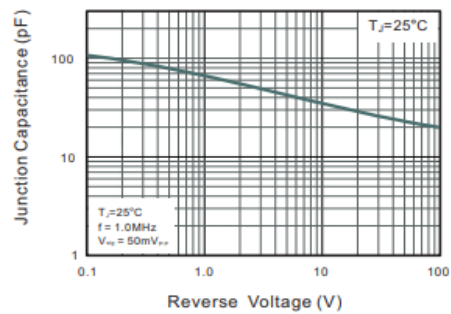
**Fig.3 Typical Reverse Characteristics**



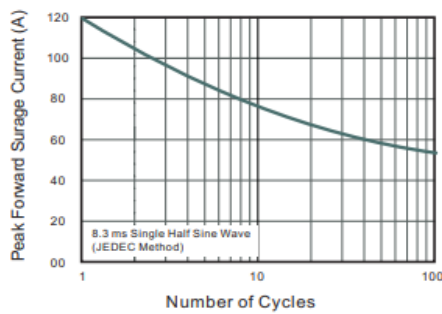
**Fig.4 Typical Forward Characteristics**



**Fig.5 Typical Junction Capacitance**



**Fig.6 Maximum Non-Repetitive Peak Forward Surge Current**



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