Operational Amplifiers, Dual Power, 1.0 A Output Current

TCA0372, TCA0372B, NCV0372B

The TCA0372 is a monolithic circuit intended for use as a power operational amplifier in a wide range of applications, including servo amplifiers and power supplies. No deadband crossover distortion provides better performance for driving coils.

Features

- Output Current to 1.0 A
- Slew Rate of 1.3 V/µs
- Wide Bandwidth of 1.1 MHz
- Internal Thermal Shutdown
- Single or Split Supply Operation
- Excellent Gain and Phase Margins
- Common Mode Input Includes Ground
- Zero Deadband Crossover Distortion
- NCV devices are AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

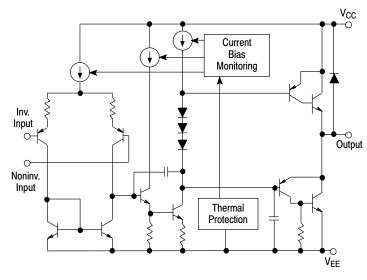


Figure 1. Representative Block Diagram

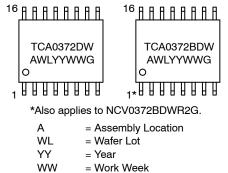


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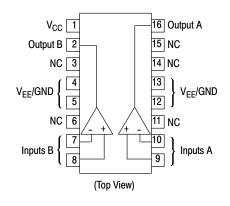
MARKING DIAGRAMS



= Pb-Free Package

G

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage (from V_{CC} to V_{EE})	V _S	40	V
Input Differential Voltage Range	V _{IDR}	Note 1	V
Input Voltage Range	V _{IR}	Note 1	V
Junction Temperature (Note 2)	Т _Ј	+150	°C
Operating Temperature Range	T _A	-40 to +125	°C
Storage Temperature Range	T _{stg}	–55 to +150	°C
DC Output Current	۱ _۵	1.0	А
Peak Output Current (Nonrepetitive)	I _(max)	1.5	A
Thermal Resistance, Junction-to-Air	R _{0JA}	80	°C/W
Thermal Resistance, Junction-to-Case	$R_{ ext{ heta}JC}$	12	°C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Either or both input voltages should not exceed the magnitude of V_{CC} or V_{EE} . 2. Power dissipation must be considered to ensure maximum junction temperature (T_J) is not exceeded.

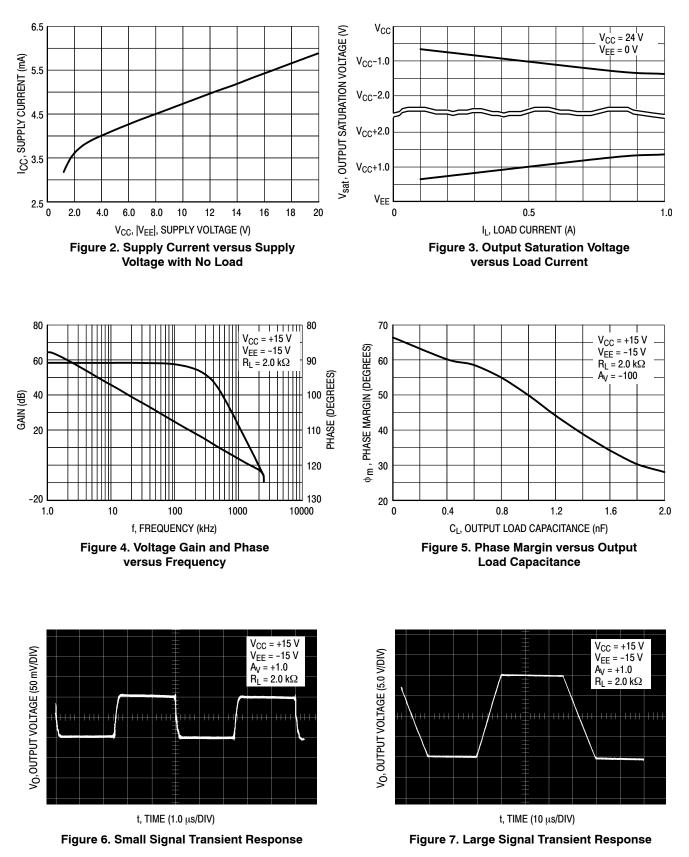
	Characteristics	Symbol	Min	Тур	Max	Unit
Input Offset Voltage (V _{CM} = $T_A = +25^{\circ}C$ T_A , T_{low} to T_{high}	0)	V _{IO}		1.0 _	15 20	mV
Average Temperature Coeffi	cient of Offset Voltage	$\Delta V_{IO} / \Delta T$	-	20	-	μV/°C
Input Bias Current (V _{CM} = 0)	I _{IB}	-	100	500	nA
Input Offset Current (V _{CM} =	0)	I _{IO}	-	10	50	nA
Large Signal Voltage Gain $V_{O} = \pm 10$ V, $R_{L} = 2.0$ k		A _{VOL}	30	100	-	V/mV
Output Voltage Swing ($I_L = 1$ $T_A = +25^{\circ}C$ $T_A = T_{low}$ to T_{high} $T_A = +25^{\circ}C$ $T_A = T_{low}$ to T_{high}	100 mA)	V _{OH} V _{OL}	14.0 13.9 - -	14.2 _ _14.2 _	- - -14.0 -13.9	V
$\begin{array}{l} \text{Output Voltage Swing (I_L = 1)} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{V}_{\text{EE}} = 0 \text{ V}, \text{T} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{V}_{\text{EE}} = 0 \text{ V}, \text{T} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{V}_{\text{EE}} = 0 \text{ V}, \text{T} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{V}_{\text{EE}} = 0 \text{ V}, \text{T} \end{array}$	$T_A = +25^{\circ}C$ $T_A = T_{low}$ to T_{high} $T_A = +25^{\circ}C$	V _{OH} V _{OL}	22.5 22.5 - -	22.7 - 1.3 -	- - 1.5 1.6	V
Input Common Mode Voltag $T_A = +25^{\circ}C$ $T_A = T_{low}$ to T_{high}	e Range	V _{ICR}		to (V _{CC} – to (V _{CC} –	,	V
Common Mode Rejection Ra	atio (R _S = 10 k)	CMRR	70	90	-	dB
Power Supply Rejection Rat	io (R _S = 100 Ω)	PSRR	70	90	-	dB
$T_A = T_{low}$ to T_{high}	A0372 A0372B/NCV0372B A0372 A0372B/NCV0372B	Ι _D	- - -	5.0 8.0 -	10 10 14 14	mA

DC ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = -15 V, R_L connected to ground, T_A = -40° to +125°C.)

AC ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = -15 V, R_L connected to ground, T_A = +25°C, unless otherwise noted.)

					,
Characteristics	Symbol	Min	Тур	Max	Unit
Slew Rate (V _{in} = –10 V to +10 V, R _L = 2.0 k, C _L = 100 pF) A_V = –1.0, T _A = T _{low} to T _{high}	SR	1.0	1.4	-	V/μs
Gain Bandwidth Product (f = 100 kHz, C _L = 100 pF, R _L = 2.0 k) T _A = 25°C T _A = T _{low} to T _{high}	GBW	0.9 0.7	1.4 -	-	MHz
Phase Margin $T_J = T_{low}$ to T_{high} $R_L = 2.0 \text{ k}, C_L = 100 \text{ pF}$	φ _m	-	65	-	Degrees
Gain Margin $R_L = 2.0 \text{ k}, C_L = 100 \text{ pF}$	A _m	-	15	-	dB
Equivalent Input Noise Voltage $R_S = 100 \ \Omega$, f = 1.0 to 100 kHz	e _n	-	22	-	nV/√Hz
Total Harmonic Distortion $A_V = -1.0$, $R_L = 50 \Omega$, $V_O = 0.5$ VRMS, f = 1.0 kHz	THD	-	0.02	-	%

NOTE: In case V_{EE} is disconnected before V_{CC}, a diode between V_{EE} and Ground is recommended to avoid damaging the device.



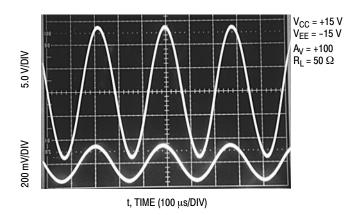


Figure 8. Sine Wave Response

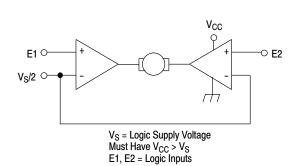
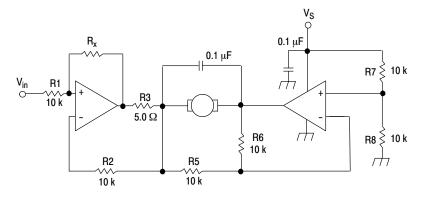


Figure 9. Bidirectional DC Motor Control with Microprocessor-Compatible Inputs



For circuit stability, ensure that $R_x > \frac{2R3 \cdot R1}{R_M}$ where, R_M = internal resistance of motor. The voltage available at the terminals of the motor is: $V_M = 2(V_1 - \frac{V_S}{2}) + |R_0| \cdot I_M$ where, $|R_0| = \frac{2R3 \cdot R1}{R_X}$ and I_M is the motor current.

Figure 10. Bidirectional Speed Control of DC Motors

ORDERING INFORMATION

Device	Package	Shipping [†]
TCA0372DWR2G	SOIC-16W (Pb-Free)	1000 / Tape & Reel
TCA0372BDWR2G	SOIC-16W (Pb-Free)	1000 / Tape & Reel
NCV0372BDWR2G*	SOIC-16W (Pb-Free)	1000 / Tape & Reel

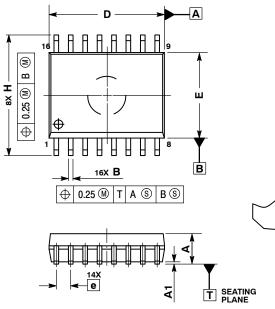
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*AEC-Q100 Qualified and PPAP Capable

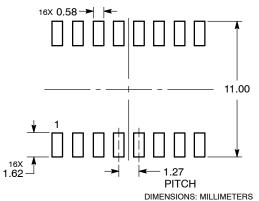


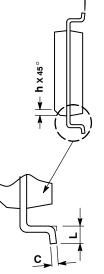






SOLDERING FOOTPRINT





SOIC-16 WB CASE 751G-03 **ISSUE D**

DATE 12 FEB 2013

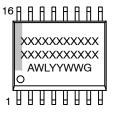
NOTES

A

- DIMENSIONS ARE IN MILLIMETERS.
 INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
- DIMENSIONS D AND E DO NOT INLCUDE MOLD PROTRUSION. З.
- MOLD PHOTHOSION. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 TOTAL IN 4 5. EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS		
DIM	MIN	MAX	
Α	2.35	2.65	
A1	0.10	0.25	
в	0.35	0.49	
С	0.23	0.32	
D	10.15	10.45	
Е	7.40	7.60	
е	1.27 BSC		
н	10.05	10.55	
h	0.25	0.75	
L	0.50	0.90	
a	0 °	7 °	

GENERIC **MARKING DIAGRAM***



XXXXX = Specific Device Code А

- = Assembly Location
- = Wafer Lot WL
- YY = Year

WW = Work Week

= Pb-Free Package G

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " .", may or may not be present.

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