NCE N-Channel Super Trench Power MOSFET

Description

The NCEP1545K uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS}(\text{ON})}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

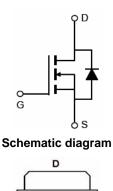
General Features

- V_{DS} =150V, I_D =45A $R_{DS(ON)}$ =24m Ω (typical) @ V_{GS} =10V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- LED backlighting
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED!





Marking and pin assignment



TO-252 -2Ltop view

Package Marking and Ordering Information

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	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity	
	NCEP1545K	NCEP1545K	TO-252-2L	Ø330mm	12mm	2500 units	

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Absolute maximum rutings (14-20 cumess cumer vise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	150	V	
Gate-Source Voltage	V _{GS}	±20	V	
Drain Current-Continuous	I _D	45	А	
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	31.8	А	
Pulsed Drain Current	I _{DM}	180	А	
Maximum Power Dissipation	P _D	130	W	
Derating factor		0.87	W/℃	
Single pulse avalanche energy (Note 5)	E _{AS}	200	mJ	
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 175	$^{\circ}$	

Thermal Characteristic

Thermal Résistance, Junction-to-Case ^(Note 2)	R _{θJC}	1.15	°C/W	
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NCEP1545K

Electrical Characteristics (T_A =25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =150V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	2.0	3.1	4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	24	27.5	mΩ
Gate resistance	R_{G}		-	6.2	-	Ω
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	15	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ 75\/\/ 0\/	-	1300		PF
Output Capacitance	Coss	V _{DS} =75V,V _{GS} =0V,	-	162		PF
Reverse Transfer Capacitance	C _{rss}	Γ-1.UIVIΠZ	F=1.0MHz			PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	t _r	V_{DD} =75 V , R L=7.5 Ω	-	6.5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	16	-	nS
Turn-Off Fall Time	t _f		-	7	-	nS
Total Gate Charge	Q_g	\/ -75\/ -204	-	21.1	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =75V,I _D =20A, V _{GS} =10V	-	8	-	nC
Gate-Drain Charge	Q_{gd}	VGS-10V	-	4.6	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	45	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = I_S$	-	30	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	135		nC

Notes:

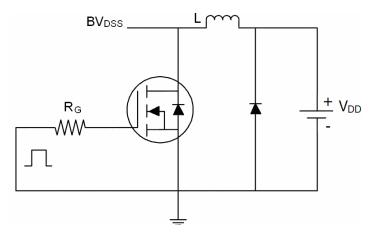
- ${\it 1. Repetitive Rating: Pulse width limited by maximum junction temperature.}\\$
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V $_{DD}$ =75V,V $_{G}$ =10V,L=0.5mH,Rg=25 Ω



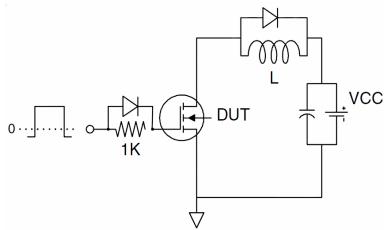
NCEP1545K

Test Circuit

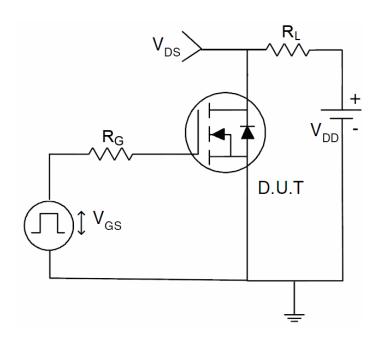
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





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Typical Electrical and Thermal Characteristics

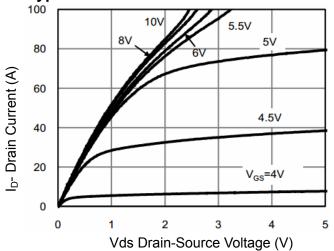


Figure 1 Output Characteristics

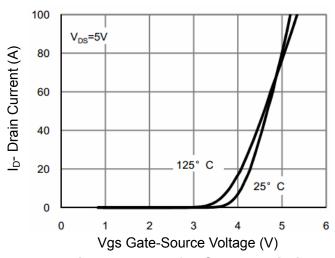


Figure 2 Transfer Characteristics

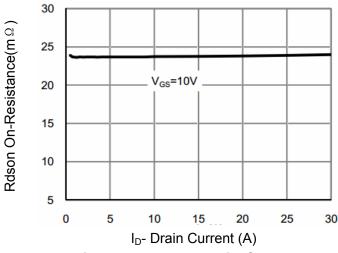


Figure 3 Rdson- Drain Current

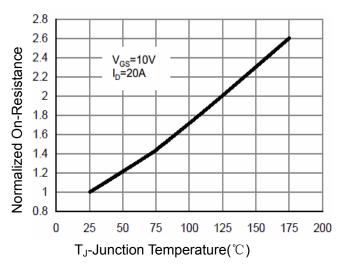


Figure 4 Rdson-Junction Temperature

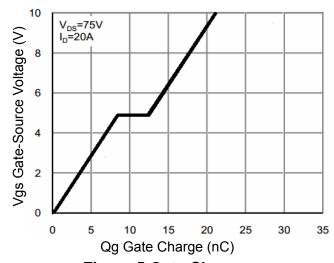


Figure 5 Gate Charge

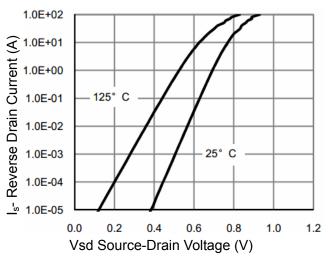


Figure 6 Source- Drain Diode Forward



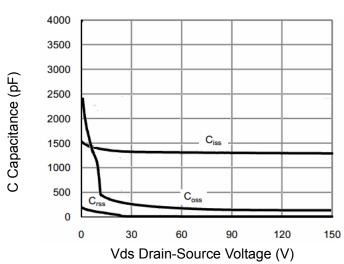


Figure 7 Capacitance vs Vds

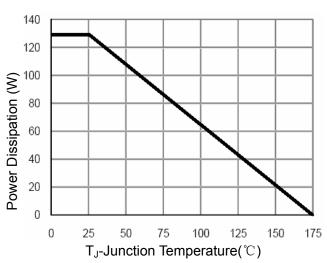
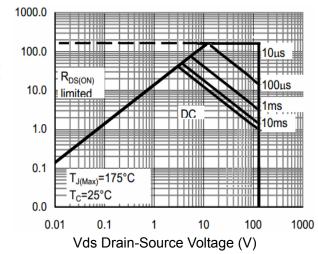


Figure 9 Power De-rating



lp- Drain Current (A)

Figure 8 Safe Operation Area

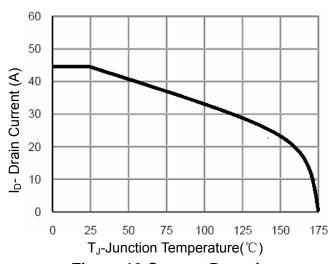


Figure 10 Current De-rating

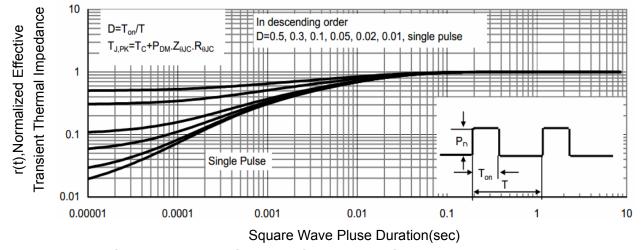
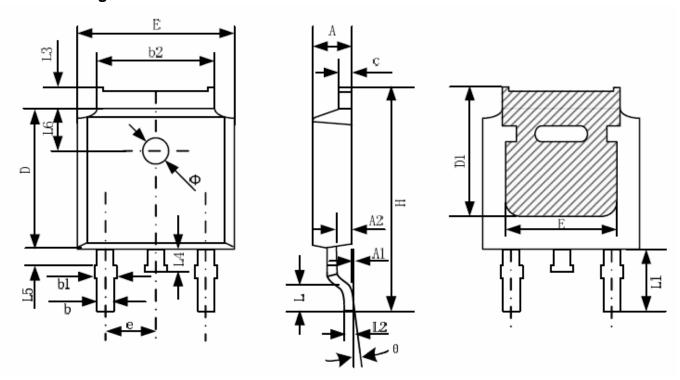


Figure 11 Normalized Maximum Transient Thermal Impedance



NCEP1545K

TO-252-2L Package Information



Cumahad	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.20	2.38	0.087	0.094	
A1	0.00	0.10	0.000	0.004	
A2	0.90	1.10	0.035	0.043	
b	0.72	0.85	0.028	0.033	
b1	0.72	0.90	0.028	0.035	
b2	5.13	5.46	0.202	0.215	
С	0.47	0.60	0.019	0.024	
D	6.00	6.20	0.236	0.244	
D1	5.25		0.207	_	
E	6.50	6.70	0.256	0.264	
E1	4.70		0.185	-	
e	2.19	2.39	0.086	0.094	
Н	9.80	10.40	0.386	0.409	
L	1.40	1.70	0.055	0.067	
L1	2.90	REF	0.114	4 REF	
L2	0.50	8 BSC	0.02	0 BSC	
L3	0.90	1.25	0.035	0.049	
L4	0.60	1.00	0.024	0.039	
L5	0.15	0.75	0.006	0.030	
L6	1.80	REF	0.071 REF		
Φ	1.20	1.40	0.047	0.055	
θ	0°	8°	0°	8°	



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