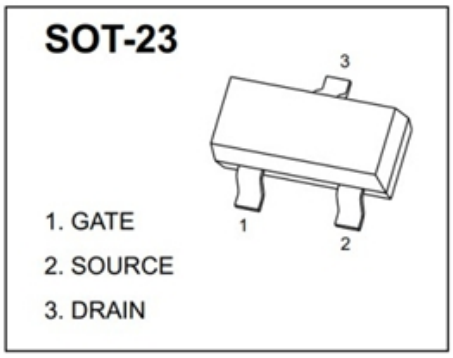
DP3401

DP3401
P-Channel Enhancement Mode Field Effect Transistor

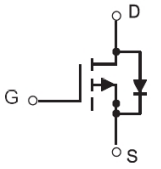
General description
P-Channel Enhancement Mode Field Effect Transistor

FEATURES
- V_{DS} (V) = -30V,I_D = -5.1A
- High dense cell design for extremely low RDS(ON).
- Load/Power Switching
- Exceptional on-resistance and maximum DC current capability
- Interfacing Switching



$V_{(BR)DSS}$	$R_{DS(on)}$MAX	I_D
-30 V	65mΩ@-10V	-4A
	75mΩ@-4.5V	
	90mΩ@-2.5V	

Equivalent Circuit



MARKING :

Device Type	Device Marking
DP3401	P05 or A19T

Maximum ratings (Ta=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	±12	V
Continuous Drain Current	I_D	-4	A
Power Dissipation	P_D	350	mW
Thermal Resistance from Junction to Ambient (t<5s)	$R_{\theta JA}$	357	℃/W
Junction Temperature	T_J	150	℃
Storage Temperature	T_{STG}	-55~+150	℃

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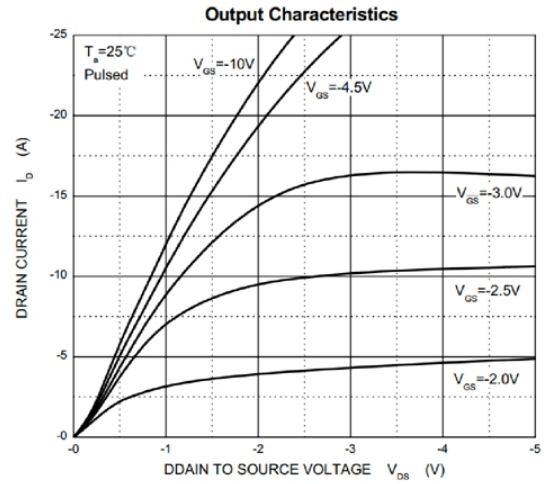
■ Electrical Characteristics Ta = 25℃

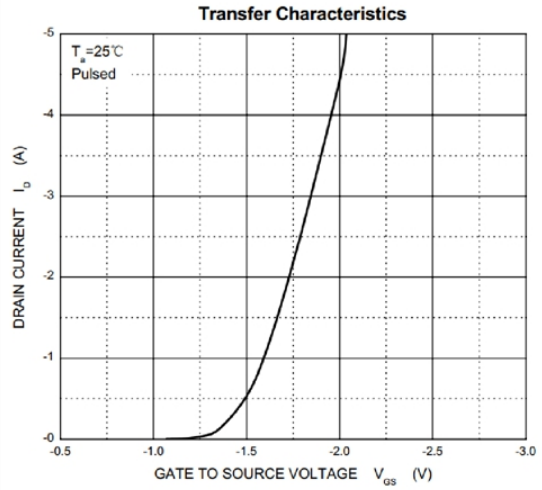
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Off characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =-250μA	-30			V
Zero gate voltage drain current	I_{DSS}	V_{DS} =-24V,V_{GS} = 0V			-1	μA
Gate-source leakage current	I_{GSS}	V_{GS} =±12V, V_{DS} = 0V			±100	nA
On characteristics						
Drain-source on-resistance (note 1)	$R_{DS(on)}$	V_{GS} =-10V, I_D =-4.2A		50	65	mΩ
		V_{GS} =-4.5V, I_D =-4A		60	75	mΩ
		V_{GS} =-2.5V,I_D=-1A		75	100	mΩ
Forward tranconductance (note 1)	g_{FS}	V_{DS} =-5V, I_D =-5A	7			S
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =-250μA	-0.7	-0.9	-1.3	V
Dynamic characteristics (note 2)						
Input capacitance	C_{iss}	V_{DS} =-15V,V_{GS} =0V,f =1MHz		954		pF
Output capacitance	C_{oss}			115		pF
Reverse transfer capacitance	C_{rss}			77		pF
Switching characteristics (note 2)						
Turn-on delay time	$t_{d(on)}$	V_{GS}=-10V,V_{DS}=-15V, R_L=3.6Ω,R_{GEN}=6Ω			6.3	ns
Turn-on rise time	t_r				3.2	ns
Turn-off delay time	$t_{d(off)}$				38.2	ns
Turn-off fall Time	t_f				12	ns
Drain-source diode characteristics and maximum ratings						
Diode forward voltage (note 1)	V_{SD}	I_S=-1A,V_{GS}=0V			-1	V

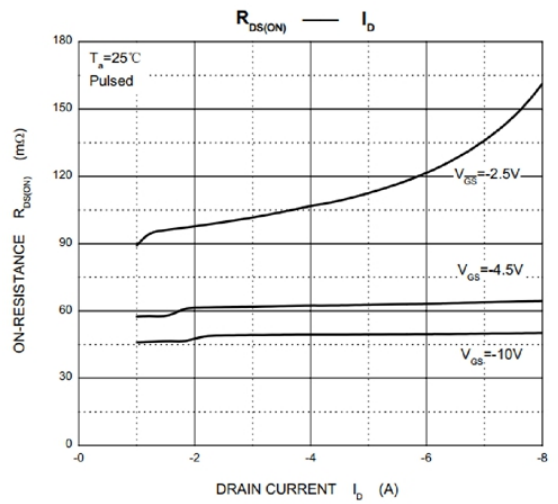
Note :

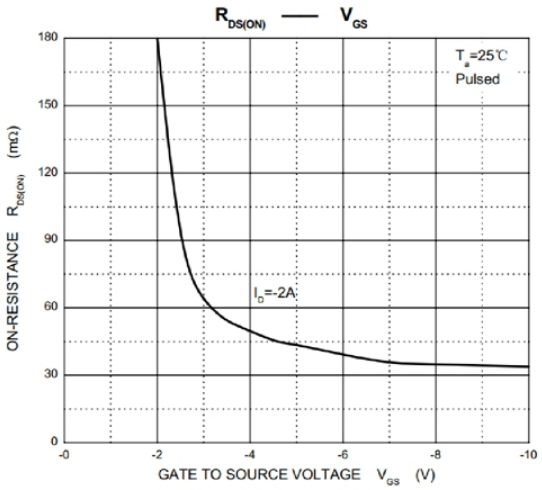
1. Pulse Test : Pulse width≤300μs, duty cycle≤2%.
2. These parameters have no way to verify.

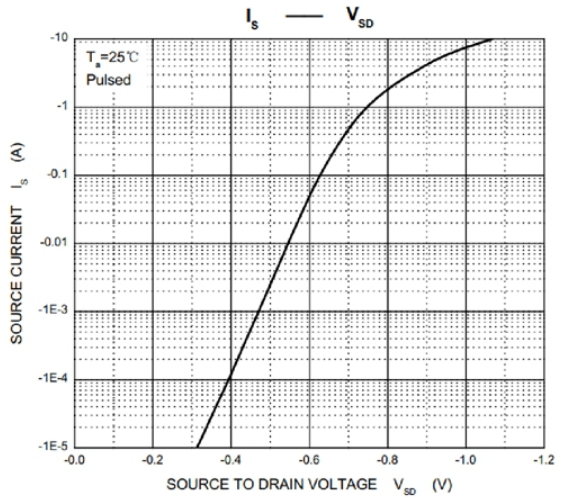
■ Typical Characteristics



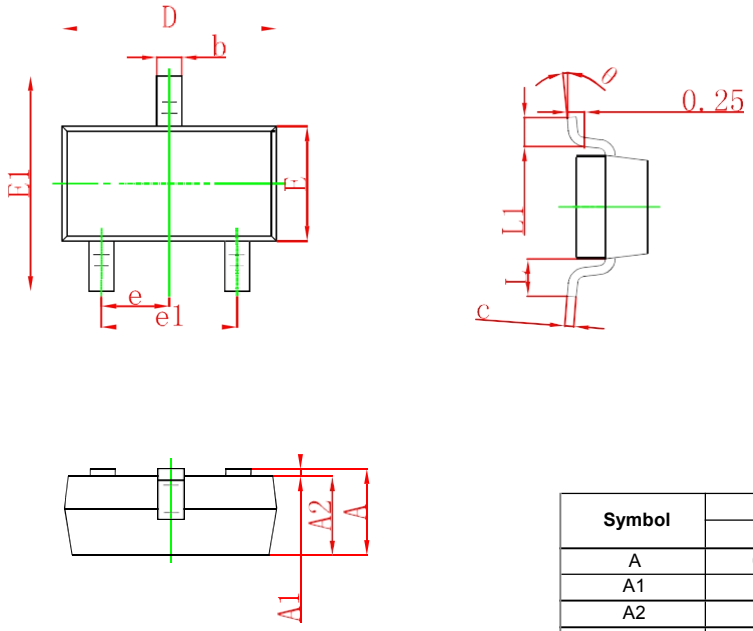






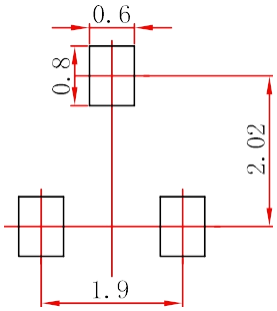


SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

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