

ES1AL THRU ES1JL

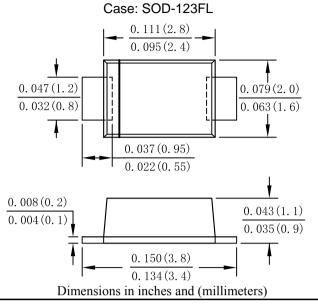
Single Phase 1.0AMP Surface Mount Super Fast Recovery Rectifier

Features

- · Glass passivated device
- · Ideal for surface mouted applications
- · Low reverse leakage
- · Metallurgically bonded construction
- High temperature soldering guaranteed: 260°C/10 seconds,0.375"(9.5mm) lead length, 5 lbs. (2.3kg) tension
- Plastic material-UL flammability 94V-0

Mechanical Data

- Case: SOD-123FL, molded plastic
- Terminals: plated leads solderable per MIL-STD-750, Method 2026
- · Polarity: Color band denotes cathode end
- · Mounting position: Any



Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	ES1AL	ES1BL	ES1DL	ES1GL	ES1JL	UNITS			
	Code	EA	EB	ED	EG	EJ				
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM	50	100	200	400	600	٧			
	VRWM									
	VDC									
RMS Reverse Voltage	VRMS	35	70	140	280	420	V			
Average Rectified Output Current	I F(AV)			1.0			Α			
Non-Repetitive Peak Forward Surge @T _{j=25} ℃		30 24					А			
Current 8.3ms Single half sine-wave@T _{j=125} °C	IFSM									
Superimposed On Rated Load (JEDEC Method)										
Non-Repetitive Peak Forward Surge @T _{j=25} ℃				60						
Current 1.0ms Single half sine-wave @Tj=125°C	I _{FSM} 48					Α				
Superimposed On Rated Load (JEDEC Method)										
10000 times of the wave surge current	IFSM 22.5					Α				
(time width 1ms, time interval 3s)	IFSIVI	22.0					A			
I ² t Rating for Fusing (t < 8.3ms)	l²t	3.735					A ² s			
Forward Voltage per element @IF=1.0A	VFM		0.95		1.3	1.7	V			
Peak Reverse Current @TA =25℃ At Rated DC Blocking Voltage @TA =125℃	lr			5.0						
		100					uA			
Maximum reverse recovery time @TA =25℃ (NOTE 1) @TA =125℃	Trr	35					ns			
		200								
Typical Junction Capacitance (Note 2)	C _J	10					pF			
Typical thermal resistance (Note 3)	Reja	60					°C/W			
Operating and Storage Temperature Range	TJ,Tstg	-55to+150					$^{\circ}\mathbb{C}$			

Note:1.Measured with IF=0.5A, IR=1A, Irr=0.25A.

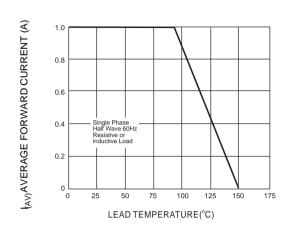
- 2 Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C.
- 3. Device mounted on FR-4 substrate, 25.4 25.4mm,2oz,single-sided,PC boards with 2.1 2.1mm copper pac

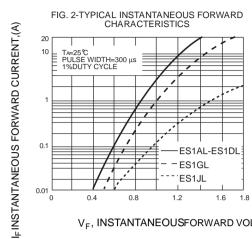
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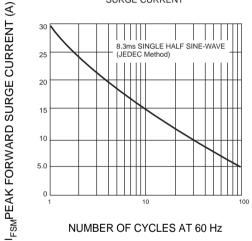
FIG. 1- FORWARD CURRENT DERATING CURVE



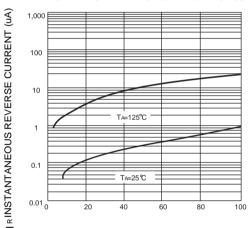


V_F, INSTANTANEOUSFORWARD VOLTAGE (V)



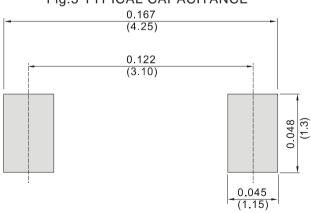






PERCENT OF RATED PEAK REVERSE VOLYAGE(%)

Fig.5 TYPICAL CAPACITANCE



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