

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): (日期):2017-09-30

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : KM 400V82 μ F(ϕ 16X25)

VERSION (版本) : 01

Customer P/N : 10218210501R

SUPPLIER :

SUPPLIER				
PREPARED (拟定)	CHECKED (审核)			
李婷	刘渭清			

CUSTOMER			
APPROVAL (批准)	SIGNATURE (签名)		

ELECTROLYTIC CAPACITOR SPECIFICATION KM SERIES

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Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

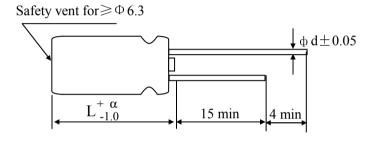
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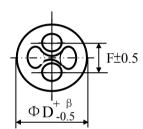
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Table 1 Product Dimensions and Characteristics

Unit: mm





α	L<20 : α=1.5; L≥20 : α=2.0
β	$\Phi D < 20 : \beta = 0.5; \Phi D \ge 20 : \beta = 1.0$

* If it is flat rubber, there is no bulge from the flat rubber surface

No.	SAMXON	wv	Cap.	Cap. tolerance	Temp.	tanδ (120Hz,	Leakage Current	Max Ripple Current at 105 °C		Load lifetim		ension mm)		Sleeve
	Part No.	(Vdc)	(μF)		$range(^{\circ}C)$	20℃)	(μA,2min)	120Hz (mA rms)	120Hz (Ωmax)	e (Hrs)	$D \times \Gamma$	F	фd	
1	EKM826M2GK25RR**P	400	82	-20%~+20%	-25~105	0.06	1024	382	3.5	2000	16X25	7.5	0.8	PET

moisture content of electrolyte $\leq 5\%$

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Attachment: Application Guidelines

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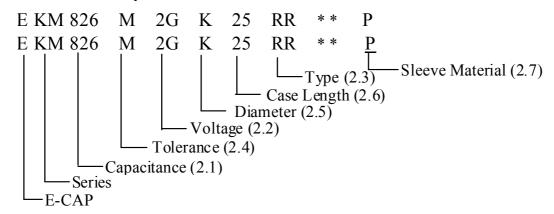
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1. Application

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment. Designed capacitor's quality meets IEC60384.

2. Part Number System



2.1 <u>Capacitance code</u>

÷	cupurium cour	
	Code	826
	Capacitance (µF)	82

2.2 Rated voltage code

Code	2G
Voltage (V.DC)	400

2.3 <u>Type</u>

Code	RR
Reference	BULK

2.4 Capacitance tolerance

"M" stands for $-20\overline{\%} \sim +20\%$

2.5 <u>Size</u>

Code	K
Diameter	16

2.6 <u>Length</u>

"25" stands for 25mm

2.7 <u>Sleeve material</u>

Code	P
Sleeve material	PET

Remark: The "*" in fifteenth and sixteenth digits is used for the product lines.

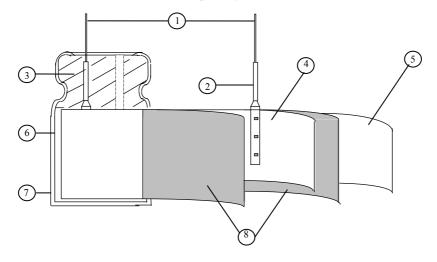
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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil 580VF
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature : $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PE	RFOR	MANC	Е			
	Rated voltage	WV (V.DC)	6.3	10	1	6	25	35	50	63	100
	(WV)	SV (V.DC)	8	13	2	0.0	32	44	63	79	125
4.1	Surge	WV (V.DC)	160	200	220	250	350	400	420	450	
	voltage (SV)	SV (V.DC)	200	250	270	300	400	450	470	500	
4.2	Nominal capacitance (Tolerance)	Condition> Measuring F Measuring V Measuring T Criteria> Shall be with	requend foltage Tempera	: uture :	Not m 20±2	$^{\circ}$ C	ın 0.5V				
4.3	Leakage current	<pre><condition> Connecting t minutes, and <criteria> Refer to 0.03</criteria></condition></pre>	the capa then, m	easure l				stor (1	kΩ±1	0Ω) in s	series for
4.4	tan δ	<pre><condition> See 4.2, Nor </condition></pre> <pre><criteria> Refer to Tabl</criteria></pre>	m Capa	citance,	for mo	easurin	g frequ	ency, vo	oltage ar	nd temper	ature.
4.5	Terminal strength	0.51	rength of capacitor apacitor 2~3 sector of le	or, applied of Termin, applied onds, and wire less	inals. d force	to ben n bent i Γensile (k 5 (t the ter t for 90 force N gf) 0.51)	minal (1	~4 mm original Bendin (1 2.5	from the position g force N kgf) (0.25)	rubber) within 2
	1	Over 0	5mm to	0.8mm	1	10	(1.0)		5 (0.51)	1

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		<condition></condition>								
		STEP Tes	ting Tempe	erature(°C)			Time			
		1	20±	2	Time	to read	ch thermal e	quilibriu	n	
		2	-40(-25)	<u>±3</u>	Time	to reac	ch thermal e	- quilibriu	n	
		3	20±		_		ch thermal ed	•		
		4	105±		_		ch thermal ed	•		
		5	20±		_		ch thermal e	-		
		<criteria></criteria>	20 1		1 IIIIC	to read	on uncimal co	quiiioiiui	.11	
		a. $\tan \delta$ shall be wi	thin the lin	it of Itom	4 4Tho 1	oolzo aa	ourrant mas	surad sh	all not	
		more than 8 times o			4.411101	cakage	current mea	isureu sii	an not	
	Temperature	b. In step 5, $\tan \delta$ s	_		nit of Ites	n 4 4T	he leakage (ourrent c	hall no	
	characteristi	more than the special		iiiii tiic iiii	iit or itei	11 7.71	ne reakage (Juii Ciit S	nan no	
4.6	cs	c. At-40°C (-25°C),		e (z) ratio s	shall not	exceed	I the value of	f the follo	owing	
		table.	mpedane	(<i>E</i>) 14110 1	man not	0710000	tile value o	tine rom	5 W III 5	
		Working Voltage (V	6.3	10	16	25	35	50	63	
		Z-25°C/Z+20°C	5	4	3	2	2	2	2	
		Z-40°C/Z+20°C	10	8	6	4	3	3	3	
		Z-40 C/Z+20 C	10	0	0	4	3	3		
		Working Voltage (V)	100	160~220	250	-350	400~420	450		
		Z-25°C/Z+20°C	2	3	4	1	6	15		
		Z-40°C/Z+20°C	3		_	-				
		For capacitance value > 1000 \mu F, Add 0.5 per another 1000 \mu F for Z-25/Z+20°C,								
		•		Add 1.0	per ano	ther 10	000 µ F for Z			
		Capacitance, $\tan \delta$, a		Add 1.0	per ano	ther 10	000 µ F for Z			
		Capacitance, tan δ, a	nd impeda:	Add 1.0	per ano e measur	ther 10 ed at 1	000 µ F for Z 20Hz.	Z-40°C/Z-	+20℃.	
		Capacitance, tan δ, a <condition> According to IEC60</condition>	nd impeda:	Add 1.0 nce shall b	per ano e measur	ther 10 red at 1	000 μ F for Z 20Hz.	a tempe	+20°C.	
		Capacitance, tan δ, a <condition> According to IEC60 105°C ±2 with DC</condition>	nd impeda 384-4No.4.	Add 1.0 nce shall b	per ano e measur ls, The ca	ther 10 red at 1	000 µ F for Z 20Hz. or is stored at rent for Tabl	a temper	+20°C.	
		Capacitance, tan δ, a <condition> According to IEC60 105°C ±2 with DC DC and ripple peak</condition>	nd impeda: 384-4No.4 bias voltage	Add 1.0 nce shall b	ls, The cated ripp	ther 10 red at 1 apacito ale curro e rateo	2000 µ F for Z 20Hz. For is stored at the ent for Tabl	a tempe e 1. (The oltage) T	rature of sum of then the	
	Load	Capacitance, tan δ, a <condition> According to IEC60 105°C ±2 with DC</condition>	nd impeda 384-4No.4 bias voltage voltage s sted after 1	Add 1.0 nce shall but 13 method e plus the reall not ex 6 hours red	ls, The cated ripp	ther 10 red at 1 apacito ale curro e rateo	2000 µ F for Z 20Hz. For is stored at the ent for Tabl	a tempe e 1. (The oltage) T	rature of sum of then the	
4.7	Load life	Capacitance, tan δ, a <condition> According to IEC60 105°C ±2 with DC DC and ripple peak product should be te</condition>	nd impeda 384-4No.4 bias voltage voltage s sted after 1	Add 1.0 nce shall but 13 method e plus the reall not ex 6 hours red	ls, The cated ripp	ther 10 red at 1 apacito ale curro e rateo	2000 µ F for Z 20Hz. For is stored at the ent for Tabl	a tempe e 1. (The oltage) T	rature of sum of then the	
4.7		Capacitance, tan δ, a <condition> According to IEC60 105°C ±2 with DC DC and ripple peak product should be te result should meet the</condition>	384-4No.4. bias voltage voltage sted after 1 te following	Add 1.0 nce shall but 13 method e plus the reall not ex 6 hours reag table:	by per anough per anou	apacito	pr is stored at the rent for Table working we atmospheric	a tempe e 1. (The oltage) T	rature of sum of then the	
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4.7	life	Capacitance, tan δ , a <condition> According to IEC60 105°C ±2 with DC DC and ripple peak product should be te result should meet the <criteria> The characteristic slower in the characteristic</criteria></condition>	nd impeda. 384-4No.4. bias voltage s sted after 1 te following hall meet the	Add 1.0 nce shall but 13 method e plus the reful not execute table:	b) per ano e measureds, The cated ripp sceed the covering g required 4.3 shall	ther 10 red at 1 red	2000 µ F for Z 20Hz. For is stored at the ent for Table I working vot atmospherical	a tempe e 1. (The oltage) T	rature of sum of then the	
4.7	life	Capacitance, tan δ , a < Condition> According to IEC60 105°C ±2 with DC DC and ripple peak product should be te result should meet the Criteria> The characteristic stock Leakage currents.	nd impeda. 384-4No.4. bias voltage s sted after 1 te following hall meet the	Add 1.0 nce shall but 13 method e plus the reference following table: Within Head of the shall not expense following table.	by per anough per anou	red at 1 rements rements rements rements	2000 µ F for Z 20Hz. For is stored at the ent for Table I working vot atmospherical	a tempe e 1. (The oltage) T	rature of sum of then the	
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4.7	life	Capacitance, tan δ, a <condition> According to IEC60 105°C ±2 with DC DC and ripple peak product should be te result should meet th <criteria> The characteristic sl Leakage curre Capacitance of tan δ</criteria></condition>	nd impeda. 384-4No.4. bias voltage s sted after 1 te following hall meet the	Add 1.0 nce shall be 13 method e plus the result in the re	by per anough per anou	apacito apacit	2000 µ F for Z 20Hz. For is stored at the ent for Table I working vot atmospherical isfied value. the specified	a tempe e 1. (The oltage) To c condition	rature of sum of then the	
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4.7	life test	Capacitance, tan δ , a <condition> According to IEC60 105°C ±2 with DC DC and ripple peak product should be te result should meet th <criteria> The characteristic sl Leakage curre Capacitance tan δ Appearance <condition> The capacitors are the 1000+48/0 hours. For chamber and be allowed.</condition></criteria></condition>	and impeda 384-4No.4. bias voltage s sted after 1 he following hall meet the ent Change	Add 1.0 nce shall be 13 method e plus the result in the following table: The following table in the following within the shall not expected to the following with no voltation of the following table in the following with no voltation is period the following table in the foll	by per anounce measured stated ripper accept the covering arequired 4.3 shall a 20% of all be nounce age applicated apparatus of the capacter	apacitor apa	property of the property of th	a tempe e 1. (The oltage) To condition to the condition t	rature of the sum of the the s	
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		<criteria></criteria>	
		The characteristic shall meet the	
		Leakage current	Value in 4.3 shall be satisfied
4.0	Shelf	Capacitance Change	Within $\pm 20\%$ of initial value.
4.8	life	tan 8	Not more than 200% of the specified value.
	test	Appearance	There shall be no leakage of electrolyte.
		Remark: If the capacitors are sto	ored more than 1 year, the leakage current may
		increase. Please apply voltage th	arough about 1 k Ω resistor, if necessary.
4.9	Surge test	The capacitor shall be submitted followed discharge of 5 min 30s. The test temperature shall be 1 C _R :Nominal Capacitance (µ F < Criteria> Leakage current Capacitance Change tan δ Appearance Attention:	15~35℃.
4.10	Vibration test	perpendicular directions. Vibration frequency rang Peak to peak amplitude Sweep rate Mounting method: The capacitor with diameter greatin place with a bracket. 4mm or less <criteria> After the test, the following item Inner construction No Appearance Of 6</criteria>	: 1.5mm : $10 \text{Hz} \sim 55 \text{Hz} \sim 10 \text{Hz}$ in about 1 minute atter than 12.5 mm or longer than 25 mm must be fixed Within 30°

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		<condition></condition>					
		The capacitor shall be tes		_	onditions	s:	
		Soldering temperature		245±3°C			
	0.111.77	Dipping depth		2mm			
4.11	Solderability	Dipping speed		25±2.5mm/s 3±0.5s	3		
	test	test Dipping time					
		<criteria></criteria>		Aii	-£050/ ·	- C41C 1	
		Coating quality		immersed	01 95% (of the surface	being
		<condition></condition>					
		Terminals of the capacito	r shall be im	mersed into	solder b	bath at 260 ± 3	5°Cfor10±
		1seconds or $400 \pm 10^{\circ}\text{C}$ for	or3 ⁺¹ ₋₀ seconds	s to 1.5~2.0m	nm from	the body of ca	apacitor.
		Then the capacitor shall be					
	Resistance to	for 1~2 hours before mea			r		
4.12	solder heat	<c<u>riteria></c<u>					
	test	Leakage current	Not	more than the	e specifi	ed value.	
		Capacitance Change	With	$\sin \pm 10\%$ of	`initial v	value.	
		tan 8	Not	more than the	e specifi	ed value.	
		Appearance	Ther	e shall be no	leakage	of electrolyte	
		C . 1'4'					
		<condition> Temperature Cycle:Acco</condition>	rding to IEC	50384-4No.4	7metho	de canacitor e	hall he
		placed in an oven, the con				us, capacitor s	man oc
			emperature			ime	
		(1)+20°C	• · · · · p • · · · · · · · ·		€3	Minutes	
		(2)Rated low temper	ature (-40°C)	-	30 ± 2	Minutes	
4.12	Change of	(3)Rated high tempe		-	$\frac{30\pm 2}{30\pm 2}$	Minutes	
4.13	temperature test			C)	<u> </u>	Williates	
	icsi	(1) to (3)=1 cycle, to Criteria>	ital 3 cycle				
		The characteristic shall m	neet the follow	ving requiren	ment		
		Leakage current				zalue	
		tan 8		e than the sp			
		Appearance		nall be no leal			
		<condition></condition>	THERE SI	iun oc no icu	Ruge or	creetiory te.	
		Humidity Test:					
		According to IEC60384-	4No 4 12 met	thods capacit	tor shall	be exposed for	or 500±8
		hours in an atmosphere o		_		-	
		meet the following requir		1.41 10_2 0	, 1110 011		ange snan
		<criteria></criteria>					
4 1 4	Damp heat	Leakage current	Not more the	han the speci	fied valu	ie.	
4.14	test	Capacitance Change	Within ±2	0% of initial	l value.		
		tan δ	Not more th	han 120% of	the spec	cified value.	
		Appearance	There shall	be no leakag	ge of ele	ctrolyte.	

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		<condition> The following tes with vent.</condition>	st only app	ly to those j	products	with ve	ent prodi	ucts at d	iameter >	>CX6.2
4.15	Vent	D.C. test The capacitor is current selected <table 3=""> Diameter (m 22.4 or les Over 22.4 <criteria> The vent shall op pieces of the capacitor is current selected The vent shall op pieces of the capacitor is current selected.</criteria></table>	from belownm) DC ss 4	Current (A 1 10 no dangero	plarity repplied.	versed t	to a DC	power	source. T	Then a
		Condition> The maximum at 120Hz and of Table-1 The combined rated voltage at Frequency M Rated Voltage (V)	value of E	D.C voltage of reverse v	and the prolatage.	erating	tempera	ature		ed the
4.16	Maximum permissible (ripple current)	6.3~100	68	-47 -470 -560 7~220	0.75 0.80 0.85 0.80	1.00 1.00 1.00 1.00	1.35 1.23 1.10 1.25	1.57 1.34 1.13 1.40	2.00 1.50 1.15 1.60	
	Temperatur Temperatur Facto	re Coefficure (°C)	:270	95 1.41	1.00 1.00 1.00	1.10	1.13	1.15		

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Product Marking

Marking Details

Capacitor shall be marked the following items:

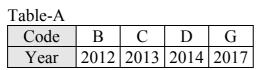
1) Nominal capacitance

Rated voltage

Series symbol (KM)

Tolerance: $-20\% \sim +20\%$ (M)

- 2) Polarity: Cathode shall be marked with a black stripe and indicate "-" symbol on it.
- 3) Trademark (SAMXON)
- 4) Maximum operating temperature: 105°C
- 5) Date code numbering system



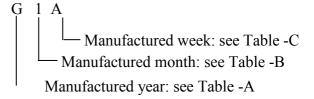


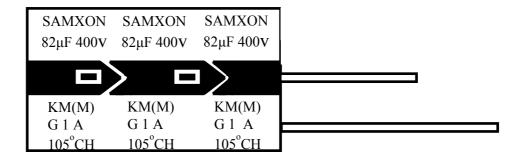
Table-B

Month	1	2	3	4	5	6	7	8	9	10	11	12
Code	1	2	3	4	5	6	7	8	9	О	N	D

Table-C

Week	1	2	3	4	5
Code	A	В	C	D	F

6) Marking Sample:



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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances					
	Cadmium and cadmium compounds					
Heavy metals	Lead and lead compounds					
Ticavy metais	Mercury and mercury compounds					
	Hexavalent chromium compounds					
	Polychlorinated biphenyls (PCB)					
Chloinated	Polychlorinated naphthalenes (PCN)					
organic	Polychlorinated terphenyls (PCT)					
compounds	Short-chain chlorinated paraffins(SCCP)					
Other chlorinated organic compounds						
D : 1	Polybrominated biphenyls (PBB)					
Brominated organic	Polybrominated diphenylethers(PBDE) (including					
	decabromodiphenyl ether[DecaBDE])					
compounds	Other brominated organic compounds					
Tributyltin comp	ounds(TBT)					
Triphenyltin com	pounds(TPT)					
Asbestos						
Specific azo com	pounds					
Formaldehyde						
Beryllium oxide						
Beryllium copp	er					
Specific phthalat	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)					
Hydrofluorocarb	on (HFC), Perfluorocarbon (PFC)					
Perfluorooctane :	sulfonates (PFOS)					
Specific Benzotr	iazole					

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Attachment: Application Guidelines

1.Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while tanδ increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2) Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about 1kΩ.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 ℃ for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

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- 2.6 Capacitor Handling after Solder
- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60°C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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