

# ATM2310NSA

## N-Channel Enhancement Mode Field Effect Transistor

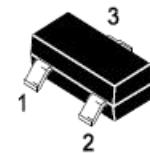
Drain-Source Voltage: 60V

Drain Current: 3A

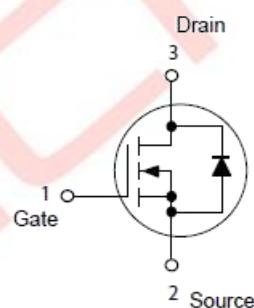
### Description

The ATM2310NSA uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as Battery protection or in other Switching application.

SOT-23



1 Gate 2 Source 3 Drain



### Features

- High power and current handing capability
- Surface mount package
- $R_{DS(ON)} < 100m\Omega$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 110m\Omega$  ( $V_{GS} = 4.5V$ )

### Application

- Battery Switch
- DC/DC Converter

### Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	3	A
Pulsed Drain Current <sup>1)</sup>	$I_{DM}$	10	
Maximum Power Dissipation	$P_D$	1	W
Thermal Resistance from Junction to Ambient <sup>2)</sup>	$R_{\theta JA}$	125	°C/W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55~+150	

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## Electrical characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise noted)

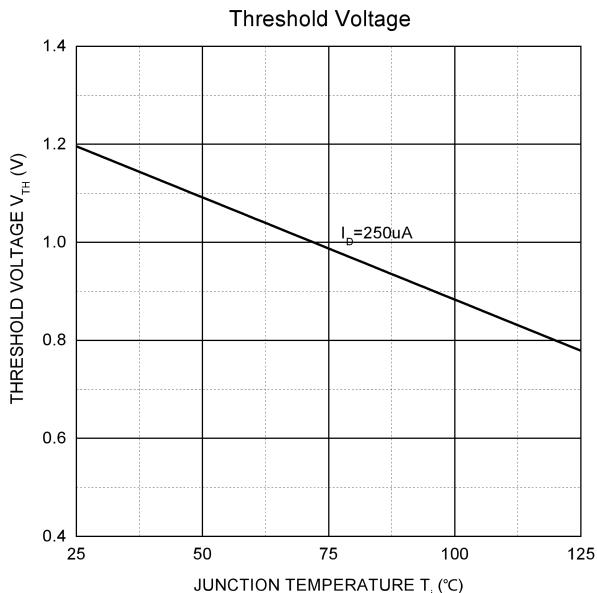
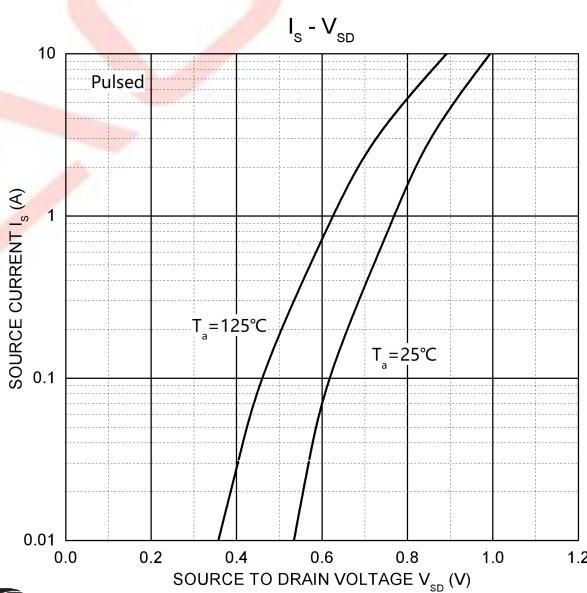
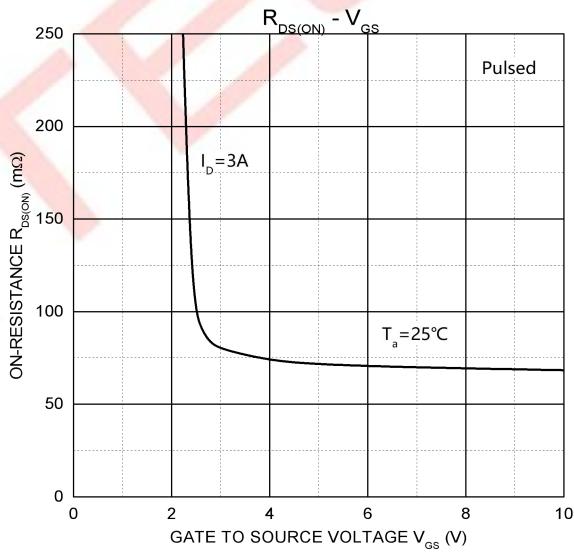
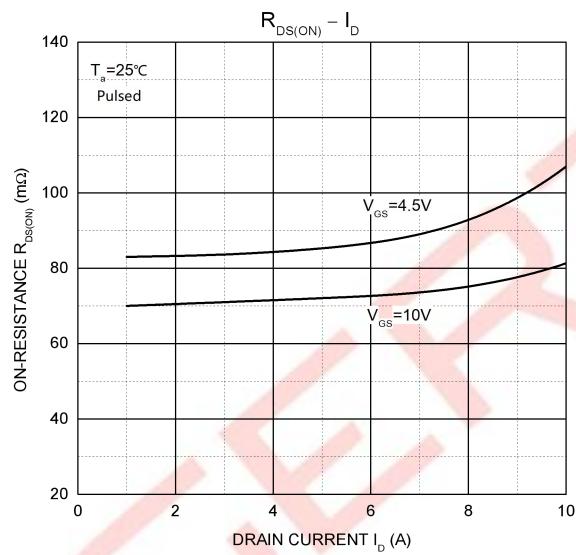
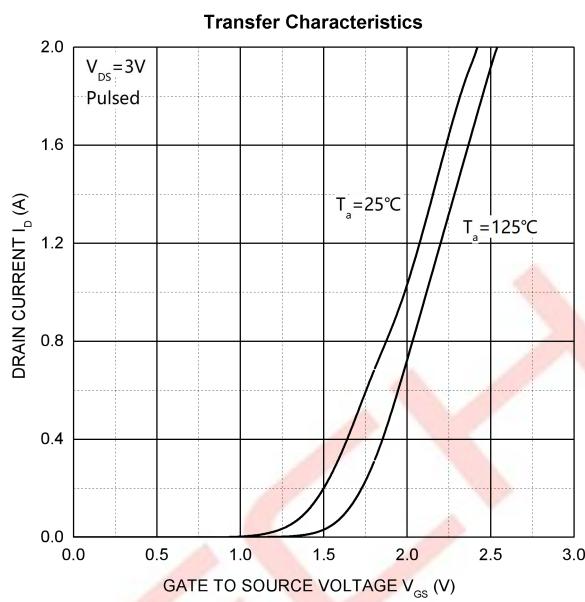
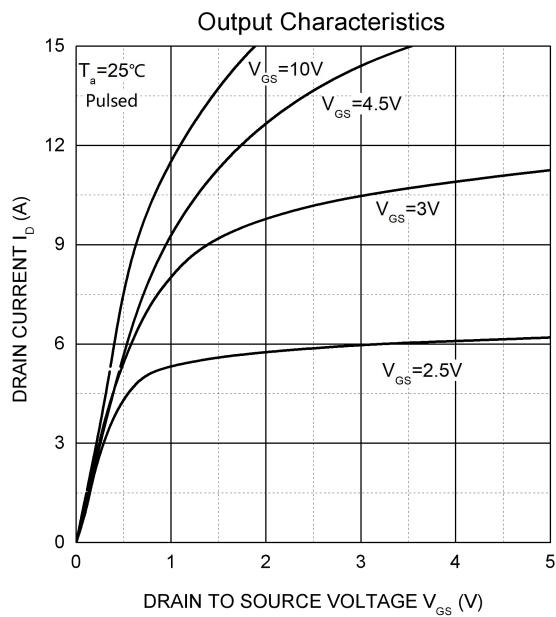
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	60			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 48V, V_{GS} = 0V$			1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Gate Threshold Voltage <sup>3)</sup>	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1		2.5	V
Drain-Source On-Resistance <sup>3)</sup>	$R_{DS(\text{on})}$	$V_{GS} = 10V, I_D = 2\text{A}$			100	$\text{m}\Omega$
		$V_{GS} = 4.5V, I_D = 1\text{A}$			110	
<b>Dynamic characteristics<sup>4)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 30V, V_{GS} = 0V, f = 1\text{MHz}$		250		pF
Output Capacitance	$C_{oss}$			26		
Reverse Transfer Capacitance	$C_{rss}$			20		
<b>Switching Characteristics<sup>4)</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 30V, V_{GS} = 4.5V, I_D = 3\text{A}$		7		nC
Gate-Source Charge	$Q_{gs}$			1.2		
Gate-Drain Charge	$Q_{gd}$			1.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 30V, I_D = 1.5\text{A}, R_{GEN} = 1\Omega$		6.5		ns
Turn-On Rise Time	$t_r$			15.2		
Turn-Off Delay Time	$t_{d(off)}$			15.2		
Turn-Off Fall Time	$t_f$			10.3		
<b>Source-Drain Diode characteristics<sup>4)</sup></b>						
Body Diode Voltage	$V_{SD}$	$I_S = 1\text{A}, V_{GS} = 0V$			1.2	V

### Notes:

- 1) Repetitive rating: Pulse width limited by junction temperature.
- 2) Surface mounted on FR4 board,  $t \leq 10\text{s}$ .
- 3) Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .
- 4) Guaranteed by design, not subject to production.

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## Typical Characteristics Curves



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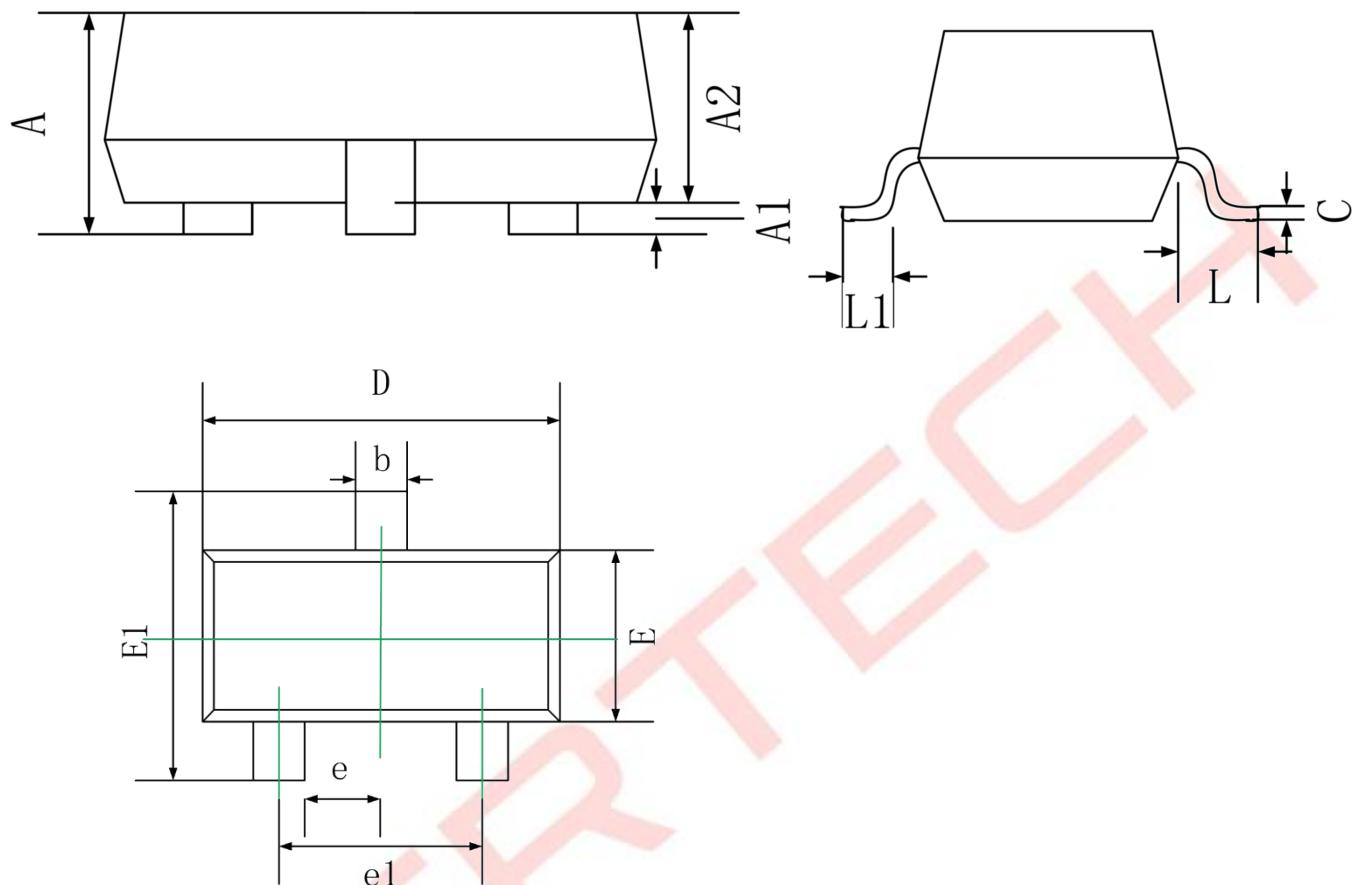
Dated: 5/2020

Rev: 2.1

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## Package Outline

SOT-23

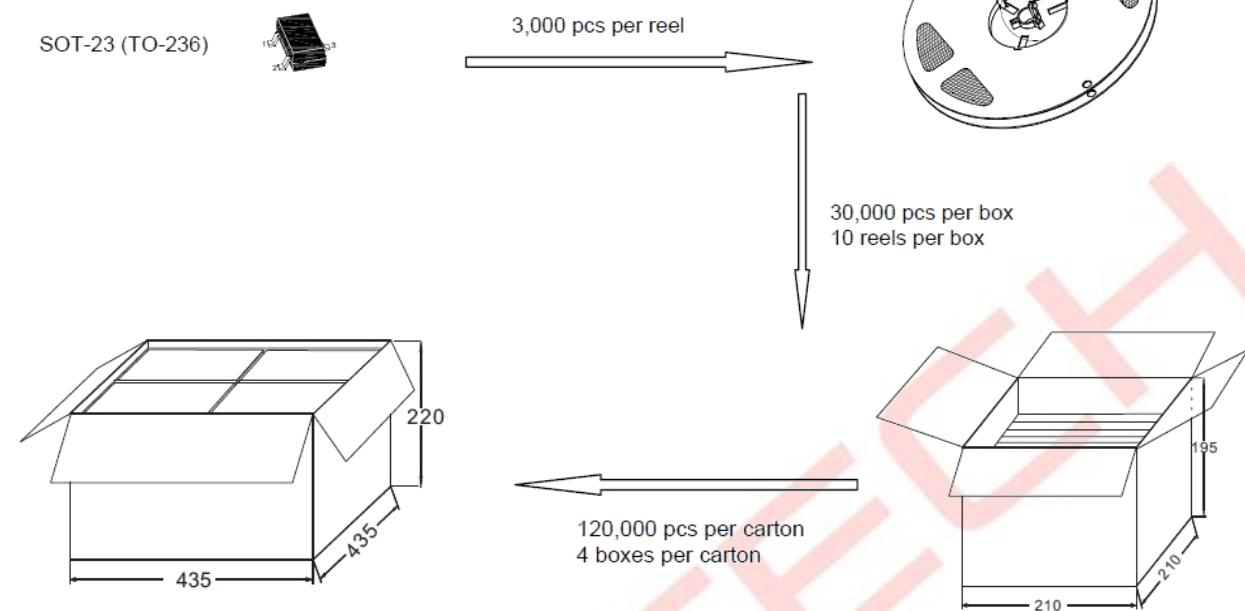


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.90	1.15
A1	0.00	0.10
A2	0.90	1.05
b	0.30	0.50
c	0.08	0.15
D	2.80	3.00
E	1.20	1.40
E1	2.25	2.55
e	0.95 REF.	
e1	1.80	2.00
L	0.55 REF.	
L1	0.30	0.50

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## Package Specifications

### ◆ The method of packaging



### ◆ Embossed tape and reel data

