

## **NCE N-Channel Super Trench Power MOSFET**

### **Description**

The NCEP40T35ALL uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

## **Application**

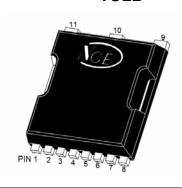
- DC/DC Converter
- •Ideal for high-frequency switching and synchronous rectification

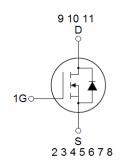
### **General Features**

- $V_{DS}$  =40V, $I_D$  =350A  $R_{DS(ON)}$ =0.63m $\Omega$  (typical) @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!







**Schematic Diagram** 

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP40T35ALL	NCEP40T35ALL	TOLL	-	-	-

### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	40	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous (Silicon Limited)	I <sub>D</sub>	350	Α
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	247	А
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	1400	А
Maximum Power Dissipation	P <sub>D</sub>	500	W
Derating factor		3.33	W/℃
Single pulse avalanche energy (Note 5)	Eas	1800	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	$^{\circ}$ C

### **Thermal Characteristic**

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	$R_{ heta JC}$	0.3	°C/W
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# NCEP40T35ALL

## Electrical Characteristics (T<sub>C</sub>=25 °C unless otherwise noted)

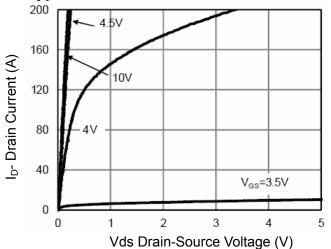
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	40		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	2.0		4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =175A	-	0.63	8.0	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =175A	100	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	V 20V/V 0V	-	11669	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =20 $V$ , $V_{GS}$ =0 $V$ ,	-	4641	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	140	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	26	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =20V, $I_{D}$ =175A	-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =1.6 $\Omega$	-	110	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	17	-	nS
Total Gate Charge	Qg	\/ -20\/  -4754	-	182	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=20V,I_{D}=175A,$	-	59		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	38		nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =175A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	350	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25^{\circ}C, I_F = I_S$	-		66	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-		240	nC

### Notes:

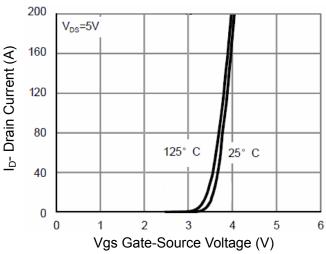
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25  $^{\circ}\text{C}$  ,V  $_{\text{DD}}$  =20V,V  $_{\text{G}}$  =10V,L=0.5mH,Rg=25 $\Omega$



### **Typical Electrical and Thermal Characteristics**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

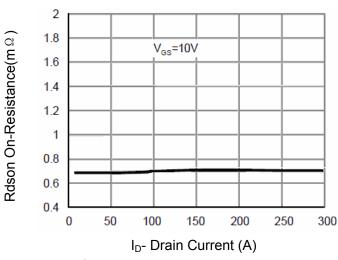
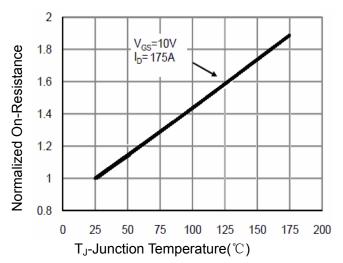


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 

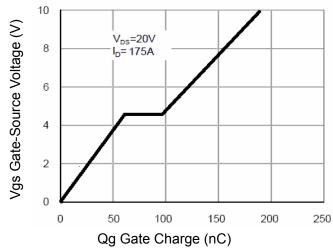


Figure 5 Gate Charge

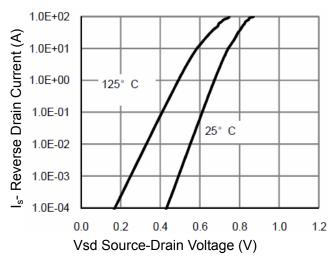


Figure 6 Source- Drain Diode Forward



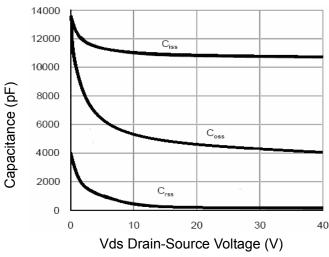


Figure 7 Capacitance vs Vds

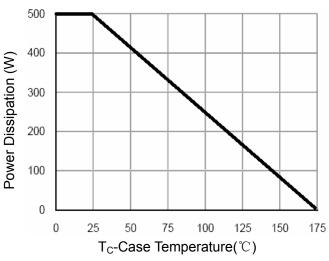
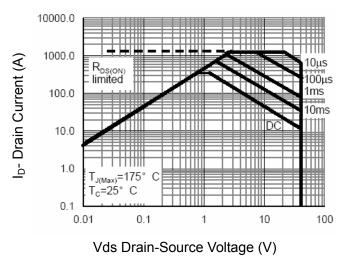


Figure 9 Power De-rating



**Figure 8 Safe Operation Area** 

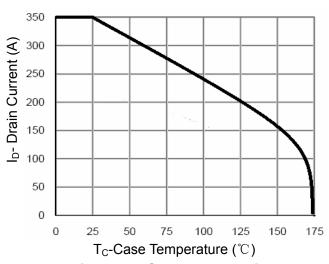
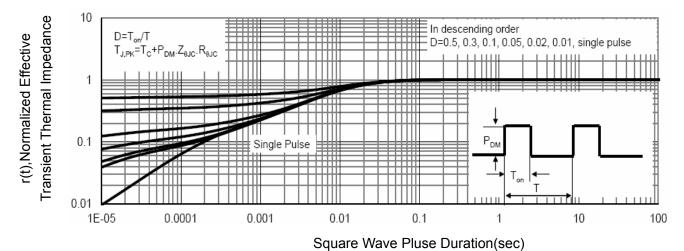


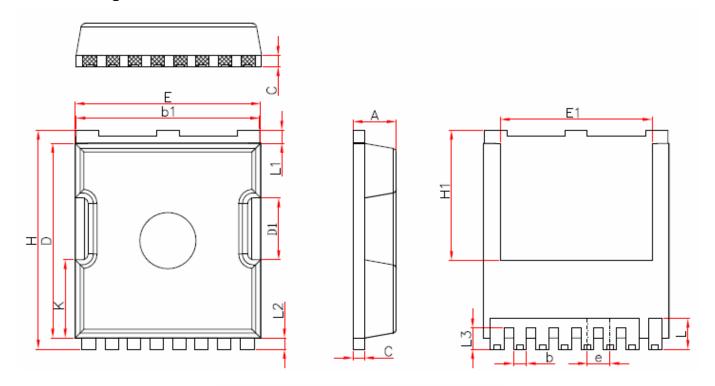
Figure 10 Current De-rating



**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **TOLL Package Information**



Symbol	Mi	ers		
	Min.	Nom.	Max.	
A	2.20	2.30	2.40	
b	0.65	0.75	0.85	
b1	9.70	9.80	9.90	
С	C 0.50		0.70	
D	10.30	10.40	10.50	
D1	3.15	3.3	3.45	
Е	9.70	9.90	10.10	
E1	8.00	8.10	8.20	
е	1.10	1.20	1.30	
Н	11.6	11.7	11.8	
H1	6.85	6.95	7.05	
K	4.08	4.18	4. 28	
L	1.60	1.65	2.10	
L1	0.60	0.70	0.80	
L2	L2 0.50 L3 1.05		0.70	
L3			1.30	





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