


Features

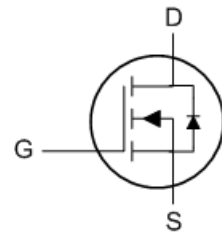
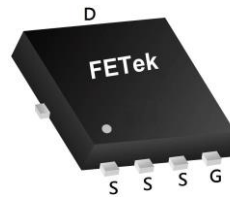
- Advanced Trench MOS Technology
- Low Gate Charge
- Low $R_{DS(ON)}$
- 100% EAS Guaranteed
- Green Device Available

Applications

- Power Management in Desktop Computer or DC/DC Converters.
- Isolated DC/DC Converters in Telecom and Industrial.

Product Summary

BVDSS	RDSON	ID
40V	3.2mΩ	114A

PRPAK5X6 Pin Configuration

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V ¹	114	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, V_{GS} @ 10V ¹	72	A
I_{DM}	Pulsed Drain Current ²	240	A
EAS	Single Pulse Avalanche Energy ³	145	mJ
I_{AS}	Avalanche Current	54	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	73.5	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	55	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	1.7	°C/W

N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=20A$	---	2.5	3.2	m Ω
		$V_{GS}=4.5V, I_D=15A$	---	3.8	5.3	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.7	2.2	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=40V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=40V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=20A$	---	75	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	1.5	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=20V, V_{GS}=4.5V, I_D=20A$	---	22.7	---	nC
Q_{gs}	Gate-Source Charge		---	7.5	---	
Q_{gd}	Gate-Drain Charge		---	5.5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=20V, V_{GS}=10V, R_G=3\Omega, I_D=20A$	---	10	---	ns
T_r	Rise Time		---	5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	33	---	
T_f	Fall Time		---	6.5	---	
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, f=1\text{MHz}$	---	2648	---	μF
C_{oss}	Output Capacitance		---	899	---	
C_{rss}	Reverse Transfer Capacitance		---	71	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	30	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=54A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

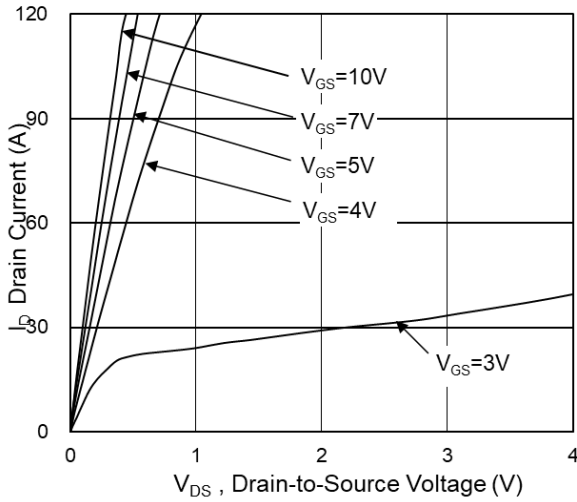


Fig.1 Typical Output Characteristics

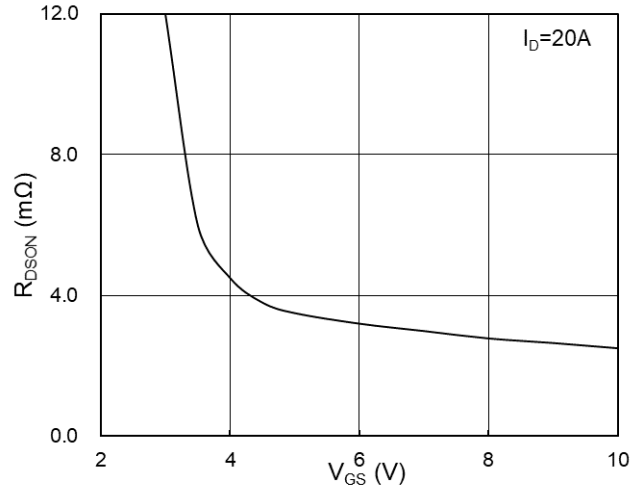


Fig.2 On-Resistance vs G-S Voltage

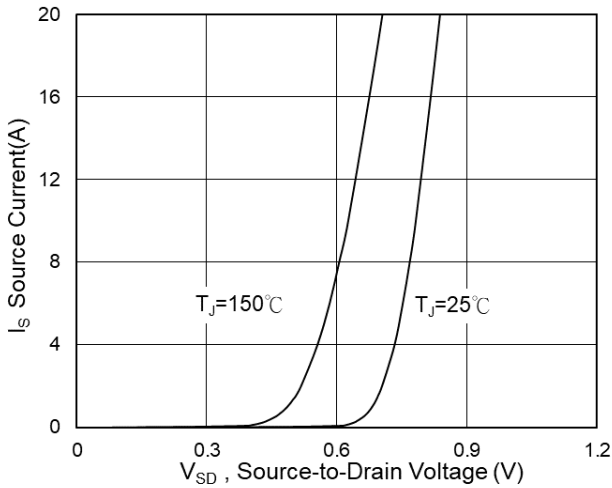


Fig.3 Source Drain Forward Characteristics

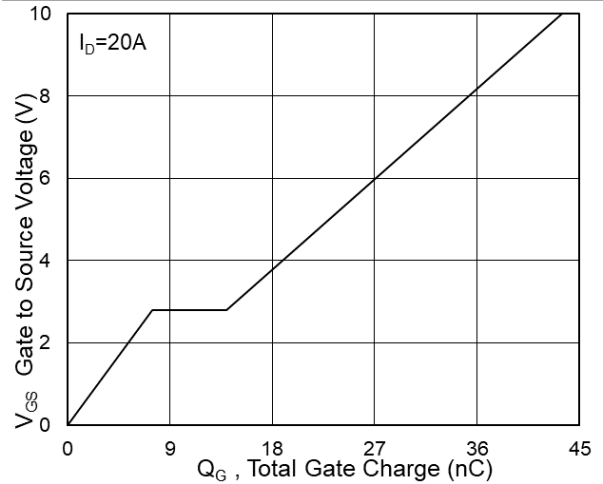


Fig.4 Gate-Charge Characteristics

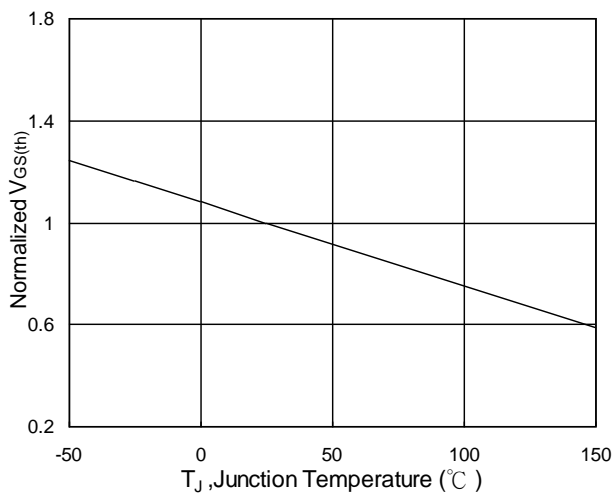


Fig.5 Normalized $V_{GS(th)}$ vs T_J

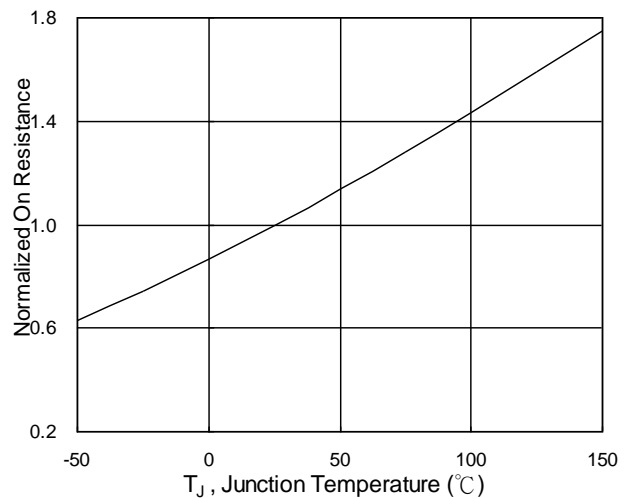


Fig.6 Normalized $R_{DS(on)}$ vs T_J

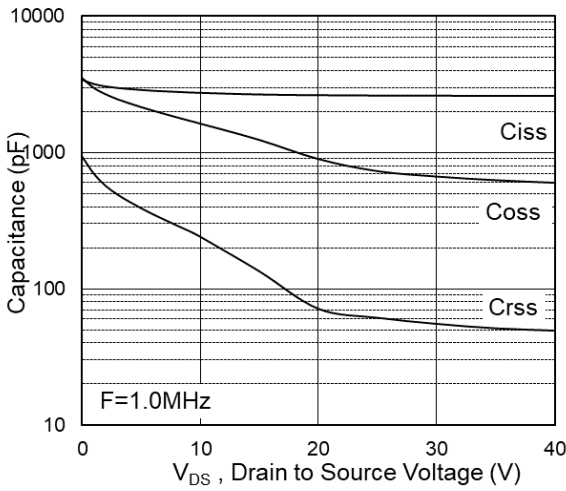


Fig.7 Capacitance

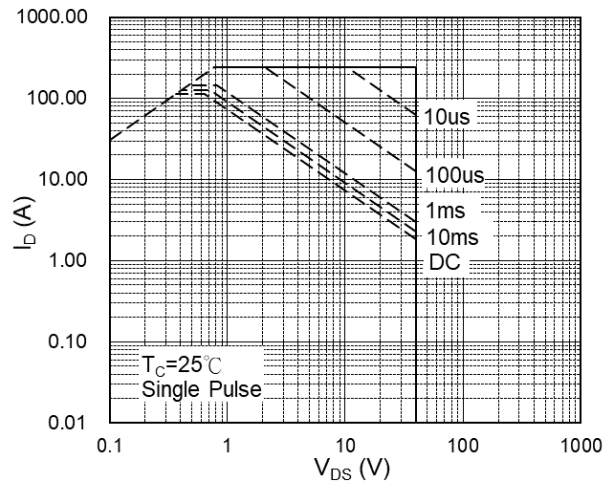


Fig.8 Safe Operating Area

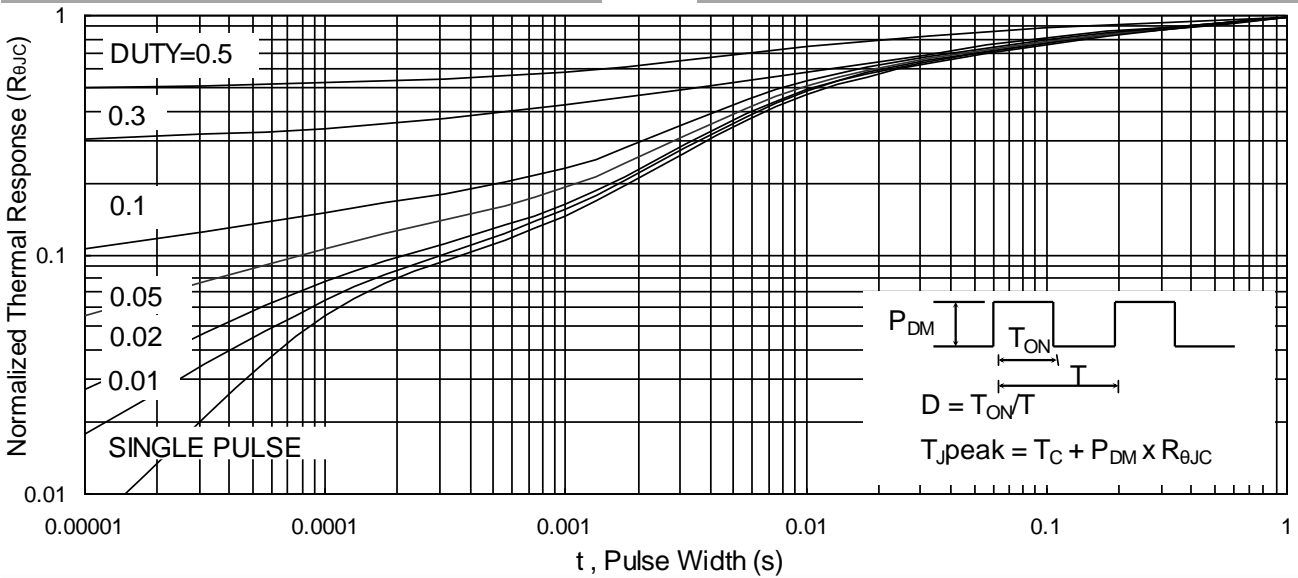


Fig.9 Normalized Maximum Transient Thermal Impedance

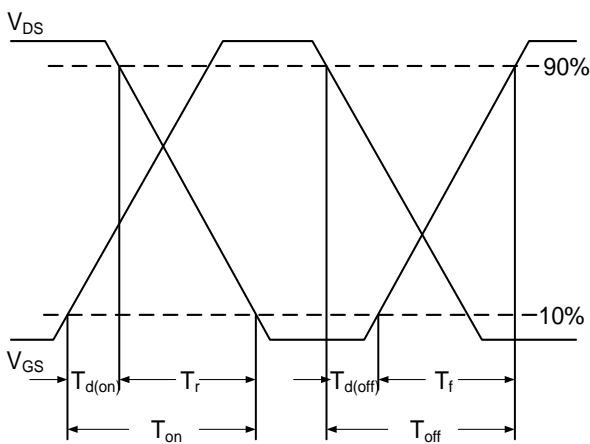


Fig.10 Switching Time Waveform



Fig.11 Unclamped Inductive Switching Wave