

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE55P15I uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- V_{DS} =-55V,I_D =-15A
 R_{DS(ON)} <75mΩ @ V_{GS}=-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- DC-DC Converter

Schematic diagram

G

Marking and pin assignment

TO-251top view

Package Marking and Ordering Information

100% UIS TESTED!

100% ΔVds TESTED!

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE55P15I	NCE55P15I	TO-251	-	-	-

Absolute Maximum Ratings (T_c=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-55	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	-15	А
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	-10	А
Pulsed Drain Current	I _{DM}	-50	А
Maximum Power Dissipation	PD	35	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	°C





Thermal Characteristic

Thermal Resistance ,Junction-to-Case ^(Note 2) R _{0JC} 4.3

Electrical Characteristics (T_c=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	· · ·		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-55V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.5	-2.6	-3.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-5A	-	60	75	mΩ
Forward Transconductance	G FS	V _{DS} =-15V,I _D =-5A	16	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	Clss	N/ 00)/// 0)/	-	1450	-	PF
Output Capacitance	Coss	V _{DS} =-20V,V _{GS} =0V, F=1.0MHz	-	145	-	PF
Reverse Transfer Capacitance	C _{rss}		-	110	-	PF
Switching Characteristics (Note 4)	· · ·		•			
Turn-on Delay Time	t _{d(on)}		-	8	-	nS
Turn-on Rise Time	tr	V_{DD} =-30V, ,R _L =30 Ω	-	9	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =6 Ω	-	65	-	nS
Turn-Off Fall Time	t _f		-	30	-	nS
Total Gate Charge	Qg	(1 - 20)(1 - 50)	-	26	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-30V,I _D =-5A,	-	4.5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-10V	-	7	-	nC
Drain-Source Diode Characteristics	· ·		•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-15A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	-15	Α

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t \leq 10 sec.

- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

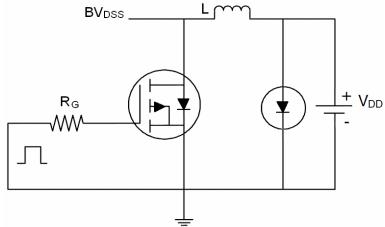


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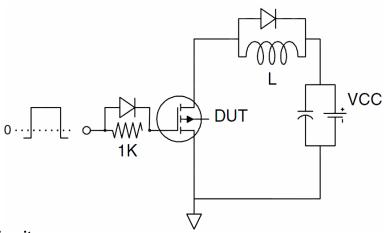




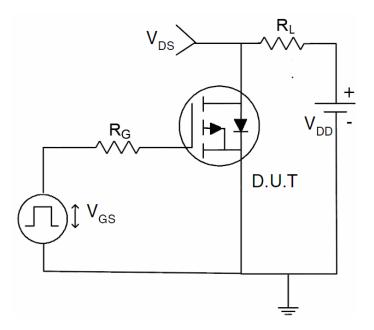
Test Circuit 1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit





75

100

40

T」= 25 ℃

1.0

0.8

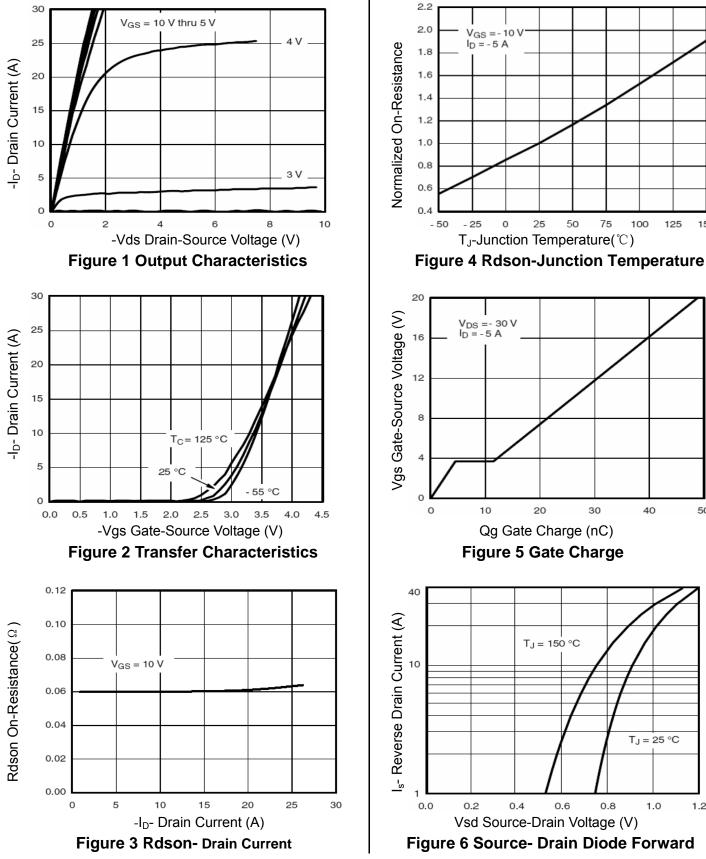
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125

150





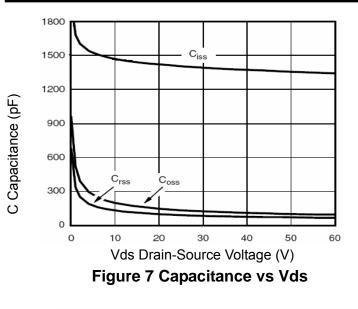


1.2



I_D- Drain Current (A)

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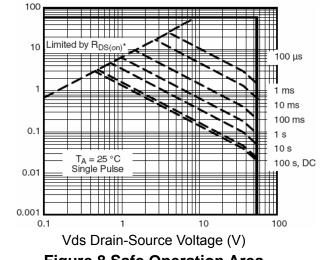
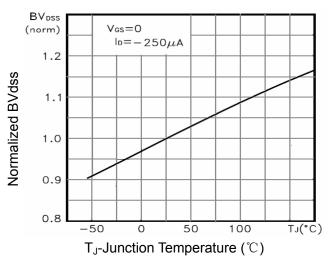


Figure 8 Safe Operation Area



Pb Free Product

NCE55P15I

Figure 9 BV_{DSS} vs Junction Temperature

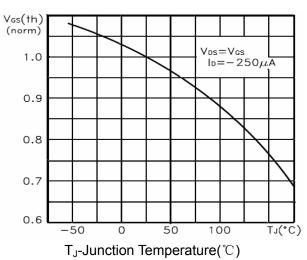
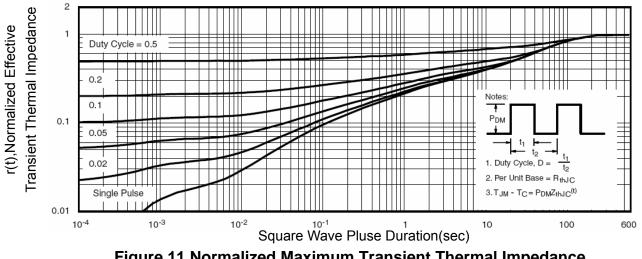


Figure 10 V_{GS(th)} vs Junction Temperature





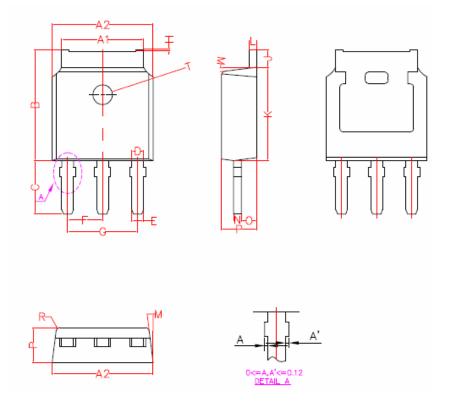


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TO-251 Package Information



Symbol	Min	Non	Max
A1	5.22	5.32	5.42
A2	6.55	6.60	6.65
В	7.05	7.10	7.15
C	4.80	5.00	5.20
D		1.00	
E		0.76	
F		2.286	
G		4.572	
Н		0.15	
J	0.95	1.00	1.05
K	6.05	6.10	6.15
L		0.508	
H		7°	
N		0.508	
0	0.96	1.01	1.06
P	2.25	2.30	2.35
R		0.25	







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