JIANGSU CHANGJING ELECTRONICS TECHNOLOGY CO., LTD

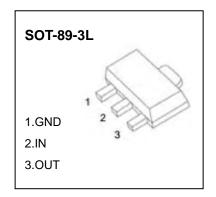


36V Low Current Consumption 250mA CMOS Voltage Regulators

# CJ75XX

#### INTRODUCTION

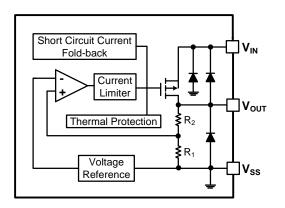
The CJ75XX series are a group of positive voltage regulators manufactured by CMOS technologies with low power consumption and low dropout voltage, which provide large output currents even when the difference of the inputoutput voltage is small. The CJ75XX series can deliver 250mA output current and allow an input voltage as high as 36V. The series are very suitable for the battery-powered equipments, such as RF applications and other systems requiring a quiet voltage source.



#### FEATURES

- Low Quiescent Current: 2µA
- Operating Voltage Range: 2.5V~36V
- Output Current: 250mA
- Low Dropout Voltage: 700mV@100mA(V<sub>OUT</sub>=3.3V)
- Output Voltage: 2.1~ 12V
- High Accuracy: ±2%/±1%(Typ.)
- High Power Supply Rejection Ratio: 70dB@1kHz
- Low Output Noise: 27xVout µVRMs(10Hz~100kHz)
- Excellent Line and Load Transient Response
- Built-in Current Limiter, Short-Circuit Protection
- Over-Temperature Protection
- Stable with Ceramic or Tantalum Capacitor

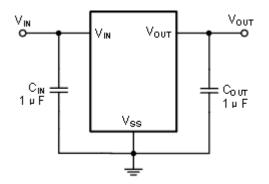
#### **BLOCK DIAGRAM**



#### **APPLICATIONS**

- Cordless Phones
- Radio control systems
- Laptop, Palmtops and PDAs
- Single-lens reflex DSC
- PC peripherals with memory
- Wireless Communication Equipments
- Portable Audio Video Equipments
- Car Navigation Systems
- LAN Cards
- Ultra Low Power Microcontrollers

#### **TYPICAL APPLICATION CIRCUIT**



## **Electrical Characteristics**

### ABSOLUTE MAXIMUM RATINGS

(Unless otherwise specified, TA=25°C)

	<b>v</b>	•	, ,
PARAMETER	SYMBOL	RATINGS	UNITS
Input Voltage <sup>(2)</sup>	V <sub>IN</sub>	-0.3~40	V
Output Voltage <sup>(2)</sup>	V <sub>OUT</sub>	-0.3~13	V
Power Dissipation	P <sub>D</sub>	0.6	W
Operating Ambient Temperature Range	T <sub>A</sub>	-40~+85	°C
Operating Junction Temperature Range <sup>(3)</sup>	Tj	-40~+125	°C
Storage Temperature	T <sub>stg</sub>	-40~+125	°C
Lead Temperature(Soldering, 10 sec)	T <sub>solder</sub>	260	°C

(1) Stresses beyond those listed under *absolute maximum ratings may* cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *recommended operating conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods my affect device reliability.

(2) All voltages are with respect to network ground terminal.

(3) This IC includes over temperature protection that is intended to protect the device during momentary overload. Junction temperature will exceed 125°C when over temperature protection is active. Continuous operation above the specified maximum operating junction temperature may impair device reliability.

#### **RECOMMENDED OPERATING CONDITIONS**

PARAMETER	MIN.	NOM.	MAX.	UNITS
Supply voltage at V <sub>IN</sub>	2.5		36	V
Operating junction temperature range, T <sub>j</sub>	-40		125	°C
Operating free air temperature range, T <sub>A</sub>	-40		85	°C

#### **ELECTRICAL CHARACTERISTICS**

PARAMETER	SYMBOL	CONDITIONS		MIN.	<b>TYP.</b> <sup>(4)</sup>	MAX.	UNITS
Input Voltage	V <sub>IN</sub>			2.5	_	36	V
Output Voltage Range	Vout			2.1	_	12	V
		1. 10m 1		-2	_	2	%
DC Output Accuracy		IOUT=	I <sub>OUT</sub> =10mA		_	1	%
Dropout Voltage	V <sub>dif</sub> <sup>(5)</sup>	I <sub>OUT</sub> =100mA	,V <sub>OUT</sub> =3.3V	_	700	—	mV
Supply Current	I <sub>SS</sub>	I <sub>OUT</sub> =	=0A	_	2	5	μA
Line Degulation	$\Delta V_{OUT}$	I <sub>OUT</sub> =	10mA	)mA		0.3	%/V
Line Regulation	$\overline{V_{OUT} \times \Delta V_{IN}}$	V <sub>OUT</sub> +1V≤V <sub>IN</sub> ≤36V		_	0.01		
Lood Dogulation	$\frac{\Delta V_{\text{OUT}}}{1 \text{ mA} \le I_{\text{OUT}} \le 100 \text{ m}}$		υт <b>+2</b> V,	10	10		mV
Load Regulation			≤100mA		10	_	mv
Temperature	$\Delta V_{OUT}$	I <sub>OUT</sub> =40mA,			50		
Coefficient	$\overline{V_{OUT} \times \Delta T_A}$	-40°C <t<sub>A&lt;85°C</t<sub>			50		ppm
Output Current Limit	I <sub>LIM</sub>	V <sub>OUT</sub> = 0.5 x V <sub>OUT(Normal)</sub>			350		mA
Short Current	I <sub>SHORT</sub>	V <sub>OUT</sub> =V <sub>SS</sub>		_	25	_	mA
		L E0mA	100Hz		80		
Power Supply			1kHz	_	70	—	
Rejection Ratio	PSRR	I <sub>OUT</sub> =50mA	10kHz	_	60	—	dB
			100kHz	_	50	—	

PARAMETER	SYMBOL	CONDITIONS	MIN.	<b>TYP.</b> <sup>(4)</sup>	MAX.	UNITS
Output Noise Voltage	V <sub>ON</sub>	BW=10Hz to 100kHz	_	27 x V <sub>OUT</sub>	_	μV <sub>RMS</sub>
Thermal Shutdown Temperature	$T_{SD}$	$I_{LOAD} = 30 \text{mA}$	_	160	_	°C
Thermal Shutdown Hysteresis	$\Delta T_{SD}$		_	20	_	°C

(4) Typical numbers are at 25°C and represent the most likely norm.

(5)V<sub>dif</sub>: The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output

Voltage Equals To 98% Of Vout (E).

#### APPLICATION INFORMATION

#### Selection of Input/ Output Capacitors

In general, all the capacitors need to be low leakage. Any leakage the capacitors have will reduce efficiency, increase the quiescent current.

A recent trend in the design of portable devices has been to use ceramic capacitors to filter DC-DC converter inputs. Ceramic capacitors are often chosen because of their small size, low equivalent series resistance (ESR) and high RMS current capability. Also, recently, designers have been looking to ceramic capacitors due to shortages of tantalum capacitors.

Unfortunately, using ceramic capacitors for input filtering can cause problems. Applying a voltage step to a ceramic capacitor causes a large current surge that stores energy in the inductances of the power leads. A large voltage spike is created when the stored energy is transferred from these inductances into the ceramic capacitor. These voltage spikes can easily be twice the amplitude of the input voltage step.

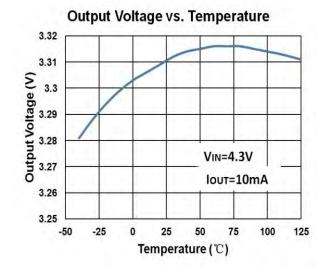
Many types of capacitors can be used for input bypassing, however, caution must be exercised when using multilayer ceramic capacitors (MLCC). Because of the self-resonant and high Q characteristics of some types of ceramic capacitors, high voltage transients can be generated under some start-up conditions, such as connecting the LDO input to a live power source. Adding a  $3\Omega$  res istor in series with an X5R ceramic capacitor will minimize start-up voltage transients.

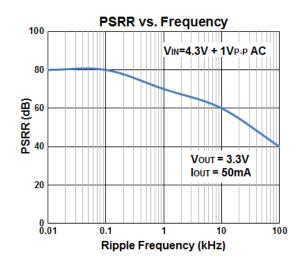
The LDO also requires an output capacitor for loop stability. Connect a 1µF tantalum capacitor from OUT to GND close to the pins. For improved transient response, this output capacitor may be ceramic.

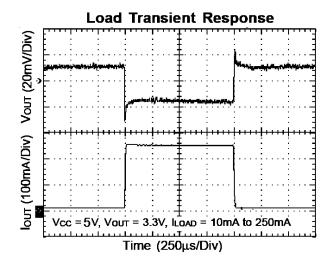
Model	Output Voltage
CJ7521	2.1V
CJ7523	2.3V
CJ7525	2.5V
CJ7527	2.7V
CJ7530	3.0V
CJ7533	3.3V
CJ7536	3.6V
CJ7540	4.0V
CJ7544	4.4V
CJ7550	5.0V
CJ7560	6.0V
CJ7570	7.0V
CJ7580	8.0V
CJ7590	9.0V
CJ75A0	10.0V
CJ75C0	12.0V

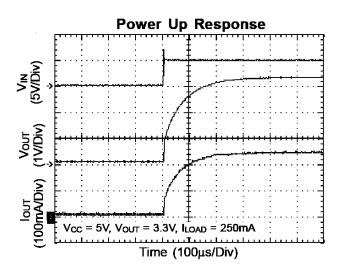
#### MODEL DEFINITION INFORMATION

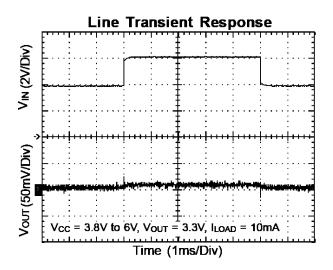
## **Typical Characteristics**

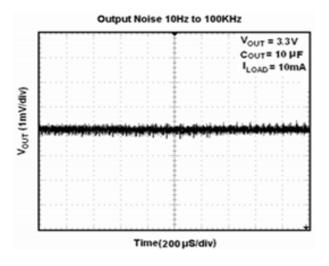




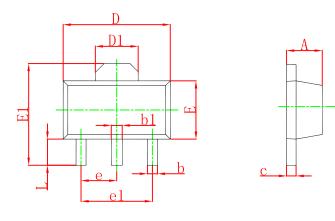






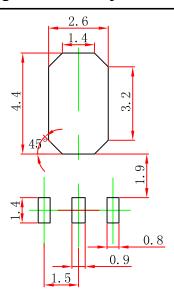


## SOT-89-3L Package Outline Dimensions



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.197	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550 REF		0.061 REF		
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP		0.060 TYP		
e1	3.000 TYP		0.118 TYP		
L	0.900	1.200	0.035	0.047	

### SOT-89-3L Suggested Pad Layout



#### Note:

1.Controlling dimension:in millimeters.

2.General tolerance:±0.05mm.

3. The pad layout is for reference purposes only.

# DISCLAIMER

### IMPORTANT NOTICE, PLEASE READ CAREFULLY

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