onsemi

MOSFET - Power, N-Channel, SUPERFET[®] III, FAST

V _{DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	95 mΩ @ 10 V	30 A

650 V, 95 mΩ, 30 A

NTH4LN095N65S3H

Description

SUPERFET III MOSFET is **onsemi's** brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

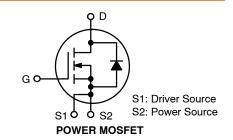
Consequently, SUPERFET III MOSFET FAST series is very suitable for the various power systems for miniaturization and higher efficiency.

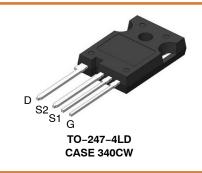
Features

- 700 V @ $T_J = 150^{\circ}C$
- Typ. $R_{DS(on)} = 77 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 58 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 522 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

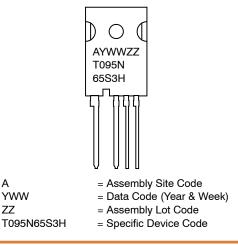
Applications

- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Parameter	Value	Unit V		
V _{DSS}	Drain to Source Voltage			650	
V _{GSS}	Gate to Source Voltage	– DC	±30	V	
		– AC (f > 1 Hz)	±30		
I _D	Drain Current	– Continuous (T _C = 25°C)	30	Α	
		– Continuous (T _C = 100°C)	18	1	
I _{DM}	Drain Current	– Pulsed (Note 1)	84	А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		284	mJ	
I _{AS}	Avalanche Current (Note 2)		5.5	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		2.08	mJ	
dv/dt MOSFET dv/dt			120	V/ns	
	Peak Diode Recovery dv/dt (Note 3)	20			
P _D	Power Dissipation	(T _C = 25°C)	208	W	
		– Derate Above 25°C	1.67	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		260	°C	

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse width limited by maximum junction temperature. 2. $I_{AS} = 5.5 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$. 3. $I_{SD} \le 15 \text{ A}$, di/dt $\le 200 \text{ A/}\mu\text{s}$, $V_{DD} \le 400 \text{ V}$, starting $T_J = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.60	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

PACKAGE MARKING AND ORDERING INFORMATION

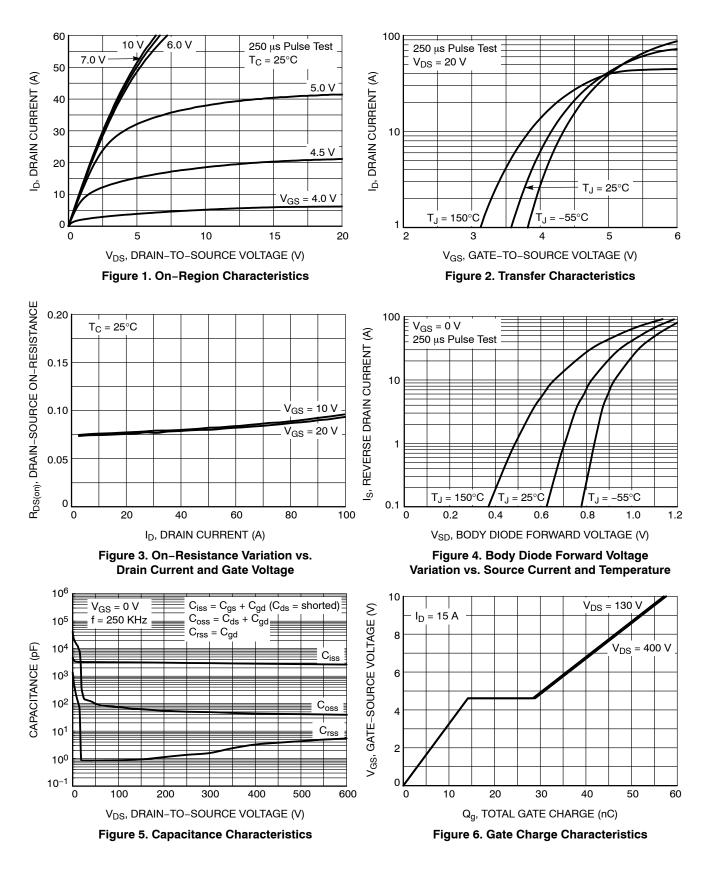
Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4LN095N65S3H	T095N65S3H	TO-247 L4 Narrow Lead	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

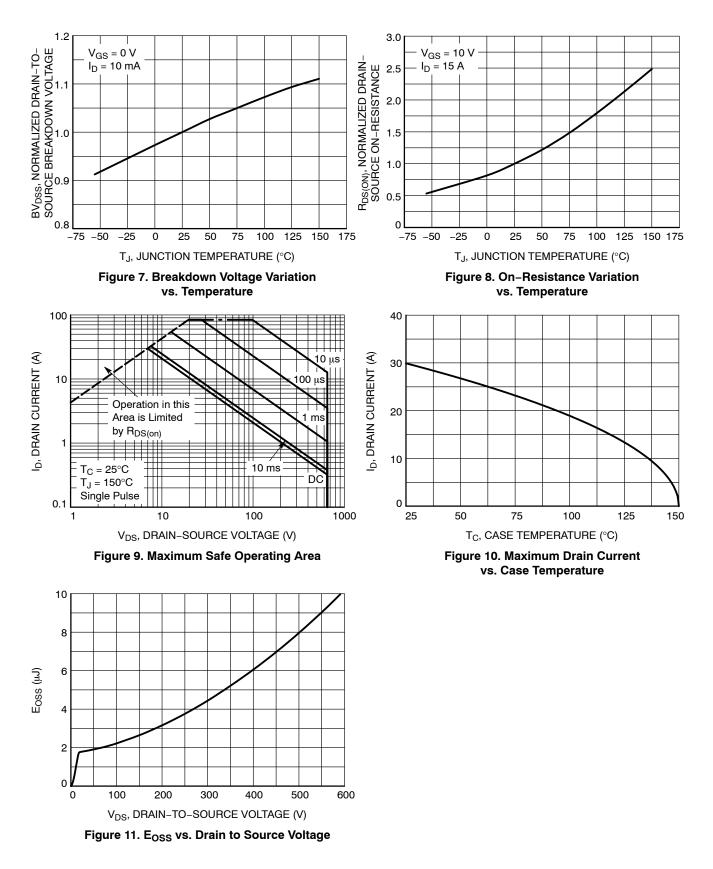
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS					
BV _{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 1 mA, T_J = 25°C	650	-	-	V
		V_{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700	-	-	V
$\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature I _D = 10 mA, Referenced to 25°C Coefficient		-	0.63	_	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 650 V, V_{GS} = 0 V	-	-	1	μΑ
		V_{DS} = 520 V, T_{C} = 125°C	-	1.8	-	
I _{GSS}	Gate to Body Leakage Current $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$		-	-	±100	nA
ON CHARACTE	RISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 2.8 \text{ mA}$	2.4	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 15 A	-	77	95	mΩ
9 FS	Forward Transconductance	V _{DS} = 20 V, I _D = 15 A	-	30	-	S
DYNAMIC CHA	RACTERISTICS					
C _{iss}	Input Capacitance		-	2833	-	pF
C _{oss}	Output Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 250 kHz	-	43	-	pF
C _{oss(eff.)}	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	-	522	-	pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	-	75	-	pF
Q _{g(tot)}	Total Gate Charge at 10 V		-	58	-	nC
Q _{gs}	Gate to Source Gate Charge	V _{DS} = 400 V, I _D = 15 A, V _{GS} = 10 V (Note 4)	-	14	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		-	15	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	1.2	-	Ω
SWITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time		-	23	-	ns
t _r	Turn-On Rise Time	V_{DD} = 400 V, I _D = 15 A, V _{GS} = 10 V, R _g = 4.7 Ω	-	6.5	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10 V, R _g = 4.7 Ω (Note 4)	-	69	-	ns
t _f	Turn-Off Fall Time		-	2.5	-	ns
SOURCE-DRAII	N DIODE CHARACTERISTICS			•	•	
۱ _S	Maximum Continuous Source to Drain Diode Forward Current			-	30	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current		-	-	84	А
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 15 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 15 A,	-	352	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 \text{ A}/\mu \text{s}$	_	5.8	_	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
4. Essentially independent of operating temperature typical characteristics.

TYPICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)



TYPICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)



TYPICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

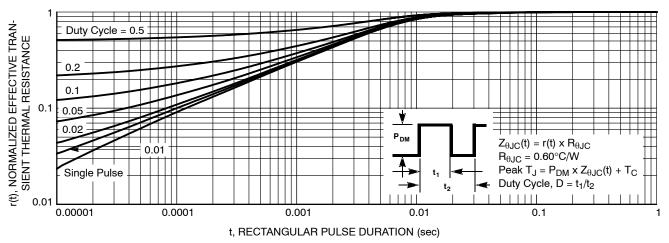


Figure 12. Transient Thermal Impedance

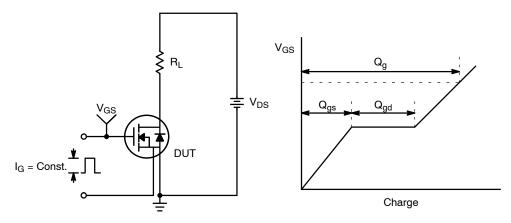


Figure 13. Gate Charge Test Circuit & Waveform

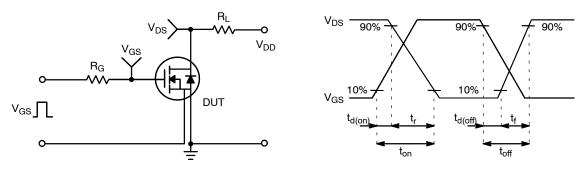
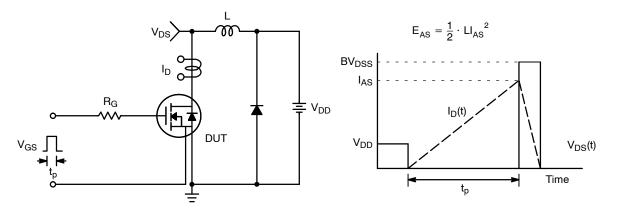


Figure 14. Resistive Switching Test Circuit & Waveforms





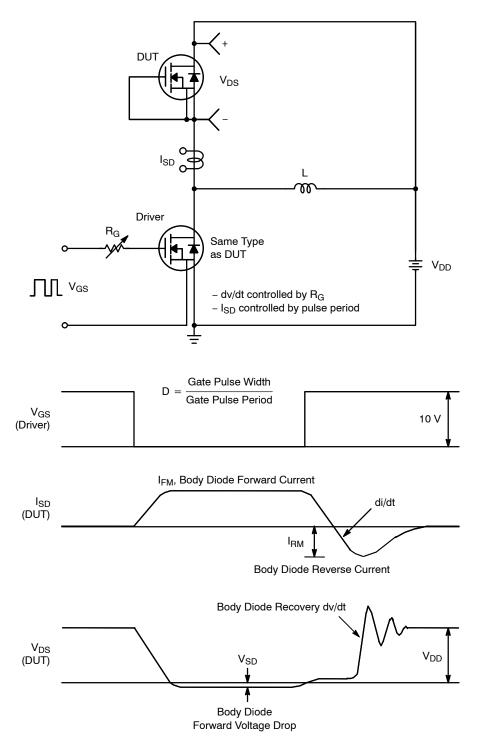
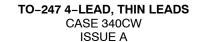
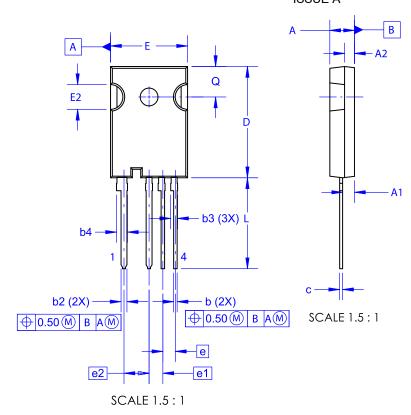


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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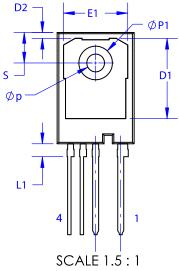
PACKAGE DIMENSIONS





NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS,MOLD FLASH AND TIE BAR EXTRUSIONS.
 C. ALL DIMENSIONS ARE IN MILLIMETERS.



JCALL 1.5.1					
DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
А	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	0.57	0.70	0.83		
b2	1.07	1.20	1.33		
b3	1.20	1.40	1.60		
b4	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.30	16.50		
D2	0.97	1.17	1.37		
е		2.54			
e1		2.79			
e2		5.08			
Е	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E2	4.80	5.00	5.20		
L	18.12	18.42	18.72		
L1	2.42	2.62	2.82		
Øр	3.40	3.60	3.80		
ØP1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

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