# onsemi

# MOSFET - Power, N-Channel, SUPERFET<sup>®</sup> III, FAST

V <sub>DSS</sub>	V <sub>DSS</sub> R <sub>DS(ON)</sub> MAX I <sub>D</sub>	
650 V	67 mΩ @ 10 V	40 A

# 650 V, 67 mΩ, 40 A

# NTH4LN067N65S3H

### Description

SUPERFET III MOSFET is **onsemi's** brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

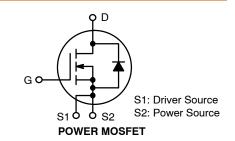
Consequently, SUPERFET III MOSFET FAST series is very suitable for the various power systems for miniaturization and higher efficiency.

## Features

- 700 V @  $T_J = 150^{\circ}C$
- Typ.  $R_{DS(on)} = 55 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 80 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 691 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

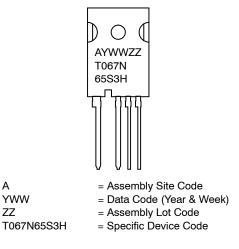
### Applications

- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar





MARKING DIAGRAM



### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Parameter	Value	Unit		
V <sub>DSS</sub>	Drain to Source Voltage		650	V	
V <sub>GSS</sub>	Gate to Source Voltage	– DC	±30	V	
		– AC (f > 1 Hz)	±30		
I <sub>D</sub>	Drain Current	– Continuous (T <sub>C</sub> = 25°C)	40	А	
		– Continuous (T <sub>C</sub> = 100°C)	25		
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	112	А	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		422	mJ	
I <sub>AS</sub>	Avalanche Current (Note 2)		6.5	А	
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		2.66	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		20		
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C)	266	W	
	– Derate Above 25°C		2.13	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C	
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		260	°C	

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse width limited by maximum junction temperature. 2.  $I_{AS} = 6.5 \text{ A}, R_G = 25 \Omega$ , starting  $T_J = 25^{\circ}\text{C}$ . 3.  $I_{SD} \le 20 \text{ A}, \text{ di/dt} \le 200 \text{ A/}\mu\text{s}, \text{V}_{DD} \le 400 \text{ V}$ , starting  $T_J = 25^{\circ}\text{C}$ .

#### **THERMAL CHARACTERISTICS**

Symbol	Parameter	Value	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.47	°C/W
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient, Max.	40	

#### PACKAGE MARKING AND ORDERING INFORMATION

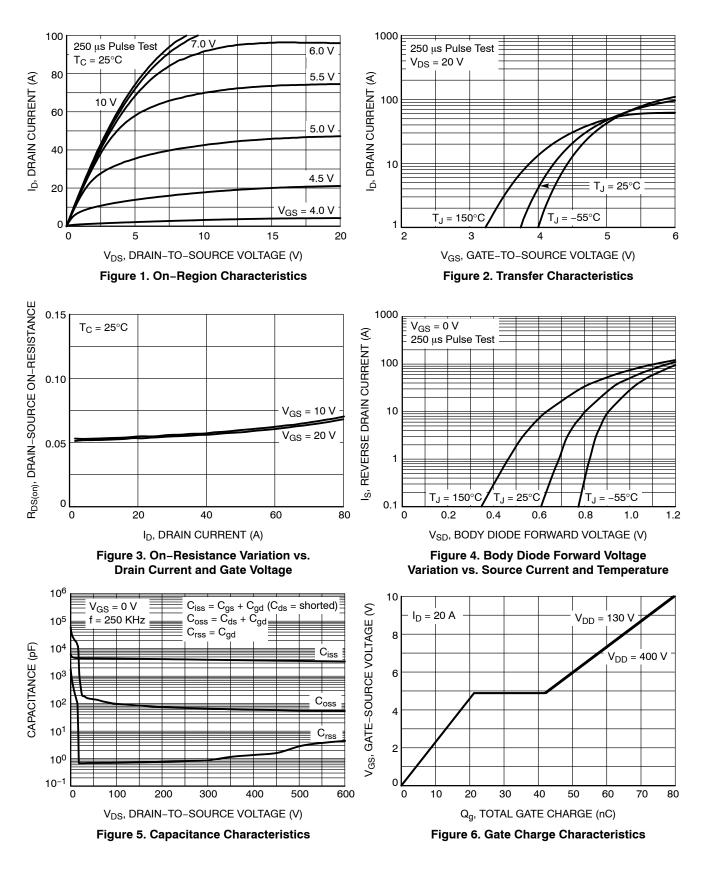
Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4LN067N65S3H	T067N65S3H	TO-247 L4 Narrow Lead	Tube	N/A	N/A	30 Units

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

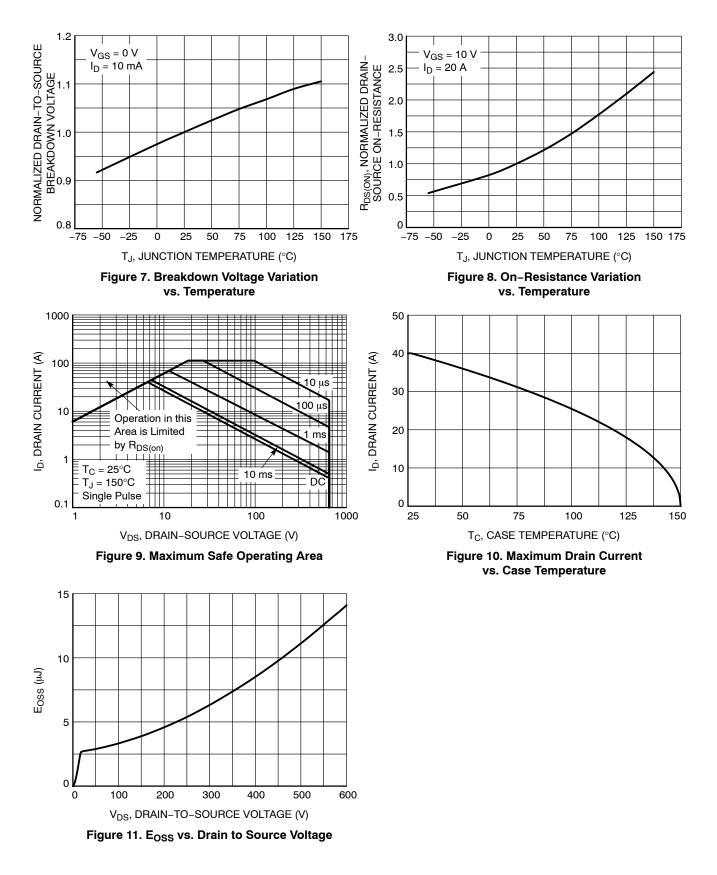
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS					-
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS}$ = 0 V, $I_D$ = 1 mA, $T_J$ = 25°C	650	-	-	V
		$V_{GS}$ = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C	700	-	-	V
$\Delta \text{BV}_{\text{DSS}}  /  \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature I <sub>D</sub> = 10 mA, Referenced to 25°C Coefficient		-	0.63	_	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS}$ = 650 V, $V_{GS}$ = 0 V	-	-	2	μΑ
		$V_{DS}$ = 520 V, $T_{C}$ = 125°C	-	1.6	-	
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V	-	-	±100	nA
ON CHARACTE	RISTICS					-
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ = 3.9 mA	2.4	-	4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS}$ = 10 V, I <sub>D</sub> = 20 A	-	55	67	mΩ
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	28	-	S
DYNAMIC CHAI	RACTERISTICS					-
C <sub>iss</sub>	Input Capacitance		_	3750	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V, f = 250 kHz	-	60	-	pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V	-	691	-	pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V	-	107	-	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V		-	80	-	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>DS</sub> = 400 V, I <sub>D</sub> = 20 A, V <sub>GS</sub> = 10 V (Note 4)	-	21	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	(	-	20	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	0.6	-	Ω
SWITCHING CH	IARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time		-	28	-	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD}$ = 400 V, I <sub>D</sub> = 20 A, V <sub>GS</sub> = 10 V, R <sub>g</sub> = 4.7 Ω	-	7.2	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$v_{GS} = 10 \text{ V}, \text{ H}_g = 4.7 \text{ S2}$ (Note 4)	-	81	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2.6	-	ns
SOURCE-DRAI	N DIODE CHARACTERISTICS					
I <sub>S</sub>	Maximum Continuous Source to Drain Diode Forward Current			-	40	Α
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current		_	-	112	А
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{SD} = 20 A$	_	-	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 20 A,	_	411	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	7.8	-	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
4. Essentially independent of operating temperature typical characteristics.

#### TYPICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)



# **TYPICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)



TYPICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

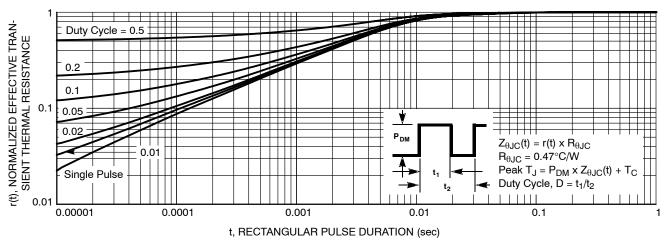


Figure 12. Transient Thermal Impedance

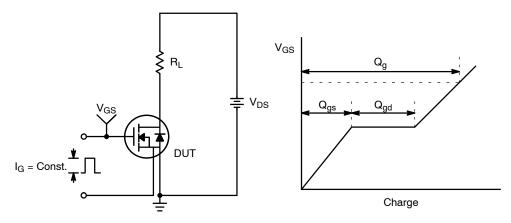


Figure 13. Gate Charge Test Circuit & Waveform

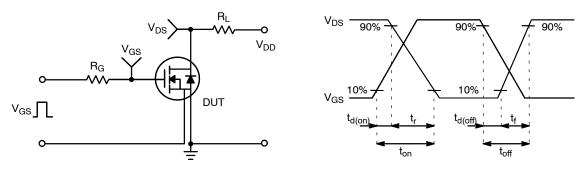
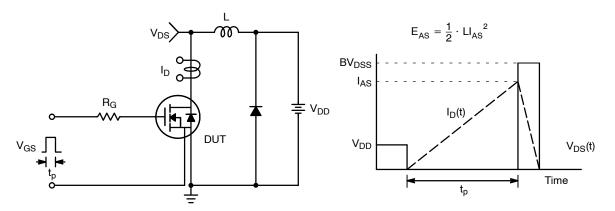


Figure 14. Resistive Switching Test Circuit & Waveforms





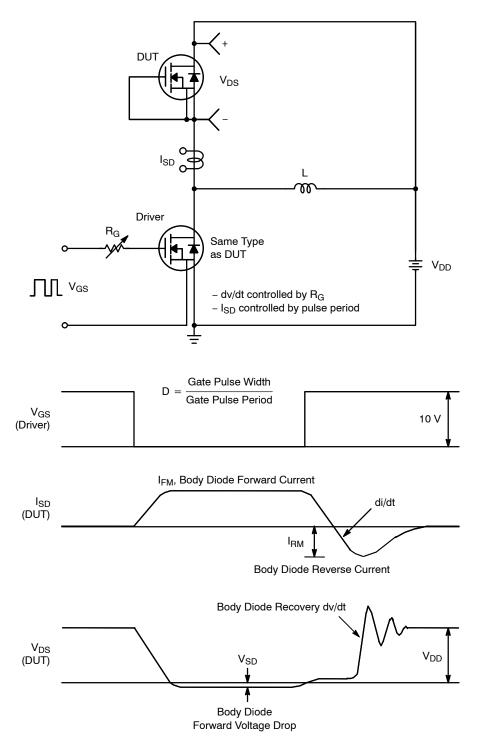
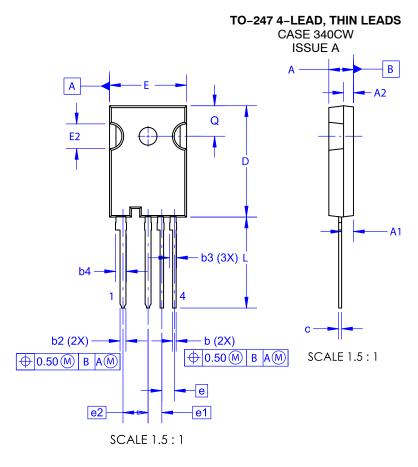


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

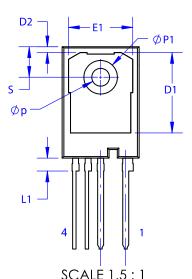
SUPERFET and FRFET are a registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.

# PACKAGE DIMENSIONS



NOTES:

- A. NO INDUSTRY STANDARD APPLIES
- TO THIS PACKAGE. B. DIMENSIONS ARE EXCLUSIVE OF BURRS,MOLD FLASH AND TIE BAR EXTRUSIONS.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.



	SCA	LE 1.5	•	
	MILLIMETERS			
DIM	MIN	NOM	MAX	
A	4.80	5.00	5.20	
A1	2.10	2.40	2.70	
A2	1.80	2.00	2.20	
b	0.57	0.70	0.83	
b2	1.07	1.20	1.33	
b3	1.20	1.40	1.60	
b4	2.02	2.22	2.42	
с	0.50	0.60	0.70	
D	22.34	22.54	22.74	
D1	16.00	16.30	16.50	
D2	0.97	1.17	1.37	
е		2.54		
e1		2.79		
e2		5.08		
E	15.40	15.60	15.80	
E1	12.80	13.00	13.20	
E2	4.80	5.00	5.20	
L	18.12	18.42	18.72	
L1	2.42	2.62	2.82	
Øp	3.40	3.60	3.80	
ØP1	6.60	6.80	7.00	
Q	5.97	6.17	6.37	
S	5.97	6.17	6.37	

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such

#### PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

#### TECHNICAL SUPPORT

onsemi Website: www.onsemi.com

Email Requests to: orderlit@onsemi.com

North American Technical Support: Voice Mail: 1 800–282–9855 Toll Free USA/Canada Phone: 011 421 33 790 2910 Europe, Middle East and Africa Technical Support: Phone: 00421 33 790 2910 For additional information, please contact your local Sales Representative