40 Watt Peak Power Zener Surge Protection Device

SC-70 Dual Common Cathode Zeners

MMBZ27VCW

These dual monolithic silicon zener diodes are designed for applications requiring transient overvoltage protection capability. They are intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment and other applications. Their dual junction common cathode design protects two separate lines using only one package. These devices are ideal for situations where board space is at a premium.

Specification Features:

- SC-70 Package Allows Either Two Separate Unidirectional Configurations or a Single Bidirectional Configuration
- Working Peak Reverse Voltage Range 22 V
- Standard Zener Breakdown Voltage 27 V
- Peak Power 40 W @ 1.0 ms (Bidirectional), per Figure 4 Waveform
- ESD Rating of Class N (exceeding 16 kV) per the Human Body Model
- Low Leakage < 100 nA
- Flammability Rating: UL 94 V-O
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics:

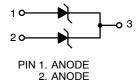
CASE: Void-free, transfer-molded, thermosetting plastic case **FINISH:** Corrosion resistant finish, easily solderable

MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES: 260°C for 10 Seconds

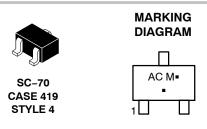


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AC = Specific Device Code M = Date Code = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBZ27VCWT1G	SC-70 (Pb-Free)	3000 / Tape & Reel
SZMMBZ27VCWT1G	SC–70 (Pb–Free)	3000 / Tape & Reel
MMBZ27VCWT3G	SC–70 (Pb–Free)	10000 / Tape & Reel
SZMMBZ27VCWT3G	SC–70 (Pb–Free)	10000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation @ 1.0 ms (Note 1) @ $T_L \le 25^{\circ}C$	P _{pk}	40	Watts
Total Power Dissipation on FR-5 Board (Note 2) @ T _A = 25°C Derate above 25°C	P _D	200 1.6	mW mW/°C
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	618	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	– 55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Nonrepetitive current pulse per Figure 4 and derate above $T_A = 25^{\circ}C$ per Figure 5.

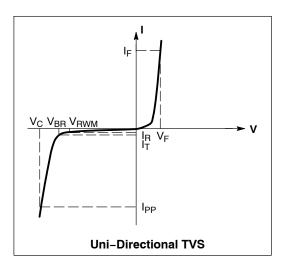
2. FR-5 = 1.0 x 0.75 x 0.62 in.

ELECTRICAL CHARACTERISTICS

(T_A = 25° C unless otherwise noted)

UNIDIRECTIONAL (Circuit tied to Pins 1 and 3 or 2 and 3)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
Ι _Τ	Test Current
V _{BR}	Maximum Temperature Coefficient of V_{BR}
١ _F	Forward Current
V _F	Forward Voltage @ I _F



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) **UNIDIRECTIONAL** (Circuit tied to Pins 1 and 3 or Pins 2 and 3)

(V_F = 1.1 V Max @ I_F = 200 mA)

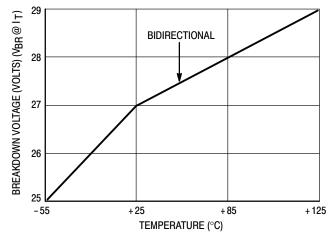
				Breakdown Voltage			V _C @ I _{PF}	o (Note 4)		
	Device	V _{RWM}	I _R @ V _{RWM}	VBI	a (Note 3)	(V)	@ I _T	Vc	I _{PP}	V _{BR}
Device	Marking	Volts	nA	Min	Nom	Max	mA	v	Α	mV/°C
MMBZ27VCWT1G, SZMMBZ27VCWT1G, MMBZ27VCWT3G, SZMMBZ27VCWT3G	AC	22	50	25.65	27	28.35	1.0	38	1.0	26

3. V_{BR} measured at pulse test current I_T at an ambient temperature of 25°C.

4. Surge current waveform per Figure 4 and derate per Figure 5

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TYPICAL CHARACTERISTICS





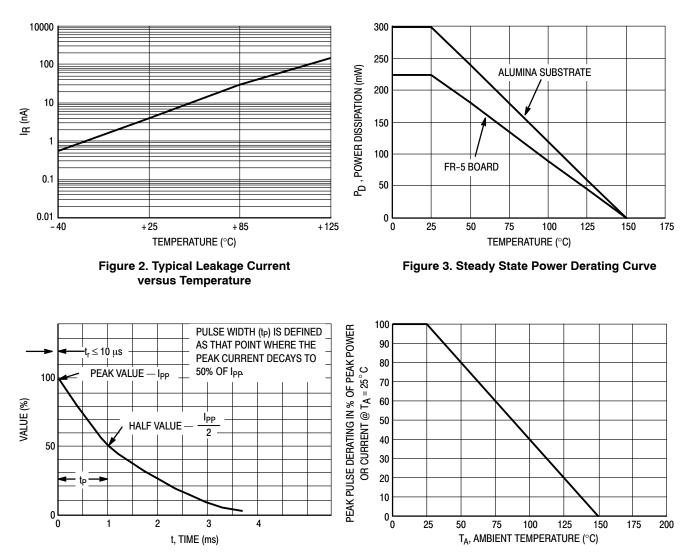
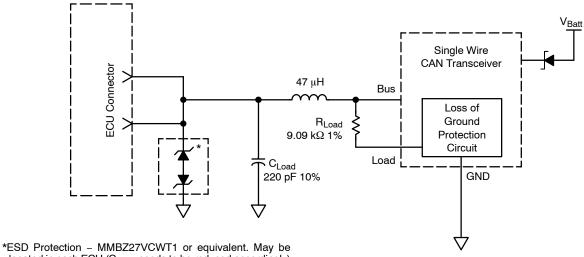




Figure 5. Pulse Derating Curve

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TYPICAL APPLICATIONS

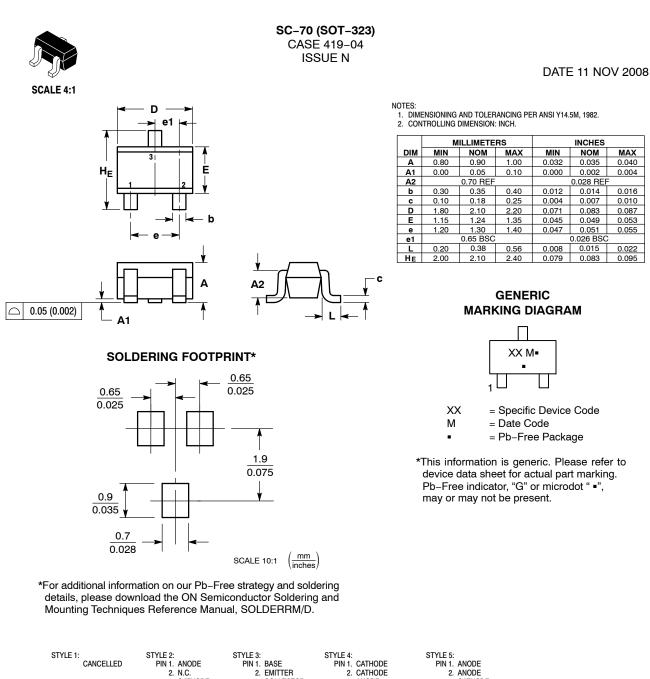


located in each ECU (C_{Load} needs to be reduced accordingly) or at a central point near the DLC.

Figure 6. Single Wire CAN Network

Figure is the recommended solution for transient EMI/ESD protection. This circuit is shown in the Society of Automotive Engineers February, 2000 J2411 "Single Wire CAN Network for Vehicle Applications" specification (Figure 6, page 11). Note: the dual common anode zener configuration shown above is electrically equivalent to a dual common cathode zener configuration.





	3. CATHODE	3. COLLECTOR	3. ANODE	3. CATHODE	
Style 6:	STYLE 7:	STYLE 8:	STYLE 9:	Style 10:	STYLE 11:
Pin 1. Emitter	PIN 1. BASE	PIN 1. GATE	Pin 1. Anode	Pin 1. Cathode	PIN 1. CATHODE
2. Base	2. EMITTER	2. SOURCE	2. Cathode	2. Anode	2. CATHODE
3. Collector	3. COLLECTOR	3. DRAIN	3. Cathode-Anode	3. Anode-Cathode	3. CATHODE

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