No.: TWMC-K-HTS-0001 /4

Date: 2017. 4. 21

Data sheet

Title: FIXED CHIP RESISTORS; RECTANGULAR TYPE & WIDE

TERMINATION

Style: TWMC32, 50, 63

AEC-Q200 qualified

RoHS COMPLIANCE ITEM Halogen and Antimony Free

Note: • Stock conditions

Temperature: $+5^{\circ}\text{C} \sim +35^{\circ}\text{C}$ Relative humidity: $25\% \sim 75\%$

The period of guarantee: Within 2 year from shipmen t by the company.

Solderability shall be satisfied.

 Product specification contained in this data sheet are subject to change at any time without notice

•If you have any questions or a Purchasing Specification for any quality Agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

Style

/4

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1. Scope

1.1 This data sheet covers the detail requirements for fixed chip resistors; rectangular type & wide termination, style of TWMC32, 50, 63...

1.2 Applicable documents

JIS C 5201-1: 2011, JIS C 5201-8: 2014, JIS C 5201-8-1: 2014 IEC60115-1: 2008, IEC60115-8: 2009, IEC60115-8-1: 2014

2. Classification

Type designation shall be the following form.

(Example)

1 Fixed thick film chip resistors; rectangular type and wide termination

2 dimension

3 Temperature coefficient of resistance

	-(Dash)	Standard
4 Rated resistance		Example;

1202 4digit. 1202→12kΩ

5 Tolerance on rated resistance

F	±1%
٦	±5%

6 Packaging form

В	Bulk (loose package)	
TP	Paper taping	
TE	Embossed taping	

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3. Rating

3.1 The ratings shall be in accordance with Table-1.

Table-1

Style	Rated dissipation (W)		ture coefficient of nce (10°/°C)	Rated resistance range (Ω)	Preferred number series for resistors	Tolerance on rated resistance
TWMC32	0.75	Standard	+200	1.0~1M	E24, 96	F(±1%)
1 VVIVIC32	0.75	Stariuaru	±200	1.0~1M	E24	J(±5%)
TWMC50	1.0	Standard	±200	1.0~1M	E24, 96	F(±1%)
1 VVIVICOU	1.0	Stariuaru		1.0~1M	E24	J(±5%)
TWMC63	2.0	Standard	1200	1.0~1M	E24, 96	F(±1%)
1 VVIVICOS	∠.∪ Standar	Statitualu	±200	1.0~1M	E24	J(±5%)

Style	Limiting element voltage (V)	Isolation voltage (V)	Category temperature range (°C)
TWMC32			
TWMC50	200	500	<i>–</i> 55∼+155
TWMC63			

3.2 Climatic category

55/155/56 Lower category temperature -55°C

Upper category temperature +155 °C Duration of the damp heat, steady state test 56days

3.3 Stability class

5% Limits for change of resistance:

> $\pm (5\% + 0.1\Omega)$ -for long-term tests -for short-term tests $\pm (1\% + 0.05\Omega)$

3.4 Derating

The derated values of dissipation at temperature in excess of 70 °C shall be as indicated by the following curve.

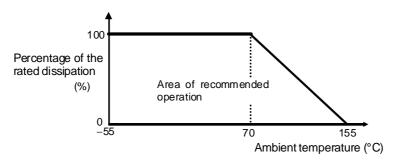


Figure-1 Derating curve

I Init: mm

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3.5 Rated voltage

d. c. or a. c. r. m. s. voltage calculated from the square root of the product of the rated resistance and the rated dissipation.

E : Rated voltage (V)

E =
$$P \cdot R$$

P : Rated dissipation (W)

R : Rated resistance (Ω)

Limiting element voltage can only be applied to resistors when the resistance value is equal to or higher than the critical resistance value.

At high value of resistance, the rated voltage may not be applicable.

4. Packaging form

The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	Packaging form		Standard packaging quantity / units	Application	
В	Bulk (loose package)		1,000 pcs.	TWMC32.50,63	
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	TWMC32	
TE	Embossed taping	12mm width, 4mm pitches	4,000 pcs.	TWMC50,63	

5. Dimensions

5.1 The resistor shall be of the design and physical dimensions in accordance with Figure-2 and Table-3.

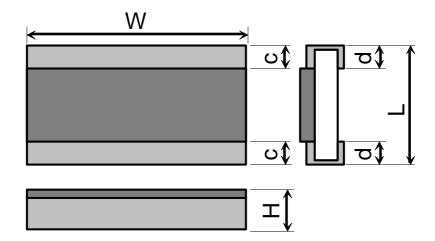


Figure 2

	iable—3				Offic. Ithiri
Style	L	W	Н	С	d
TWMC32	1.6±0.2	3.2±0.2	0.55±0.10	0.5±0.25	0.5±0.25
TWMC50	2.5±0.15	5.0±0.2	0.55±0.10	0.6±0.2	0.6±0.2
TWMC63	3.2±0.2	6.3±0.2	0.55±0.10	0.6±0.2	0.6±0.2

5.2 Net weight (Reference)

	OIL THOUTHOUGHT (I TOTOTOTIOO)			
Style		Net weight(mg)		
	TWMC32	9		
	TWMC50	25		
	TWMC63	40		

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6. Marking

The nominal resistance shall be marked in 4 digits and marked on over coat side.

Marking example	Contents	Application
1202	$120 \times 10^2 [\Omega] \rightarrow 12 [k\Omega]$	E24
2R20	2.2 [Ω]	Less than 100Ω of E24
5623	$562\times10^3 [\Omega] \rightarrow 562[k\Omega]$	E96
12R7	12.7 [Ω]	Less than 100Ω of E96

7. Performance

- 7.1 The standard condition for tests shall be in accordance with Sub-clause 4.2, JIS C 5201–1: 2011.
- 7.2 The performance shall be satisfied in Table-4.

Table-4(1)

No.	Test items	Condition of test (JIS C 5201–1)	Performance requirements
1	Visual examination	Sub-clause 4.4.1 Checked by visual examination.	As in 4.4.1 The marking shall be legible, as checked by visual examination.
2	Dimension	Sub-clause 4.4.2	As specified in Table-3 of this specification.
	Resistance	Sub-clause 4.5	As in 4.5.2 The resistance value shall correspond with the rated resistance taking into account the specified tolerance.
3	Voltage proof	Sub-clause 4.7 Method: 4.6.1.4 Test voltage: Alternating voltage with a peak value of 1.42 times the insulation voltage. Duration: 60 s ± 5 s Insulation resistance Test voltage: Insulation voltage Duration: 1 min.	No breakdown or flash over $R \geq 1 \; G \; \Omega$
4	Solderability	Sub-clause 4.17 Without ageing Flux: The resistors shall be immersed in a non-activated soldering flux for 2s. Bath temperature: 245 °C ±5 °C Immersion time: 2 s ± 0.5 s	As in 4.17.4.5 The terminations shall be covered with a smooth and bright solder coating.
5	Overload (in the mounted state) Solvent resistance of the marking	Sub-clause 4.31 Substrate material: Epoxide woven glass Sub-clause 4.13 The applied voltage shall be 5 times the rated dissipation or twice the limiting element voltage, whichever is the less severe. Duration: 2 s Visual examination Resistance Sub-clause 4.30 Solvent: 2-propanol Solvent temperature: 23 °C ± 5 °C Method 1 Rubbing material: cotton wool Without recovery	No visible damage $\Delta R \le \pm (1\% + 0.05\Omega)$ Legible marking



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Table-4(2)

No	Test items	Condition of test (JIS C 5201–1)	Performance requirements
6	Mounting	Sub-clause 4.31	
		Substrate material: Epoxide woven glass	
	Bound strength of the end face	Sub-clause 4.33	
	plating	Bent value: TWMC32: 3mm	
		TWMC50,63: 1 mm	
		Resistance	$\Delta R \le \pm (1\% + 0.05\Omega)$
	Final measurements	Sub-clause 4.33.6	No visible damage
		Visual examination	
7	Resistance to soldering heat	Sub-clause 4.18	
		Solder temperature: 260 °C ± 5 °C	
		Immersion time: 10 s ± 0.5 s	
		Visual examination	As in 4.18.3.4
			No sign of damage such as cracks.
		Resistance	$\Delta R \le \pm (1\% + 0.05\Omega)$
	Component solvent resistance	Sub-clause 4.29	
		Solvent: 2-propanol	
		Solvent temperature: 23 °C ± 5 °C	
		Method 2	
		Recovery: 48 h	Nie delle de se ese
		Visual examination	No visible damage
	1.0	Resistance	$\Delta R \le \pm (1\% + 0.05\Omega)$
8	Mounting	Sub-clause 4.31	
		Substrate material: Epoxide woven glass	
	A alla aciona	Test substrate: Figure–3	
	Adhesion	Sub-clause 4.32	
		Force: 5 N	
		Duration: 10 s ± 1 s	No visible damage
	Rapid change temperature	Visual examination	No visible damage
	rapid change temperature	Sub-clause 4.19	
		Lower category temperature: –55 °C	
		Upper category temperature: +155 °C	
		Duration of exposure at each temperature: 30	
		min.	
		Number of cycles: 5 cycles. Visual examination	No visible damage
			$\Delta R \le \pm (1\% + 0.05\Omega)$
		Resistance	

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Table-4(3)

No	Test items	Condition of test (JIS C 5201–1)	Performance requirements
9	Climatic sequence	Sub-clause 4.23	
	-Dry heat	Sub-clause 4.23.2	
	,	Test temperature: +155 °C	
		Duration: 16 h	
	-Damp heat, cycle	Sub-clause 4.23.3	
	(12+12hour cycle)	Test method: 2	
	First cycle	Test temperature: 55 °C	
		[Severity(2)]	
	-Cold	Sub-clause 4.23.4	
		Test temperature –55 °C	
		Duration: 2h	
	-Damp heat, cycle	Sub-clause 4.23.6	
	(12+12hour cycle)	Test method: 2	
	Remaining cycle	Test temperature: 55 °C	
		[Severity (2)]	
		Number of cycles: 5 cycles	
	-D.C. load	Sub-clause 4.23.7	
		The applied voltage shall be the rated voltage	
		or the limiting element voltage whichever is	
		the smaller.	
		Duration: 1 min. Visual examination	No visible damage
		Resistance	$\Delta R \leq \pm (5\% + 0.1\Omega)$
10	Mounting	Sub-clause 4.31	
	i waa ii ig	Substrate material: Epoxide woven glass	
	Endurance at 70 °C	Sub-clause 4.25.1	
		Ambient temperature: 70 °C ± 2 °C	
		Duration: 1000 h	
		The voltage shall be applied in cycles of 1.5 h	
		on and 0.5 h off.	
		The applied voltage shall be the rated voltage	
		or the limiting element voltage whichever is	
		the smaller.	
		Examination at 48 h, 500 h and	
		1000 h:	Nie viellele deue eus
		Visual examination	No visible damage
44	BA	Resistance	$\Delta R \le \pm (5\% + 0.1\Omega)$
11	Mounting	Sub-clause 4.31	
	Variation of resistance with	Substrate material: Epoxide woven glass	As in Table–1
	temperature	Sub-clause 4.8	AS III TADIE-T
	i temperature	-55 °C / +20 °C	
		+20 °C / +155°C	

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Table-4(4)

No	Test items	Condition of test (JIS C 5201–1)	Performance requirements
12	Mounting Sub-clause 4.31 Substrate material: Epoxide woven glass Sub-clause 4.24 Ambient temperature: 40 °C ± 2 °C Relative humidity: 93 ½ % a) 1st group: without voltage applied. b) 2nd group: The d. c. voltage shall be applied continuously.		1 enormance requirements
		The voltage shall be accordance with Sub-clause 4.24.2.1 b). without polarizing voltage [4.24.2.1, c)] Visual examination Resistance	No visible damage Legible marking $\Delta R \le \pm (5\% + 0.1\Omega)$
13 Dimensions (detail) Mounting Endurance at upper category temperature		Sub-clause 4.4.3 Sub-clause 4.31 Substrate material: Epoxide woven glass Sub-clause 4.25.3 Ambient temperature:155 °C ± 2 °C Duration: 1000 h Examination at 48 h, 500 h and 1000 h: Visual examination Resistance	As in Table–3 No visible damage $\Delta R \le \pm (5\%+0.1\Omega)$

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8. Taping

- 8.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010
- 8.2 Taping dimensions
- 8.2.1 Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.

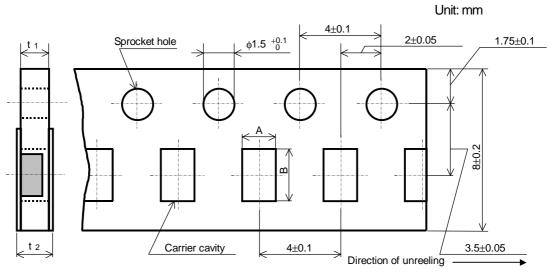


Figure-3									
		Unit: mm							
Style	Α	В	t 1	t 2					
TWMC32	2.00±0.15	3.6±0.2	0.8±0.1	1.0max.					

8.2.2 Embossed taping dimensions shall be in accordance with Figure-4 and Table-6.

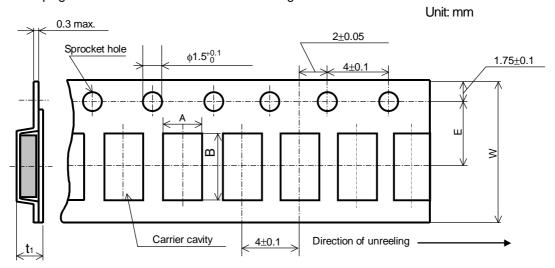


Figure-4

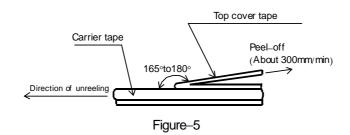
	Unit: mm				
Style	Α	В	W	Е	t 1
TWMC50	3.1±0.2	5.5±0.2	12.0±0.3	5.5±0.05	1.1±0.15
TWMC63	3.6±0.2	6.9±0.2			

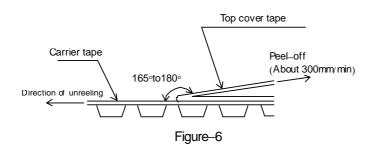
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- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following TWMC32:Figure–5,TWMC50,63: Figure–6.
- 6). When the tape is bent with the minimum radius for 30 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing.
 The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The resistors shall be faced to upward at the over coating side in the carrier cavity.



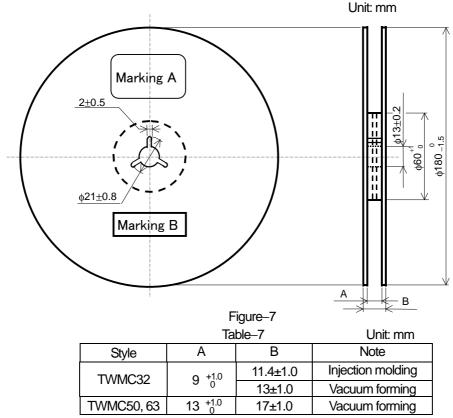


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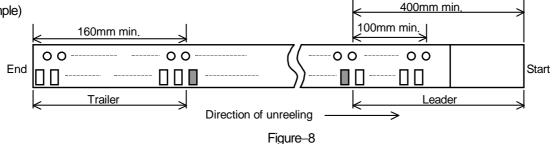
8.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure–7 and Table–7. Plastic reel (Based on EIAJ ET–7200C)



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

8.4 Leader and trailer tape.(Example)



9. Marking on package

The label of a minimum package shall be legibly marked with follows.

9.1 Marking A

(1) Classification

(Style, Temperature coefficient of resistance, Rated resistance, Tolerance on rated resistance, Packaging form)

(2) Quantity (3) Lot number (4) Manufacturer's name or trade mark (5) Others

9.2 Marking B (KAMAYA Control label)