



**JMSL0302AG**

## 30V 1.3mΩ N-Ch Power MOSFET

## Features

- Ultra-low  $R_{DS(ON)}$
  - High Current Capability
  - ESD Enhanced to HBM Rating up to 1.5kV
  - 100% UIS Tested, 100%  $R_g$  Tested

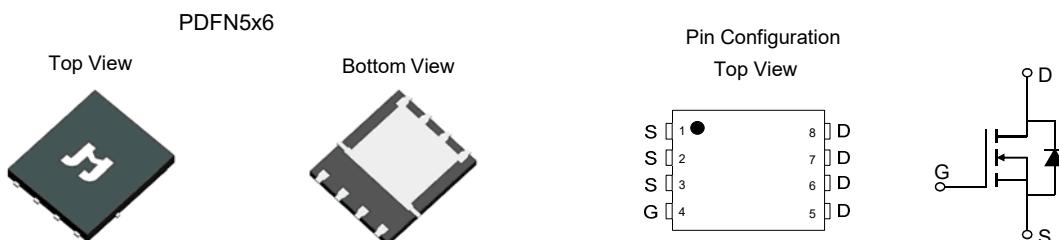
## Applications

- Power Management in Computing, CE, IE 4.0, Communications
  - Current Switching in DC/DC & AC/DC Sub-systems
  - Motor Driving, Quick/Wireless Charging

Product Summary		
Parameter	Typ.	Unit
$V_{DS}$	30	V
$V_{GS(th)}$	1.7	V
$I_D$ (@ $V_{GS} = 10V$ ) <sup>(1)</sup>	178	A
$R_{DS(ON)}$ (@ $V_{GS} = 10V$ )	1.3	mΩ
$R_{DS(ON)}$ (@ $V_{GS} = 4.5V$ )	2.0	mΩ

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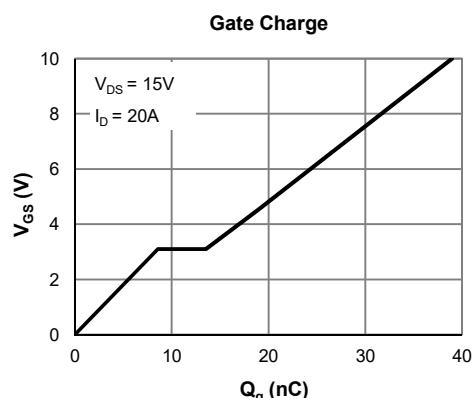
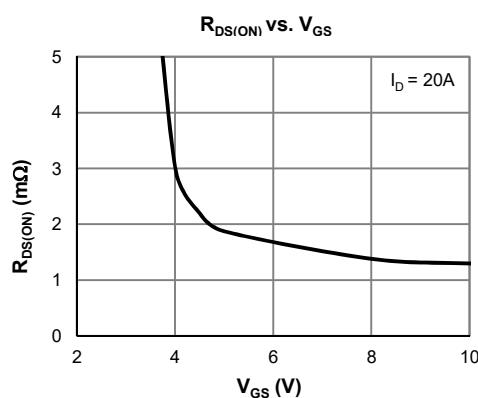


## **Ordering Information**

Device	Package	# of Pins	Marking	MSL	T <sub>J</sub> (°C)	Media	Quantity (pcs)
JMSL0302AG-13	PDFN5x6	8	SL0302A	1	-55 to 150	13-inch Reel	3000

### Absolute Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DS}$	30	V
Gate-to-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>(1)</sup>	$T_C = 25^\circ C$	$I_D$	178
	$T_C = 100^\circ C$		110
Continuous Drain Current <sup>(6)</sup>	$T_C = 25^\circ C$	$I_D$	100
Pulsed Drain Current <sup>(2)</sup>	$I_{DM}$	485	A
Avalanche Current <sup>(3)</sup>	$I_{AS}$	45	A
Avalanche Energy <sup>(3)</sup>	$E_{AS}$	101	mJ
Power Dissipation <sup>(4)</sup>	$T_C = 25^\circ C$	$P_D$	78
	$T_C = 100^\circ C$		31
Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C



**Electrical Characteristics (@  $T_J = 25^\circ\text{C}$  unless otherwise specified)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$I_D = 1\text{mA}, V_{GS} = 0\text{V}$	30			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$			1.0 5.0	$\mu\text{A}$
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.7	2.5	V
Static Drain-Source ON-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$ $V_{GS} = 4.5\text{V}, I_D = 15\text{A}$		1.3 2.0	1.6 2.9	$\text{m}\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS} = 5\text{V}, I_D = 20\text{A}$		100		S
Diode Forward Voltage	$V_{SD}$	$I_S = 1\text{A}, V_{GS} = 0\text{V}$		0.68	1.0	V
Diode Continuous Current	$I_S$	$T_C = 25^\circ\text{C}$			78	A
<b>DYNAMIC PARAMETERS<sup>(5)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V}, f = 1\text{MHz}$		2975		pF
Output Capacitance	$C_{oss}$			2650		pF
Reverse Transfer Capacitance	$C_{rss}$			117		pF
Gate Resistance	$R_g$	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$		1.4		$\Omega$
<b>SWITCHING PARAMETERS<sup>(5)</sup></b>						
Total Gate Charge (@ $V_{GS} = 10\text{V}$ )	$Q_g$	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 15\text{V}, I_D = 20\text{A}$		39		nC
Total Gate Charge (@ $V_{GS} = 4.5\text{V}$ )	$Q_g$			19.0		nC
Gate Source Charge	$Q_{gs}$			8.6		nC
Gate Drain Charge	$Q_{gd}$			5.0		nC
Turn-On DelayTime	$t_{D(\text{on})}$	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}$ $R_L = 0.75\Omega, R_{\text{GEN}} = 3\Omega$		6.0		ns
Turn-On Rise Time	$t_r$			9.0		ns
Turn-Off DelayTime	$t_{D(\text{off})}$			26		ns
Turn-Off Fall Time	$t_f$			10.0		ns
Body Diode Reverse Recovery Time	$t_{rr}$		$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	51		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		57		nC

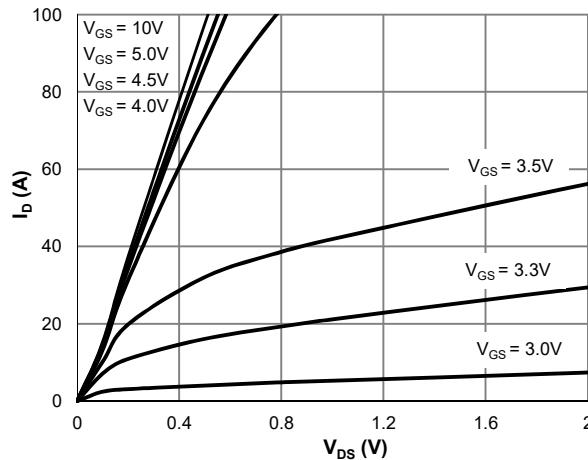
**Thermal Performance**

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	50	65	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.2	1.6	$^\circ\text{C/W}$

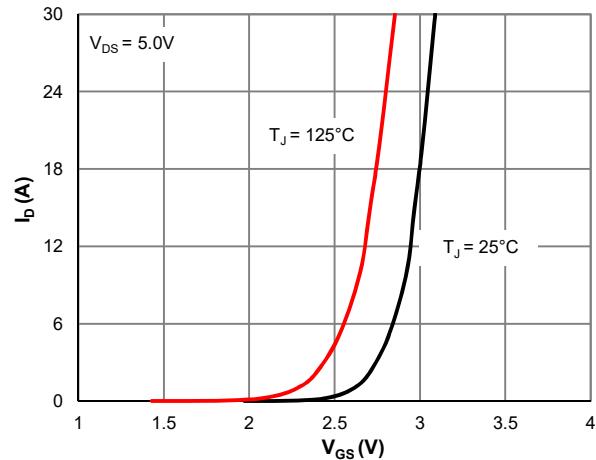
**Notes:**

1. Computed continuous current assumes the condition of  $T_{J_{\text{Max}}}$  while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under  $T_{J_{\text{Max}}} = 150^\circ\text{C}$ .
3. This single-pulse measurement was taken under the following condition [ $L = 100\mu\text{H}, V_{GS} = 10\text{V}, V_{DS} = 30\text{V}$ ] while its value is limited by  $T_{J_{\text{Max}}} = 150^\circ\text{C}$ .
4. The power dissipation  $P_D$  is based on  $T_{J_{\text{Max}}} = 150^\circ\text{C}$ .
5. This value is guaranteed by design hence it is not included in the production test.
6. Continuous current rating is limited by the package used.

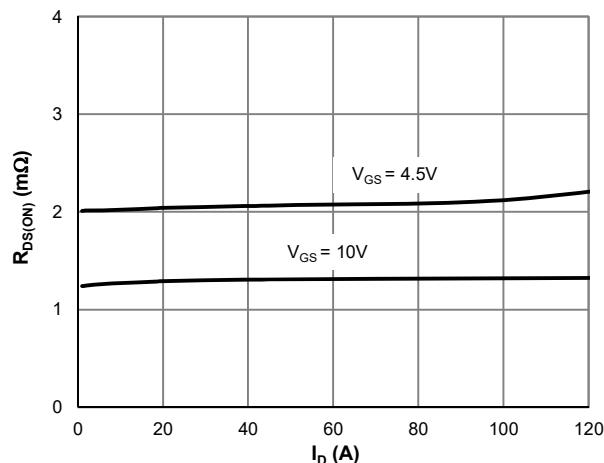
### Typical Electrical & Thermal Characteristics



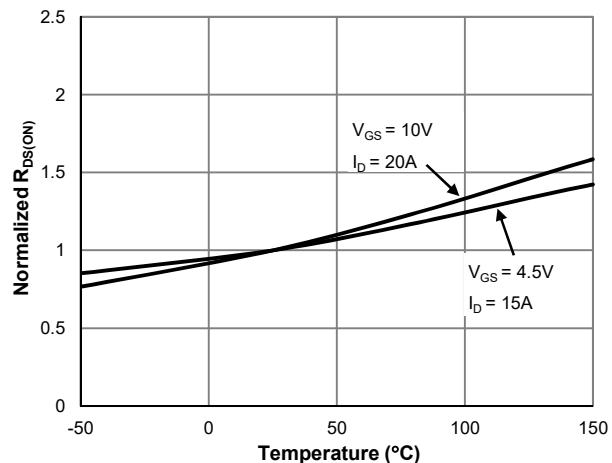
**Figure 1: Saturation Characteristics**



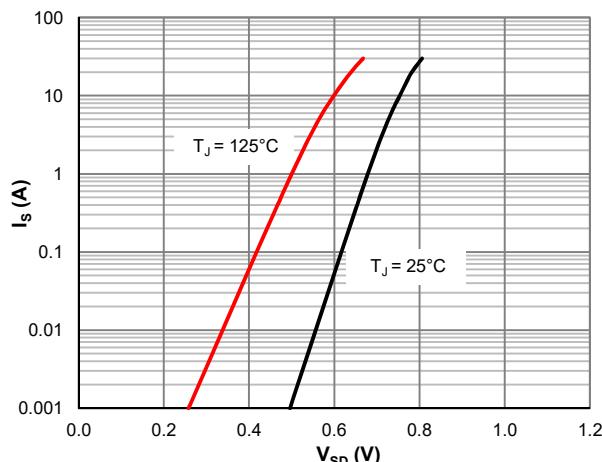
**Figure 2: Transfer Characteristics**



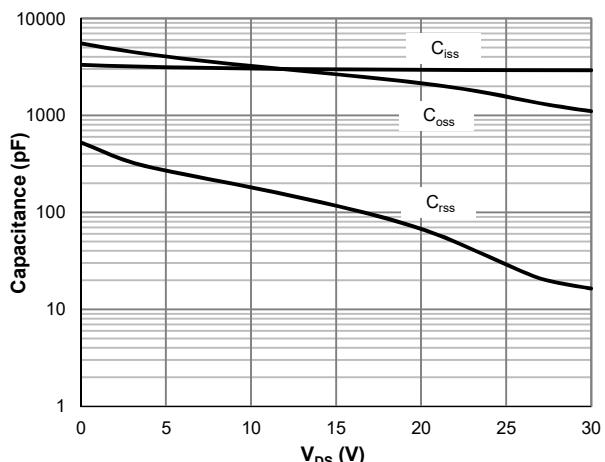
**Figure 3:  $R_{DS(ON)}$  vs. Drain Current**



**Figure 4:  $R_{DS(ON)}$  vs. Junction Temperature**



**Figure 5: Body-Diode Characteristics**



**Figure 6: Capacitance Characteristics**

### Typical Electrical & Thermal Characteristics

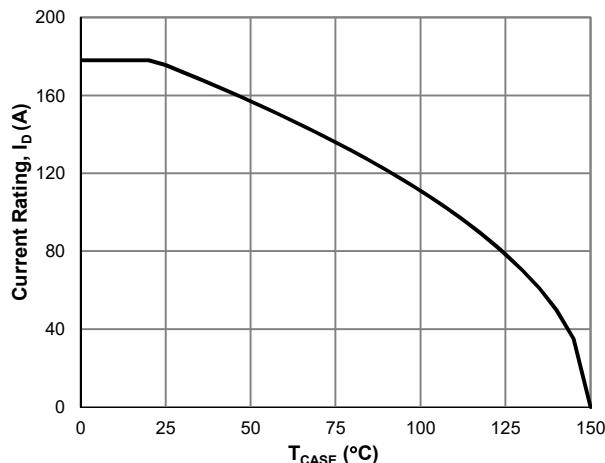


Figure 7: Current De-rating

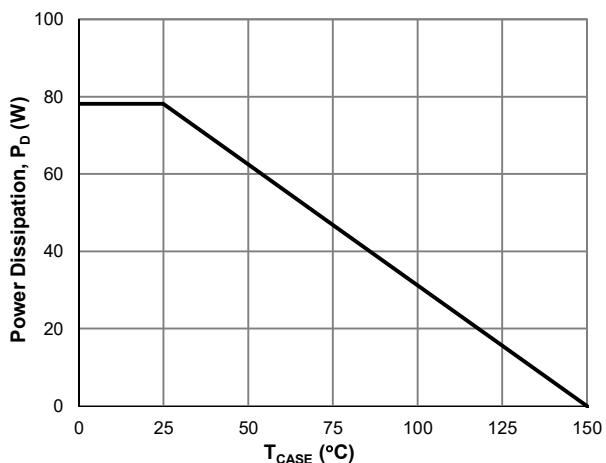


Figure 8: Power De-rating

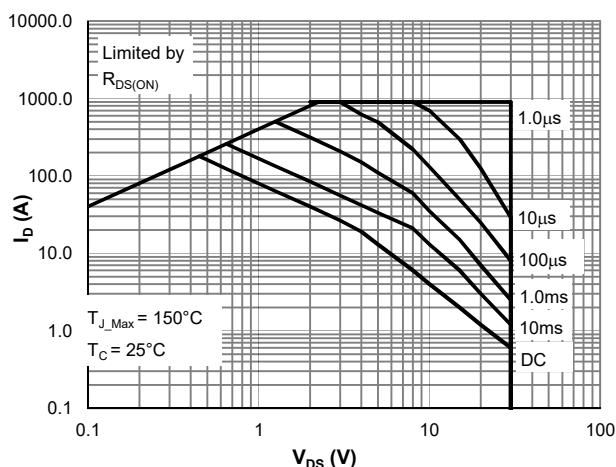


Figure 9: Maximum Safe Operating Area

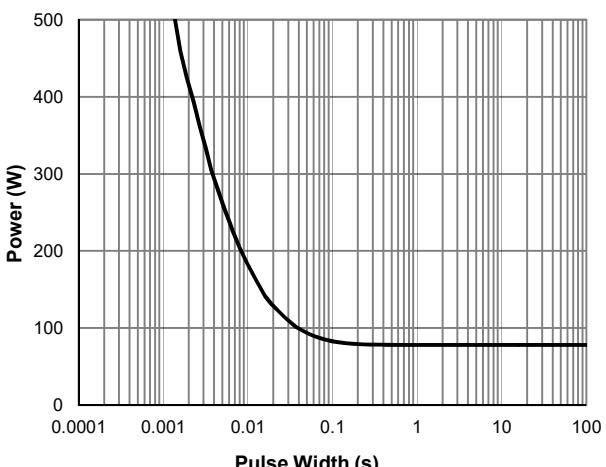


Figure 10: Single Pulse Power Rating, Junction-to-Case

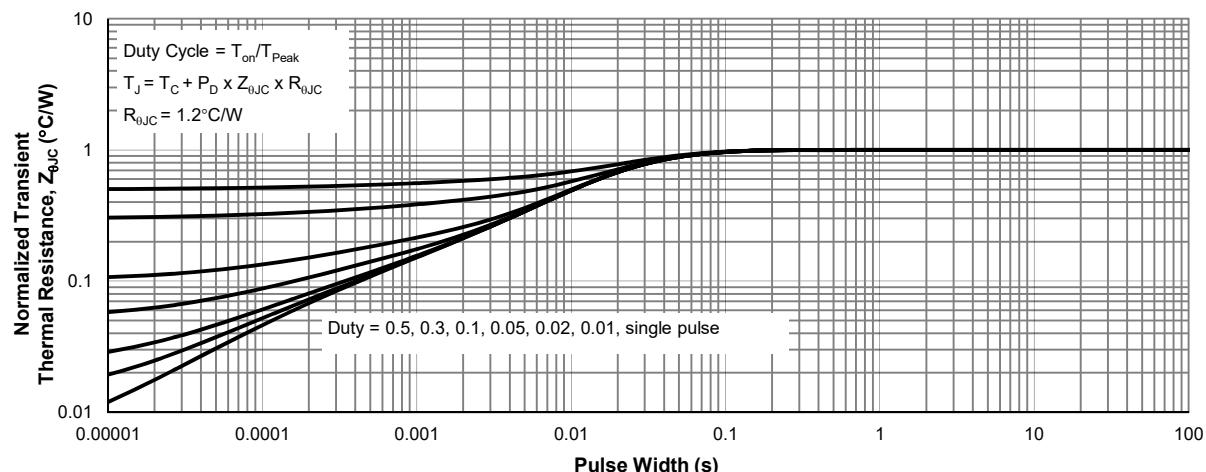
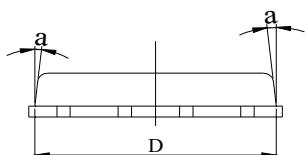
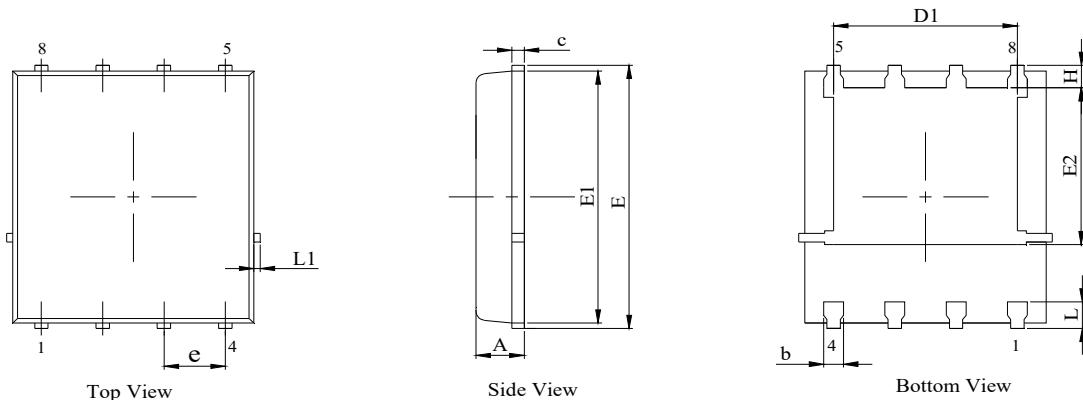


Figure 11: Normalized Maximum Transient Thermal Impedance

### PDFN5x6 Package Information

#### Package Outline



#### NOTES:

1. Dimension and tolerance per ASME Y14.5M, 1994.
2. All dimensions in millimeter (angle in degree).
3. Dimensions D and E1 do not include mold flash protrusions or gate burrs.

DIM.	MILLIMETER	
	MIN.	MAX.
A	0.90	1.20
b	0.33	0.51
c	0.23	0.33
D	4.80	5.40
D1	3.61	4.25
E	5.90	6.30
E1	5.55	5.95
E2	3.35	3.95
e	1.27 BSC	
H	0.41	0.80
L	0.51	0.80
L1	-	0.15
a	0°	12°

#### Recommended Footprint

