



JMSH1004AC  
JMSH1004AE

## 100V 3.0mΩ N-Ch Power MOSFET

### Features

- Ultra-low  $R_{DS(ON)}$
- Low Gate Charge
- High Current Capability
- 100% UIS Tested, 100%  $R_g$  Tested

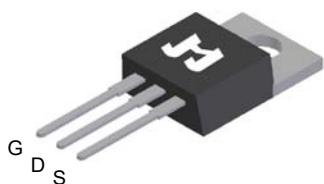
### Product Summary

Parameter	Typ.	Unit
$V_{DS}$	100	V
$V_{GS(th)}$	2.7	V
$I_D (@ V_{GS} = 10V)^{(1)}$	190	A
$R_{DS(ON)} (@ V_{GS} = 10V)$	3.0	mΩ

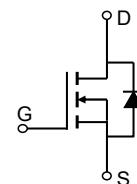
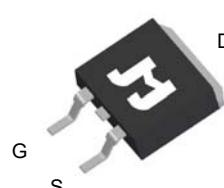
### Applications

- Power Management in Telecom., Industrial Automation, CE
- Current Switching in DC/DC & AC/DC (SR) Sub-systems
- Motor Driving in Power Tool, E-vehicle, Robotics

TO220-3L Top View



TO263-3L Top View



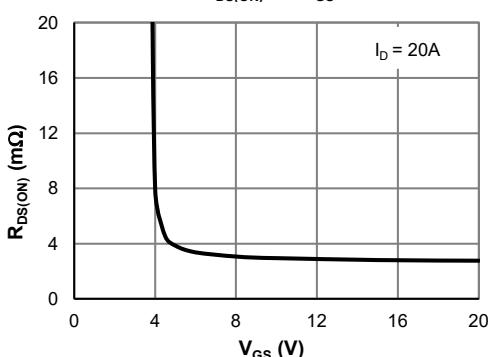
### Ordering Information

Device	Package	# of Pins	Marking	MSL	$T_J$ (°C)	Media	Quantity (pcs)
JMSH1004AC-U	TO220-3L	3	SH1004A	N/A	-55 to 175	Tube	50
JMSH1004AE-13	TO263-3L	3	SH1004A	3	-55 to 175	13-inch Reel	800

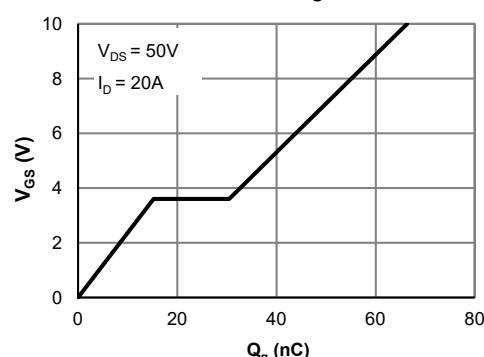
### Absolute Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value		Unit
Drain-to-Source Voltage	$V_{DS}$	100		V
Gate-to-Source Voltage	$V_{GS}$	$\pm 20$		V
Continuous Drain Current (1)	$I_D$	190		A
		134		
Continuous Drain Current (6)	$I_D$	120		A
Pulsed Drain Current (2)	$I_{DM}$	531		A
Avalanche Current (3)	$I_{AS}$	70		A
Avalanche Energy (3)	$E_{AS}$	245		mJ
Power Dissipation (4)	$P_D$	250		W
		125		
Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 175		°C

$R_{DS(ON)}$  vs.  $V_{GS}$



Gate Charge





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**Electrical Characteristics (@  $T_J = 25^\circ\text{C}$  unless otherwise specified)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	100			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$			1.0 5.0	$\mu\text{A}$
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.0	2.7	4.0	V
Static Drain-Source ON-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$		3.0	3.6	$\text{m}\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS} = 5\text{V}, I_D = 20\text{A}$		85		S
Diode Forward Voltage	$V_{SD}$	$I_S = 1\text{A}, V_{GS} = 0\text{V}$		0.71	1.0	V
Diode Continuous Current	$I_S$	$T_C = 25^\circ\text{C}$			250	A
<b>DYNAMIC PARAMETERS<sup>(5)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{V}, V_{DS} = 50\text{V}, f = 1\text{MHz}$		4398		pF
Output Capacitance	$C_{oss}$			1361		pF
Reverse Transfer Capacitance	$C_{rss}$			8.5		pF
Gate Resistance	$R_g$	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$		2.5		$\Omega$
<b>SWITCHING PARAMETERS<sup>(5)</sup></b>						
Total Gate Charge (@ $V_{GS} = 10\text{V}$ )	$Q_g$	$V_{GS} = 0$ to $10\text{V}$ $V_{DS} = 50\text{V}, I_D = 20\text{A}$		66		nC
Total Gate Charge (@ $V_{GS} = 6.0\text{V}$ )	$Q_g$			44		nC
Gate Source Charge	$Q_{gs}$			15.2		nC
Gate Drain Charge	$Q_{gd}$			15.2		nC
Turn-On Delay Time	$t_{D(\text{on})}$	$V_{GS} = 10\text{V}, V_{DS} = 50\text{V}$ $R_L = 2.5\Omega, R_{\text{GEN}} = 6\Omega$		17.2		ns
Turn-On Rise Time	$t_r$			37		ns
Turn-Off Delay Time	$t_{D(\text{off})}$			68		ns
Turn-Off Fall Time	$t_f$			61		ns
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		94		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		242		nC

**Thermal Performance**

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	45	55	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.40	0.60	$^\circ\text{C/W}$

**Notes:**

1. Computed continuous current assumes the condition of  $T_{J,\text{Max}}$  while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under  $T_{J,\text{Max}} = 175^\circ\text{C}$ .
3. This single-pulse measurement was taken under the following condition [ $L = 100\mu\text{H}, V_{GS} = 10\text{V}, V_{DS} = 50\text{V}$ ] while its value is limited by  $T_{J,\text{Max}} = 175^\circ\text{C}$ .
4. The power dissipation  $P_D$  is based on  $T_{J,\text{Max}} = 175^\circ\text{C}$ .
5. This value is guaranteed by design hence it is not included in the production test.
6. Continuous current rating is limited by the package used.

### Typical Electrical & Thermal Characteristics

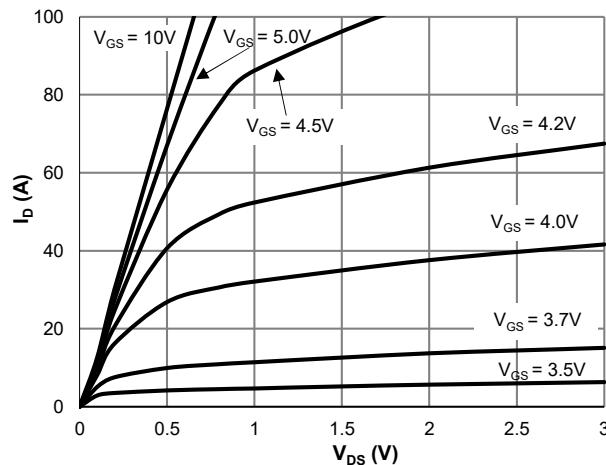


Figure 1: Saturation Characteristics

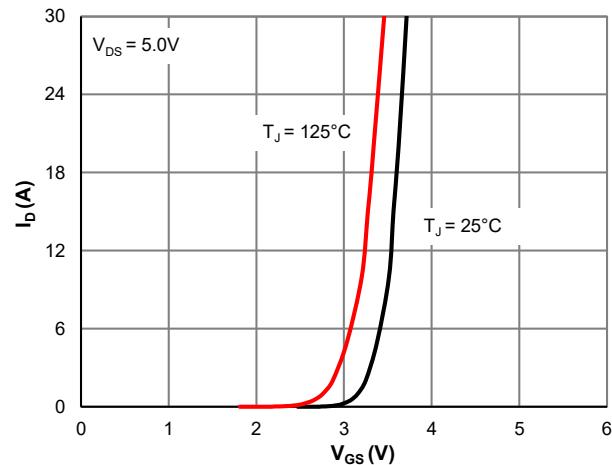


Figure 2: Transfer Characteristics

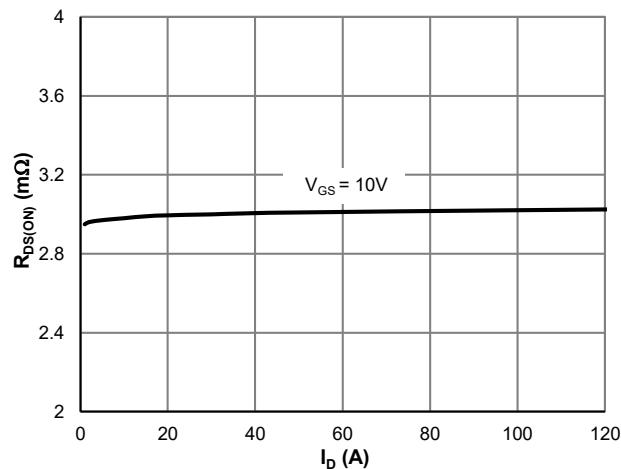


Figure 3:  $R_{DS(ON)}$  vs. Drain Current

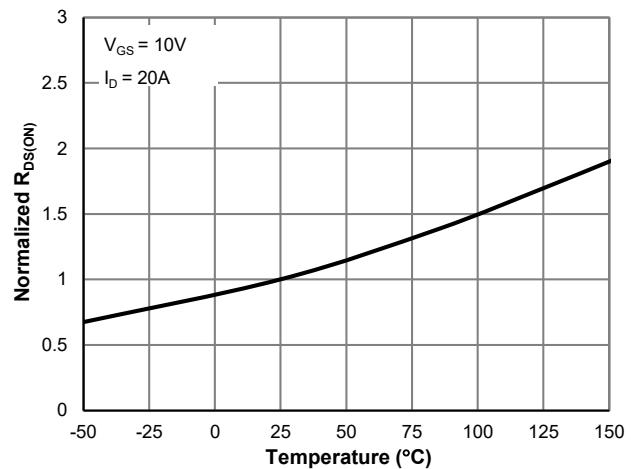


Figure 4:  $R_{DS(ON)}$  vs. Junction Temperature

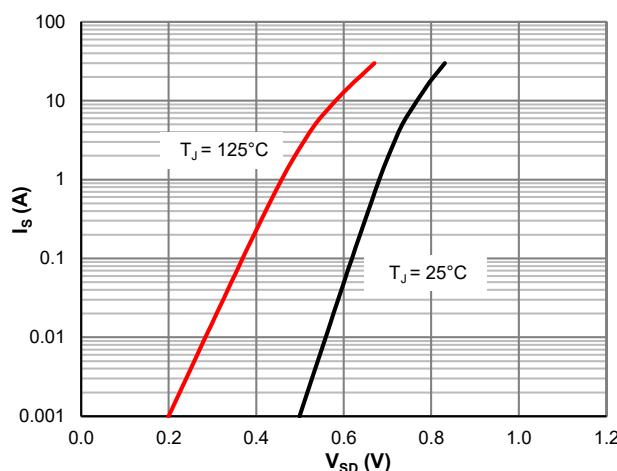


Figure 5: Body-Diode Characteristics

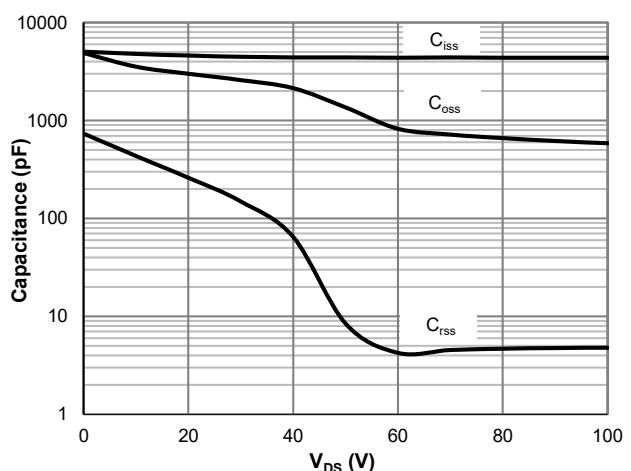


Figure 6: Capacitance Characteristics

### Typical Electrical & Thermal Characteristics

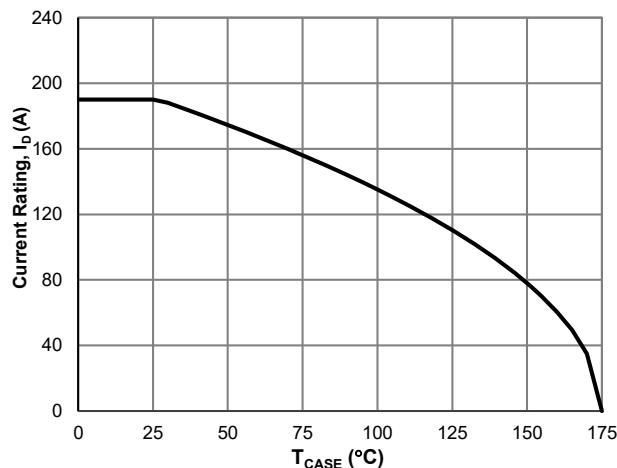


Figure 7: Current De-rating

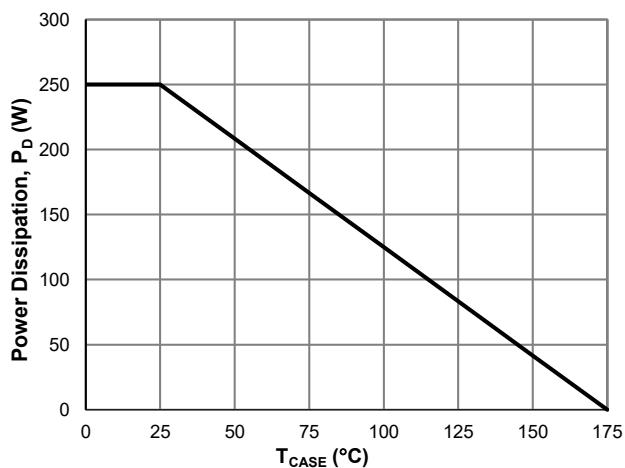


Figure 8: Power De-rating

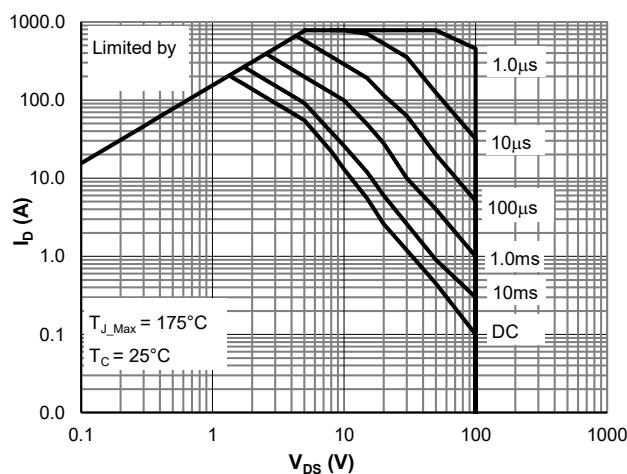


Figure 9: Maximum Safe Operating

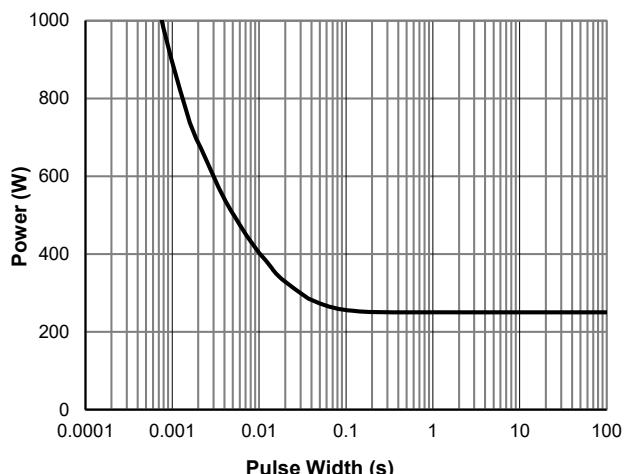


Figure 10: Single Pulse Power Rating, Junction-to-Case

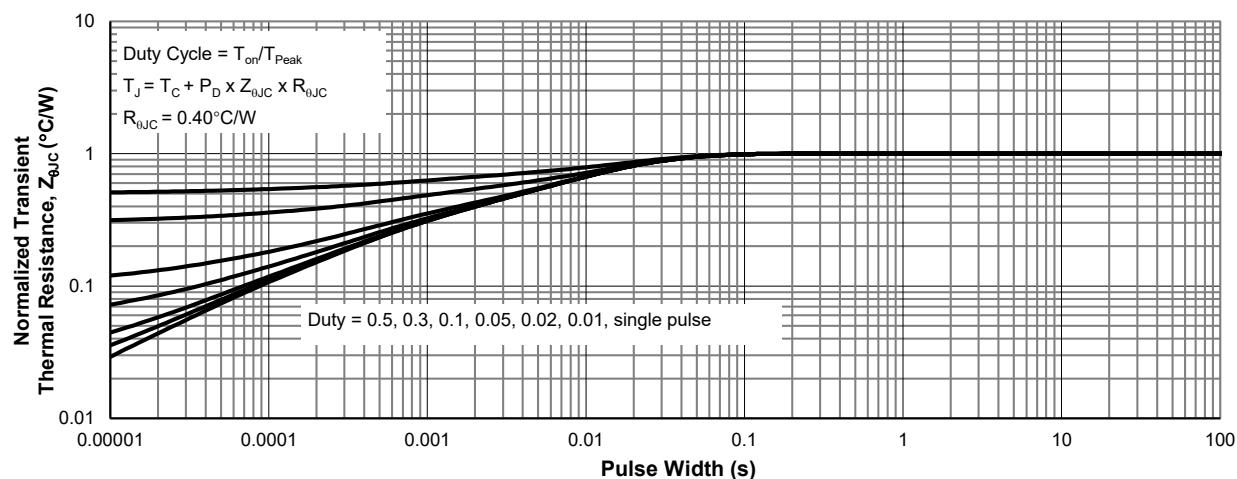


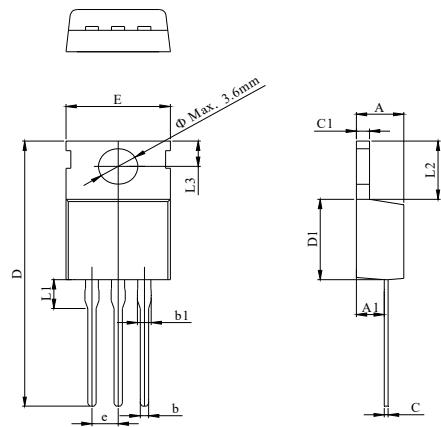
Figure 11: Normalized Maximum Transient Thermal Impedance



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## **TO220-3L Package Information**

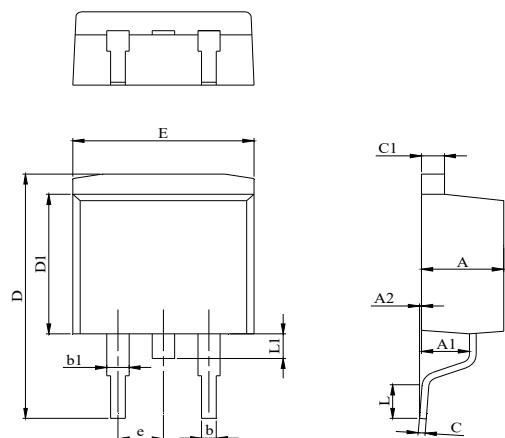
## Package Outline



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	4.24		4.70
A1	2.20		3.00
b	0.70		0.95
b1	1.14		1.70
C	0.40		0.60
C1	1.15		1.40
D	28.00		29.80
D1	8.80		9.90
E	9.70		10.50
L1			3.80
L2	6.25		6.90
L3	2.40		3.00
e		2.54 BSC	

## **TO263-3L Package Information**

## Package Outline



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	4.24		4.77
A1	2.30		2.89
A2	0.00	0.10	0.25
b	0.70		0.96
b1	1.17		1.70
C	0.30		0.60
C1	1.15		1.42
D	14.10		15.88
D1	8.50		9.60
E	9.78		10.36
L	1.78		2.79
L1			1.75
e		2.54	

## **Recommended Footprint**

