

Specification for Approval

Date: 2021/08/17

Customer :	天诚科技
TAI-TECH P/N:	TMPC0503H-150MG-D
CUSTOMER P/N:	
DESCRIPTION:	
QUANTITY:	

REMARK:													
Customer Approval Feedback													

西北臺慶科技股份有限公司 TAI-TECH Advanced Electronics Co.. Ltd

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SMD Power Choke Coil

TMPC0503H-150MG-D

1. Features

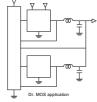
- 1. Carbonyl powder inductor.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb) & Halogen-Free and RoHS compliant.



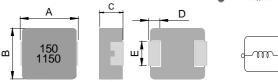


2. Applications

Note PC power system , incl. IMVP-6 DC/DC converter.

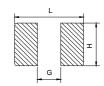


3. Dimensions



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	
TMPC0503H	5.7±0.3	5.2±0.2	2.8±0.2	1.1±0.3	1.5±0.2	

Recommend PC Board Pattern



L(mm)	G(mm)	H(mm)		
6.5	2.5	1.8		

Note: 1. The above PCB layout reference only.
2. Recommend solder paste thickness at
0.12mm and above.

4. Part Numbering



A: Series

B: Dimension BxC

C: Type Carbonyl powder
D: Inductance 150=15uH
E: Inductance Tolerance M=±20%

F: D/C 印字:黑色; 150 及 D/C 1150 (D/C 前二碼是年份,後二碼是週期,依實際生產週期而定)

5. Specification

Part Number	Inductance	l rms	I sat	DCR	DCR	
	L0 (uH) @ 0 A	(A) typ.	(A) typ.	(mΩ) typ. @25℃	(mΩ) max. @25℃	
TMPC0503H-150MG-D	15.0	2.1	2.6	165	190	

Note:

- 1. Test frequency: L: 100KHz /1.0V
- $3. \ \ \text{Testing Instrument}: L/Q: HP4284A, CH11025, CH3302, CH1320 \ , CH1320S \ LCR \ \ \text{METER} \ / \ Rdc: CH16502, Agilent 33420A \ MICRO \ OHMMETER.$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\,\Delta t40^{\circ}\!C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 20%
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.

6. Material List



NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

7. Reliability and Test Condition

Item	Performance	Test Condition			
Operating temperature	-40~+125℃ (Including self - temperature rise)				
Storage temperature	110~+40°ℂ,50~60%RH (Product without taping) 240~+125°ℂ (on board)				
Electrical Performance T	est	•			
Inductance		HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.			
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.			
Saturation Current (Isat)	Approximately △L20%	Saturation DC Current (Isat) will cause L0 to drop △L(%)			
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise \(\triangle \triangle \(\triangle \) (\(\triangle \)) (1. Applied the allowed DC current 2. Temperature measured by digital surface thermometer			
Reliability Test					
Life Test		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature: 125±2°C (Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs			
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±2 % R.H., Temperature: 85℃±2℃ Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs			
Low Temperature Unload Life Test		Preconditioning:Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Temperature:-40±2°C Time: 500±8hr. Recovery: 4 to 24hrs of recovery under the standard condition after the removal from test chamber.			
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 4. Keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1–2 hrs.			
Thermal shock		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 125±2°C 30±5min Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs			
Vibration		Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) °			

Item	Performance	Test Condition						
Shock	Appearance : No damage.	Туре	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec		
Onock	Inductance: within±10% of initial value Q: Shall not exceed the specification value.	SMD	50	11	Half-sine	11.3		
	RDC: within ±15% of initial value and shall not exceed the specification value	Lead	50	11	Half-sine	11.3		
Bending		Shall be mounted on a FR4 substrate of the following dimensions: >=0805:40x100x1.2mm <0805:40x100x0.8mm Bending depth: >=0805:1.2mm <0805:0.8mm duration of 10 sec.						
Soderability	More than 95% of the terminal electrode should be covered with solder °	Solder Tempe Flux fo Dip tin Depth:	erature: 245 or lead free: ne: 4±1sec or completely	Ag3% Cu0.5% ±5°C ° Rosin. 9.5% °				
			er of heat cy		Tamaaaatuus			
Resistance to Soldering			mperature ()		Temperature ramp/immers	ion		
Heat			0 ±5(solder np)	10 ±1	25mm/s ±6 m	m/s		
Terminal Strength	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force (>0805:1kg), <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.						

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

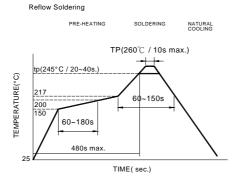
Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

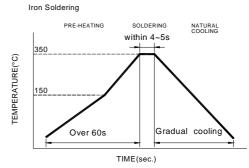
- · Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 355℃ tip temperature (max)
- 1.0mm tip diameter (max)
- · Limit soldering time to 4~5sec.



Reflow times: 3 times max.

Fig.1

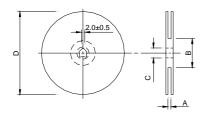


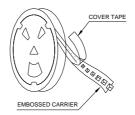
Iron Soldering times: 1 times max.

Fig.2

9. Packaging Information

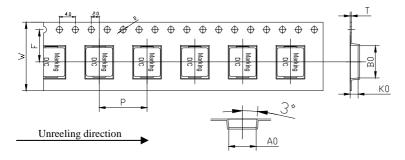
(1) Reel Dimension





Type	A(mm)	B(mm)	C(mm)	D(mm)	
13"x12mm	12.4+2/-0	100±2	13+0.5/-0.2	330	

(2) Tape Dimension

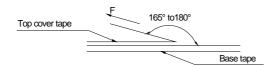


Serie	s S	ize	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
TMP	0	503	6.2±0.1	5.6±0.1	3.3±0.1	8.0±0.1	12.0±0.3	5.5±0.1	0.35±0.05	1.5±0.1

(3) Packaging Quantity

ТМРС	0503	
Chip / Reel	2000	
Inner box	4000	
Carton	16000	

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-C-2003 of 4.11 standard).

Room Temp. (℃)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

- · Storage Conditions(component level)
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.

 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves

