

RS2ABF THRU RS2MBF 2.0 AMP SURFACE MOUNT FAST RECOVERY RECTIFIERS

FEATURES

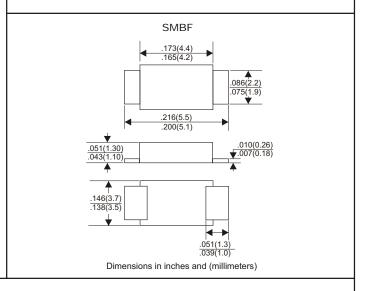
- * Ideal for surface mount applications
- * Easy pick and place
- * Built-in strain relief
- * Fast switching speed

MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-0 rate flame retardant
- * Metallurgically bonded construction
- * Polarity: Color band denotes cathode end
- * Mounting position: Any

VOLTAGE RANGE 50 to 1000 Volts CURRENT

2.0 Ampere



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwies specified. Single phase half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

TYPE NUMBER	RS2ABF	RS2BBF	RS2DBF	RS2GBF	RS2JBF	RS2KBF	RS2MBF	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current								
at Ta=90°C		2.0						
Peak Forward Surge Current, 8.3 ms single half sine-wave								
superimposed on rated load (JEDEC method)		50						Α
Maximum Instantaneous Forward Voltage at 2.0A		1.3					V	
Maximum DC Reverse Current Ta=25°C		5.0						
at Rated DC Blocking Voltage Ta=125°C		300						
Maximum Reverse Recovery Time (Note 1)		150 250 500				nS		
Typical Junction Capacitance (Note 2)		50						pF
Operating and Storage Temperature Range TJ, TsTG		-65—+150						

NOTES:

- 1. Reverse Recovery Time test condition: IF=0.5A, IR=1.0A, IRR=0.25A
- 2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

REV 1.0 2015 JAN PAGE:1/2

RATING AND CHARACTERISTIC CURVES (RS2ABF THRU RS2MBF)

FIG.1-TYPICAL FORWARD

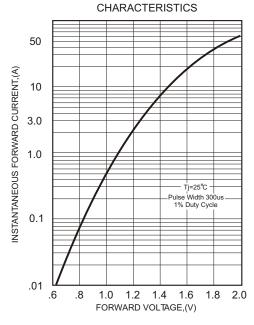
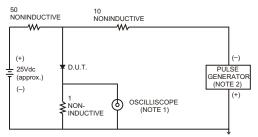


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF

2. Rise Time= 10ns max.. Source Impedance= 50 ohms

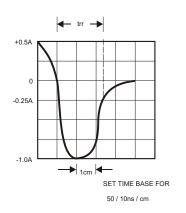


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

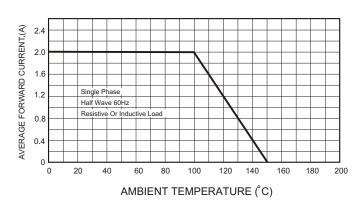


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

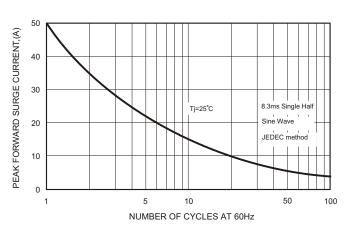


FIG.5-TYPICAL JUNCTION CAPACITANCE

