

Specification for Approval

Date: 2021/08/16

Customer: 天诚科技

TAI-TECH P/N: TMPA1265SP-330MN-D

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

REMARK:	
	Customer Approval Feedback
	西北臺慶科技股份有限公司
	TAI-TECH Advanced Electronics Co., Ltd

Sales Dep.

代理商:

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APPROVED	CHECKED
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R&D Center

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SMD Power Inductor

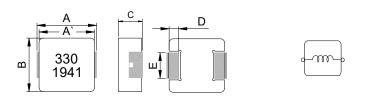
1. Features

- 1. Shielded construction.
- 2. Capable of corresponding high frequency .
- 3. Low loss realized with low DCR.
- 4. High performance (Isat) realized by metal dust core.
- 5. Ultra low buzz noise, due to composite construction.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125°C (Including self temperature rise)

2. Applications

- 1. DC/DC converters in distributed power systems.
- 2. DC/DC converter for Field Programmable Gate Array(FPGA).
- 3. Battery powered devices.
- 4. Thin type on-board power supply module for exchanger.
- 5. VRM for server.
- 6. High current, low profile POL converters.
- 7. PDA/notebook/desktop/server and battery powered devices.

3. Dimensions



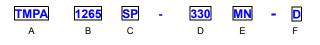
Series	Α	A`	В	С	D	E
TMPA1265	13.5±0.5	12.6±0.3	12.6±0.2	6.2±0.3	2.3±0.3	4.7±0.3

only.

2. Recommend solder paste thickness at 0.15mm and above.

Unit:mm

4. Part Numbering



A: Series	
B: Dimension	BxC.
С: Туре	Standard.
D: Inductance	330=33.0uH
E: Inductance Tolerance	M=±20%
F: Code	Marking: Black.330 and 1941(19 YY, 41 WW,follow production date).

5. Specification

Part Number	Inductance L0 A(uH) ±20%	Heat Rating Current DC Irms (A)		Saturation Current DC I sat (A)		DCR (mΩ)	DCR (mΩ) Max
	±20 %	Тур	Max	Typ Max		Тур	IVIdX
TMPA1265SP-330MN-D	33.0	9.0	6.5	9.0	8.0	46.0	55.0

Note:

1. Test frequency : Ls : 100KHz /1.0V.

2. All test data referenced to $25^\circ\!\mathrm{C}$ ambient.

3. Testing Instrument(or equ) : L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.

4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\, {\it \Delta T}$ of 40 $^\circ \! {\rm C}$

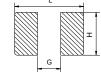
5. Saturation Current (Isat) will cause L0 to drop approximately 30%.

6. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

7. Special inquiries besides the above common used types can be met on your requirement.



Recommend PC Board Pattern



	G	
nm)	G(mm)	H(mm

L(mm)	G(mm)	H(mm)	
14.5	8.0	5.0	
lote 1 The	above PCB la	vout referenc	e o

TMPA1265SP-330MN-D

6. Material List



NO	Items	Materials
1	Core	Alloy Powder .
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	Ink	Halogen-free ketone

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	110~+40℃,50~60%RH (Product with taping) 240~+125℃ (on board)	
Electrical Performance Te	st	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502, Agilent 33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop $\ \bigtriangleup L(\%)$
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature : 125±2°C(Inductor) Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: $85\pm2\%$ R.H, Temperature: $85\%2\pm2\%$ Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs. 3. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs,keep at $25°C$ for 2 hrs then keep at $-10°C$ for 3 hrs 4. Keep at $25°C$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1–2 hrs.
Thermal shock	-	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1: $-40\pm2^{\circ}$ C 30 ±5 min Step2: $25\pm2^{\circ}$ C \leq 0.5min Step3: $125\pm2^{\circ}$ C 30 ±5 minNumber of cycles: 500 Measured at room fempraturc after placing for 24 ±2 hrs. Preconditioning: Run through IR reflow for 2
Vibration		Total Amplitude: 10 Total Total Title Science

TAI-TECH

Item	Performance	Test Condition						
Bending	Appearance ∶ No damage. Imoedance ∶ within±15% of initial value	fol <0 Be <0	Shall be mounted on a FR4 substrate of the ollowing dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm 3ending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm Juration of 10 sec.					
Shock	Inductance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not		Туре	Peak value (g's)	dura	ormal ition (D) ms)	Wave form	Velocity change (Vi)ft/sec
Shock	exceed the specification value		SMD	50	-	11	Half-sine	11.3
			Lead	50		11	Half-sine	11.3
Solder ability	More than 95% of the terminal electrode should be covered with solder。	So Te Flu Dij	lder: Si mperat ix for le o time:	150°C,609 n96.5% A ure: 245± ad free: I 4±1sec ∘ mpletely	g3% Cu :5℃ ∘ Rosin. 9.		tion	
Resistance to Soldering Heat				ature(°C)		ramp/ir and eme	tion perature mmersion ersion rate s ±6 mm/s	Number of heat cycles
Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times (J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the of tested, apply a force(>0805:1Kg, <=0805:0.5Kg)to if device being tested. This force shall be applied seconds. Also the force shall be applied gradual apply a shock to the component being tested.		the device to be g)to the side of a oplied for 60 +1				

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

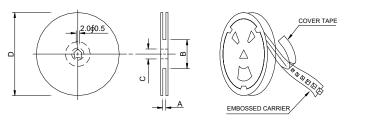
- Preheat circuit and products to 150°C
 · Never contact the ceramic with the iron tip
 · 355°C tip temperature (max)
 · 1.0mm tip diameter (max)
 ·
- Use a 20 watt soldering iron with tip diameter of 1.0mm
 Limit soldering time to 4~5sec.
- Reflow Soldering Iron Soldering PRE-HEATING SOLDERING NATURAL PRE-HEATING SOLDERING NATURAL within 4~5s TP(260℃ / 10s max. 350 TEMPERATURE(¢₿) tp(245¢&/20~40s.) remperature(¢®) 217 ~150s 150 60 200 60~180s Gradual cooling Over 60s 480s ma: TIME(sec.) TIME(sec.) Iron Soldering times: 1 times max. Reflow times: 3 times max. Fig.1 Fig.2

9. Friendly reminder

- (1) When there are questions concerning measurement result : measurement shall be made after 48 \pm 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product.

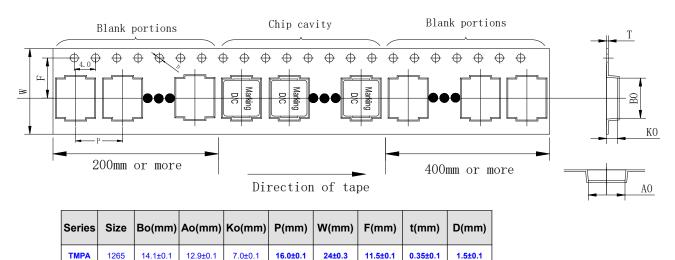
10. Packaging Information

(1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

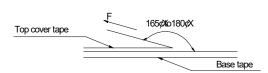
(2) Tape Dimension



(3) Packaging Quantity

TMPA	1265	
Chip / Reel	500	
Inner box	1000	
Carton	4000	

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions
 - To maintain the solderability of terminal electrodes:
 - 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
 - 2. Temperature and humidity conditions: Less than 40° C and 60° RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

11. Typical Performance Curves

