



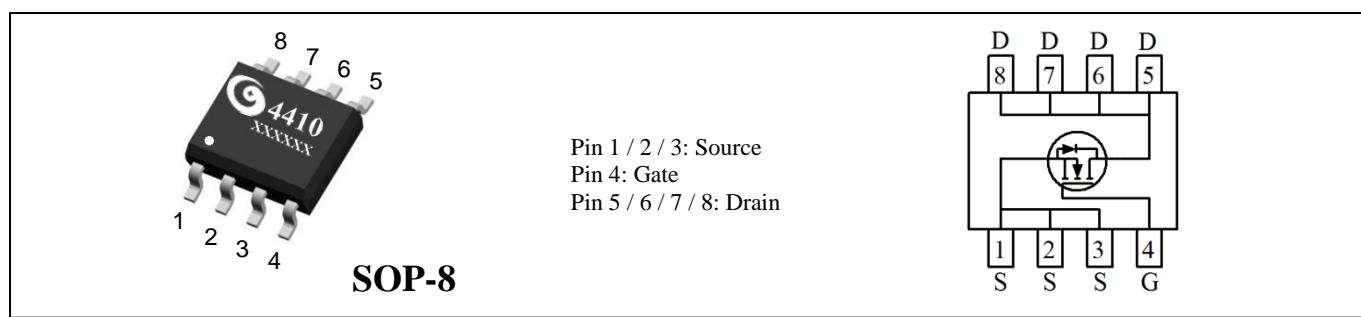
### N-Channel Enhancement-Mode MOSFET (30V, 10A)

PRODUCT SUMMARY		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(on)</sub> (mΩ) Max
30V	10A	15 @ V <sub>GS</sub> = 10V ,I <sub>D</sub> =10A
		24 @ V <sub>GS</sub> = 4.5V,I <sub>D</sub> =5A

## Features

- Advanced Trench Process Technology
- High Density Cell Design for Ultra Low On-Resistance
- Lead free product is acquired
- Surface mount Package
- Ordering information: GS4410 (Lead (Pb) - free and halogen - free)

RoHS+HF



## Absolute Maximum Ratings (T<sub>A</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current @T <sub>A</sub> =25°C	10	A
I <sub>DM</sub>	Drain Current (Pulsed) <sup>b</sup>	50	A
I <sub>S</sub>	Maximum Diode Forward Current <sup>a</sup>	4.2	A
E <sub>AS</sub>	Avalanche Energy, Single Pulse (L=0.5mH) <sup>c</sup>	20	mJ
P <sub>D</sub>	Total Power Dissipation @T <sub>A</sub> =25°C <sup>a</sup>	2	W
T <sub>j</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature Range <sup>a</sup>	-55 to +150	°C
R <sub>θJA</sub>	Thermal Resistance Junction to Ambient <sup>a</sup>	62.5	°C/W

Note: a: Surface Mounted on FR4 Board , t ≤ 5sec .

b: Pulse width limited by maximum junction temperature.

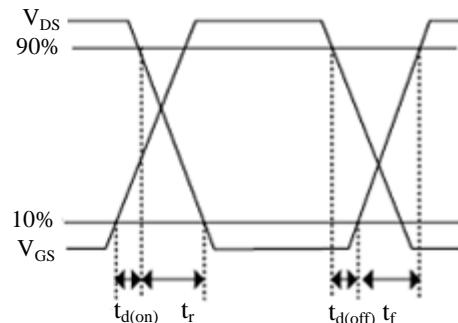
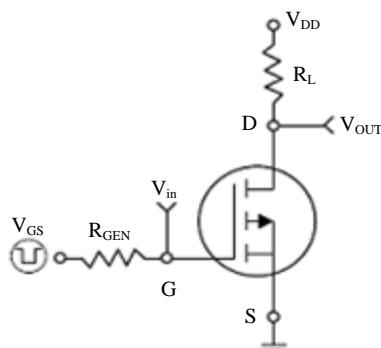
C : UIS tested and pulse width limited by maximum junction temperature 150oC (initial temperature Tj=25oC).

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
<b>• Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 16\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>• On Characteristics <sup>c</sup></b>						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	-	3	V
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	-	12	15	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	-	15	24	
$g_{\text{FS}}$	Forward Trans conductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=9\text{A}$	-	9	-	S
<b>• Dynamic Characteristics <sup>d</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	710	-	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		-	155	-	
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	145	-	
$R_g$	Gate resistance	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	1	2	3	$\Omega$
<b>• Switching Characteristics <sup>d</sup></b>						
$Q_g$	Total Gate Charge	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=9\text{A}, V_{\text{GS}}=4.5\text{V}$	-	8	-	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		-	3.3	-	
$Q_{\text{gd}}$	Gate-Drain Charge		-	2.7	-	
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=9\text{A}, R_L=15\Omega, R_{\text{GEN}}=3.3\Omega$	-	7	-	$\text{nS}$
$t_r$	Turn-on Rise Time		-	7	-	
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	22	-	
$t_f$	Turn-off Fall Time		-	7	-	
$t_{\text{rr}}$	Body Diode Reverse Recovery Time	$I_F=9\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	24	-	nS
$Q_{\text{rr}}$	Body Diode Reverse Recovery Charge	$I_F=9\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	14	-	nC
<b>• Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=2.1\text{A}$	-	-	1.2	V

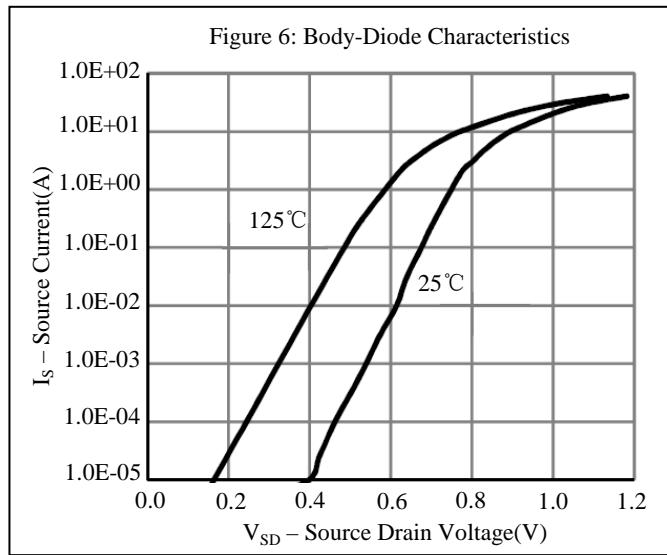
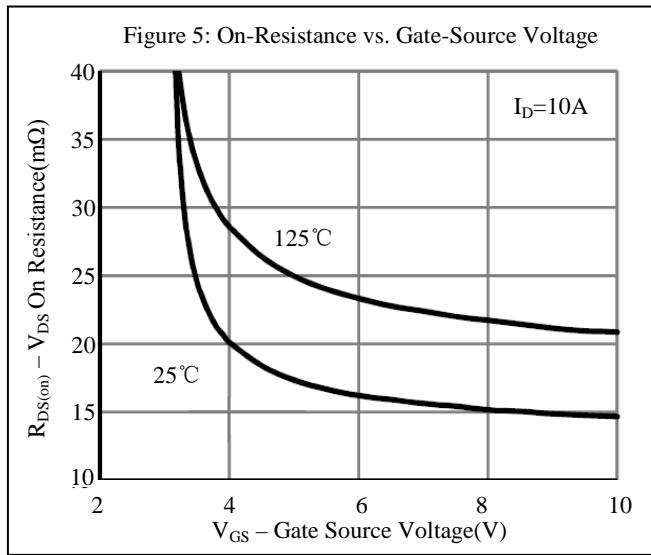
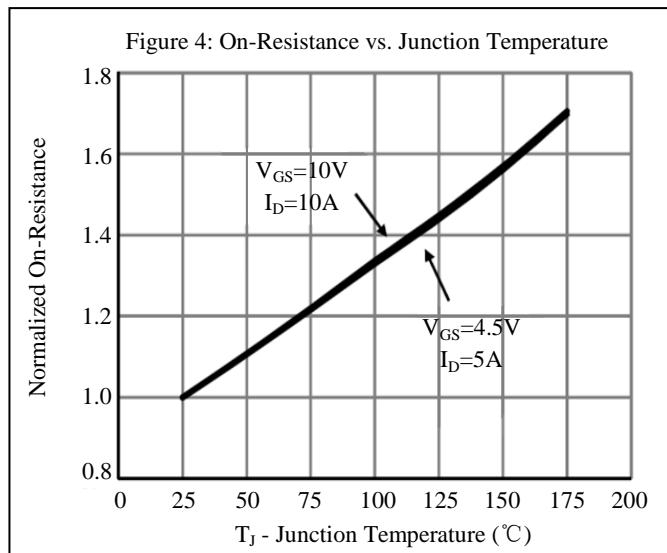
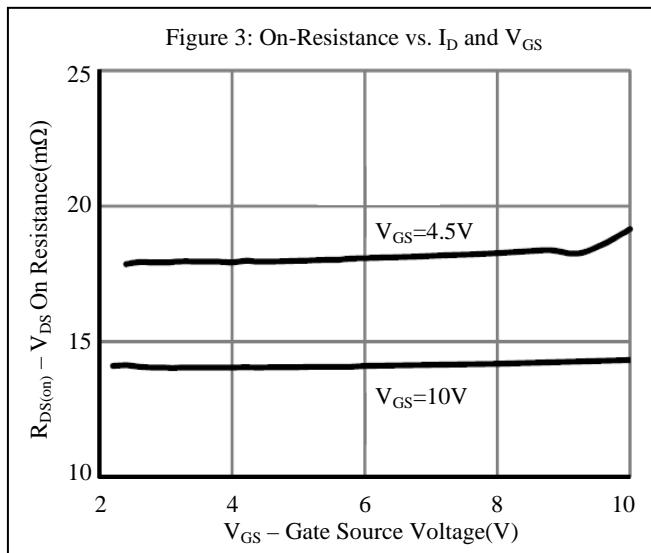
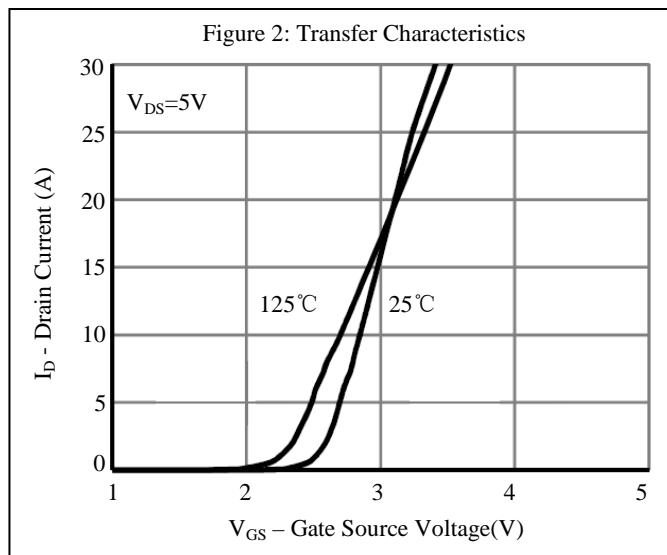
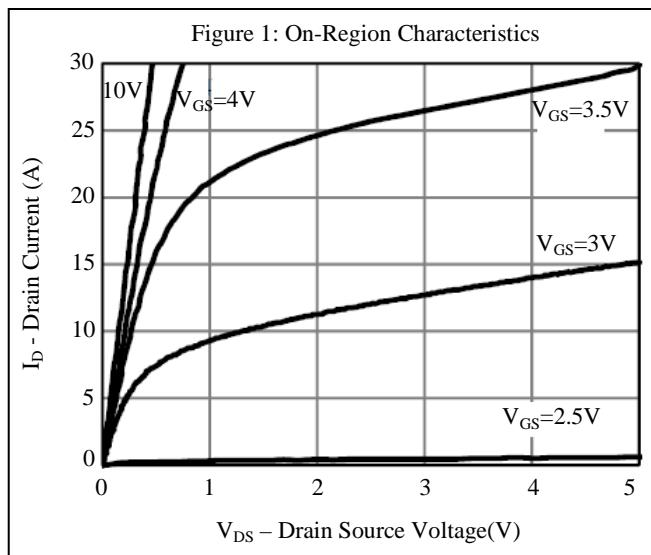
Note: c: Pulse Test : Pulse Width < 300 $\mu\text{s}$ , Duty Cycle < 2%

d: Guaranteed by design, not subject to production testing.

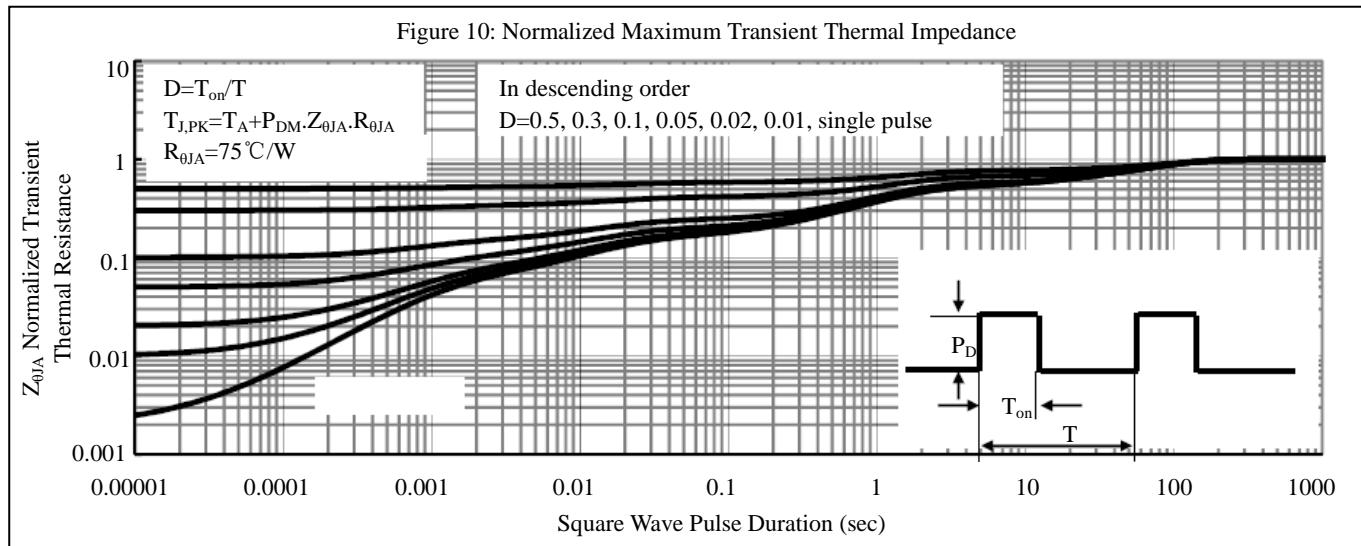
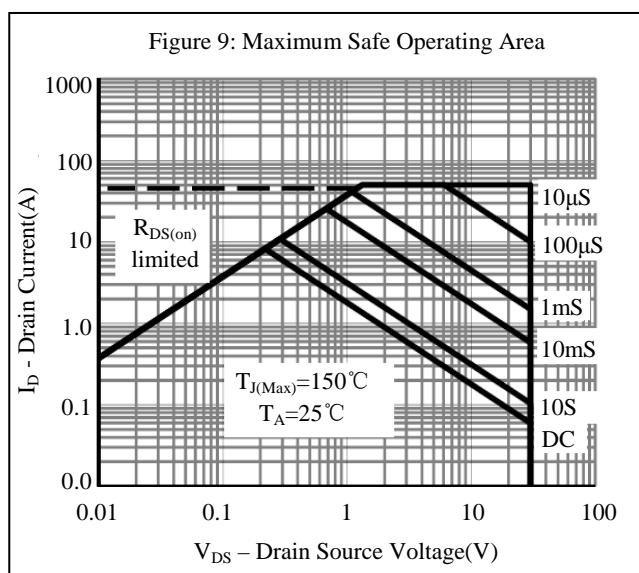
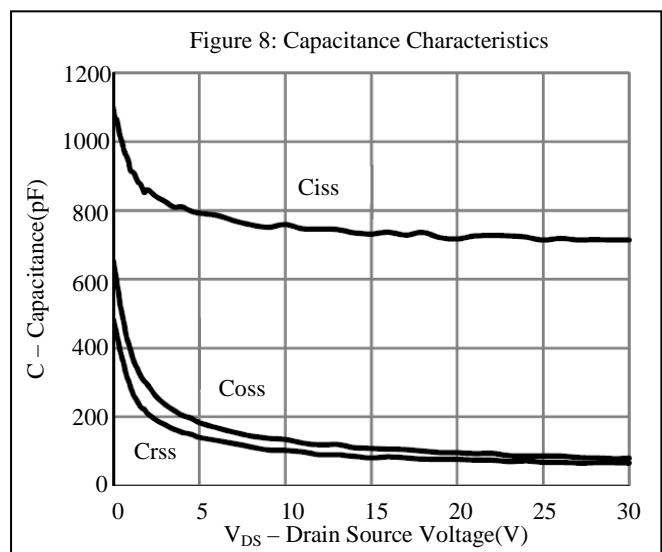
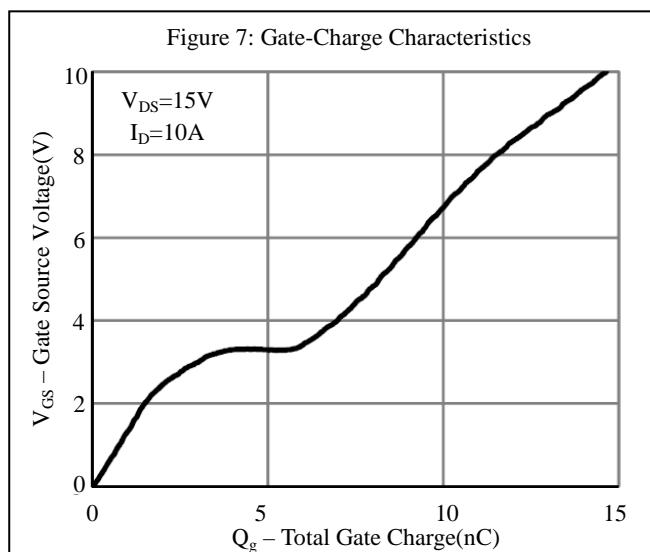


Switching Test Circuit and Switching Waveforms

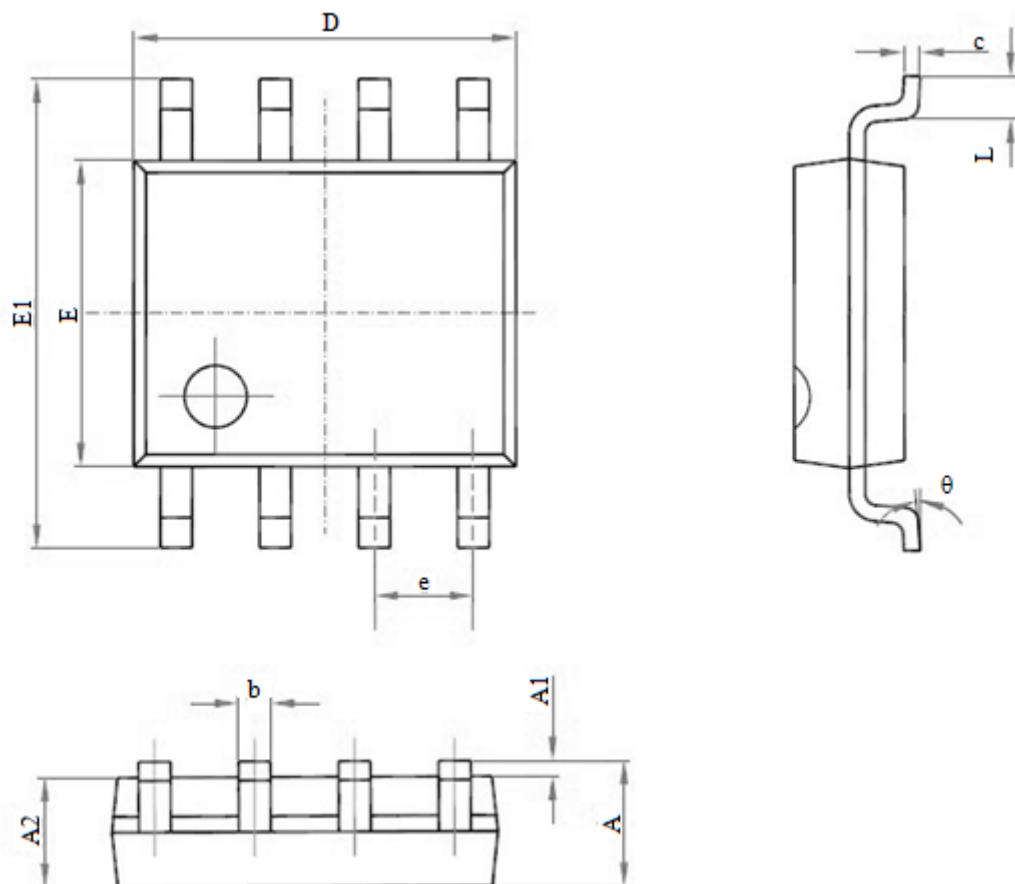
### Characteristics Curve



### Characteristics Curve



### SOP-8 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters (MM)		Dimensions In Inches (MIL)	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.310	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
$\theta$	$0^{\circ}$	$8^{\circ}$	$0^{\circ}$	$8^{\circ}$

### Notice

1. Specification of the products displayed herein is subject to change without notice. Continuous development may necessitate changes in technical data without notice. GEMMICRO or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies.

2. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.