

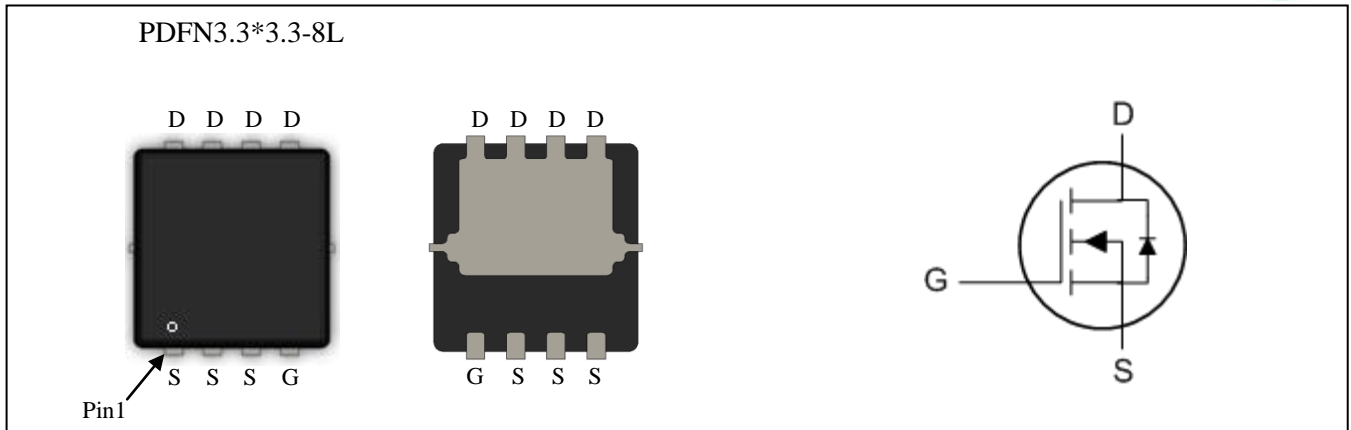
N-Channel Enhancement-Mode MOSFET (30V, 37 A)

| PRODUCT SUMMARY | | |
|-----------------|-------|---------------------------------|
| V_{DSS} | I_D | $R_{DS(on)}$ (m Ω) Max |
| 30V | 37A | 16.5 @ $V_{GS} = 4.5V, I_D=10A$ |
| | | 12 @ $V_{GS} = 10V, I_D=15A$ |

Features

- Super high dense cell trench design for low $R_{DS(on)}$.
- Green Device Available
- Advanced high cell density Trench technology
- Ordering information: GR7430 (Lead (Pb) - free and halogen-free)
- 100% EAS Guaranteed
- Super Low Gate Charge
- Excellent CdV/dt effect decline

RoHS+HF



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Ratings | Units |
|-----------------|--|-------------|--------------------|
| V_{DS} | Drain-Source Voltage | 30 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| I_D | Continuous Drain Current ^a @ $T_C=25^\circ\text{C}$ | 37 | A |
| I_D | Continuous Drain Current ^a @ $T_C=100^\circ\text{C}$ | 24 | A |
| I_{DM} | Drain Current (Pulsed) ^b | 75 | A |
| E_{AS} | Single Pulse Avalanche Energy ^c @ $L=0.1\text{mH}$ | 24.2 | mJ |
| I_{AS} | Single Pulse Avalanche Current @ $L=0.1\text{mH}$ | 22 | A |
| P_D | Total Power Dissipation ^d @ $T_A=25^\circ\text{C}$ | 1.67 | W |
| T_j, T_{stg} | Operating Junction and Storage Temperature Range | -55 to +150 | $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient (Steady State) ^a | 75 | $^\circ\text{C/W}$ |

Electrical Characteristics (T_A=25°C, unless otherwise noted)

| Symbol | Characteristic | Test Conditions | Min. | Typ. | Max. | Unit |
|--|---|--|------|------|------|------|
| •Off Characteristics | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250μA | 30 | - | - | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =24V, V _{GS} =0V | - | - | -1 | μA |
| I _{GSS} | Gate-Body Leakage Current | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| •On Characteristics^c | | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250μA | 1.0 | - | 2.5 | V |
| R _{DS(on)} | Drain-Source On-State Resistance ^b | V _{GS} =10V, I _D =15A | - | 8.5 | 12 | mΩ |
| | | V _{GS} =4.5V, I _D =10A | - | 12.5 | 16.5 | |
| g _{fs} | Forward Transconductance | V _{DS} =5V, I _D =15A | - | 24.4 | - | S |
| •Dynamic Characteristics | | | | | | |
| C _{iss} | Input Capacitance | V _{DS} =15V, V _{GS} =0V, f=1MHz | - | 896 | - | pF |
| C _{oss} | Output Capacitance | | - | 126 | - | |
| C _{rss} | Reverse Transfer Capacitance | | - | 108 | - | |
| •Switching Characteristics | | | | | | |
| Q _g | Total Gate Charge | V _{DS} =15V, I _D =12A, V _{GS} =4.5V | - | 9.82 | - | nC |
| Q _{gs} | Gate-Source Charge | | - | 2.24 | - | |
| Q _{gd} | Gate-Drain Charge | | - | 5.54 | - | |
| t _{d(on)} | Turn-on Delay Time | V _{DD} =15V, I _D =20A, V _{GS} =10V, R _G =1.5Ω | - | 6.4 | - | nS |
| t _r | ^b Turn-on Rise Time | | - | 39 | - | |
| t _{d(off)} | Turn-off Delay Time | | - | 21 | - | |
| t _f | Turn-off Fall Time | | - | 4.7 | - | |
| •Drain-Source Diode Characteristics | | | | | | |
| V _{SD} | Drain-Source Diode Forward Voltage ^b | V _{GS} =0V, I _S =-1.0A | - | - | 1 | V |
| I _S | Continuous Source Current ^{a,e} | V _G =V _D =0V, Force Current | - | - | 37 | A |
| I _{SM} | Pulsed Source Current ^{b,e} | | - | - | 75 | |

Note :

a. The data tested by surface mounted on a 1 inch2 FR-4 board with 20Z copper.

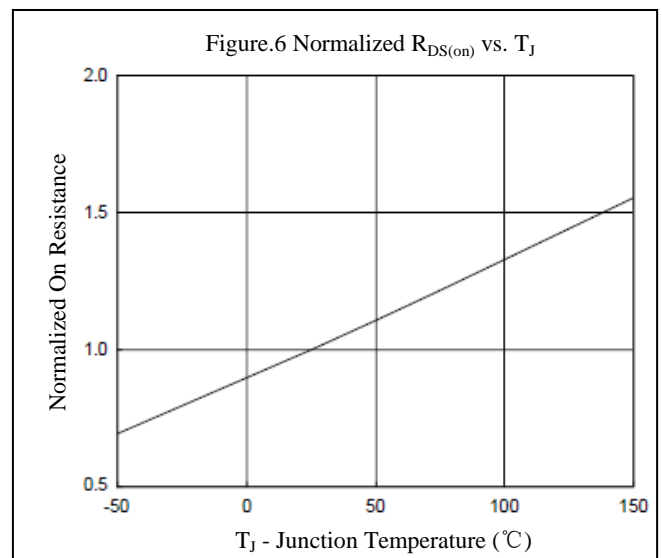
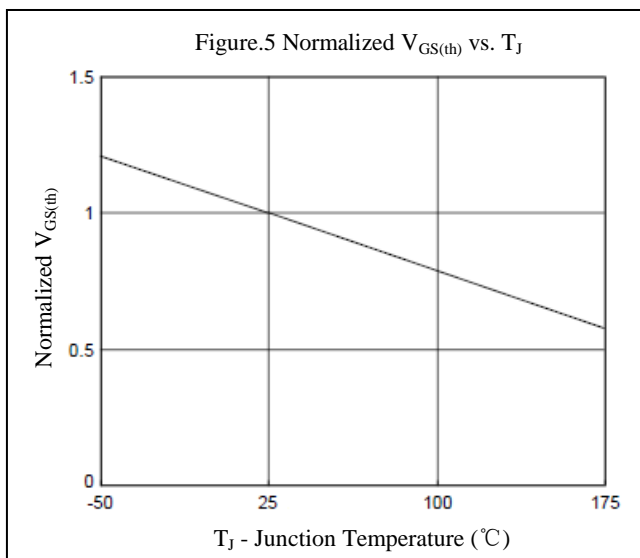
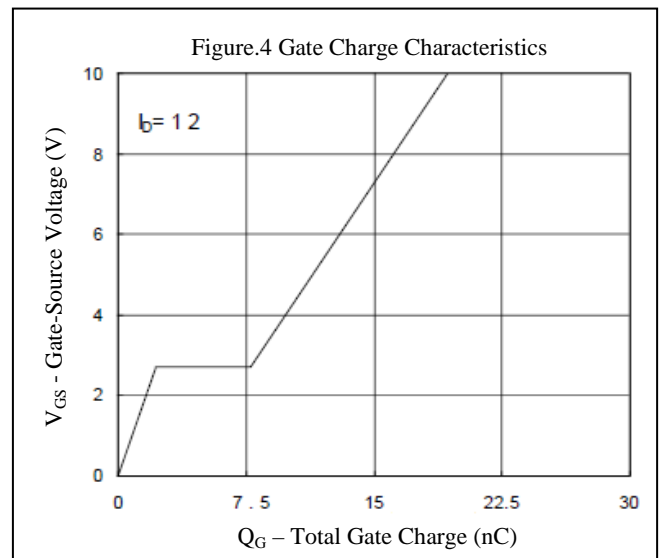
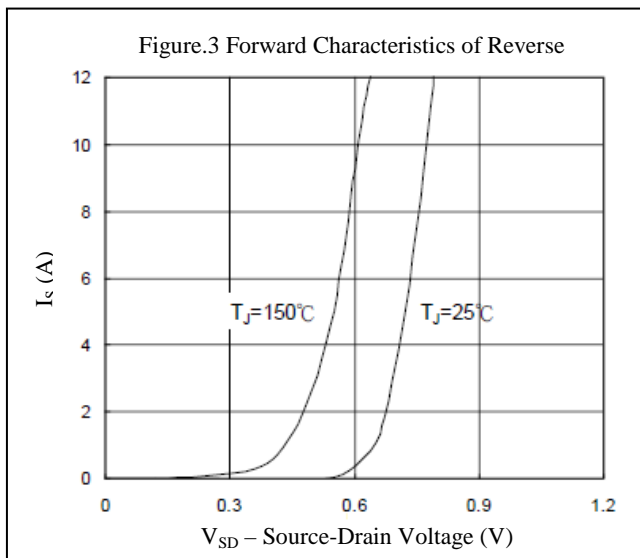
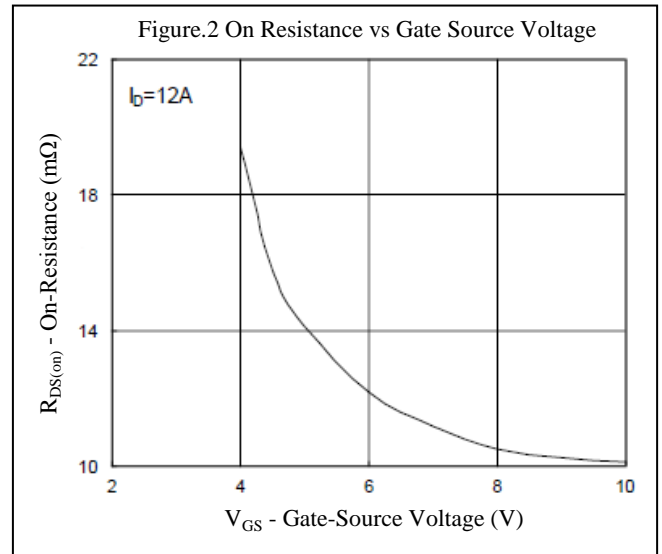
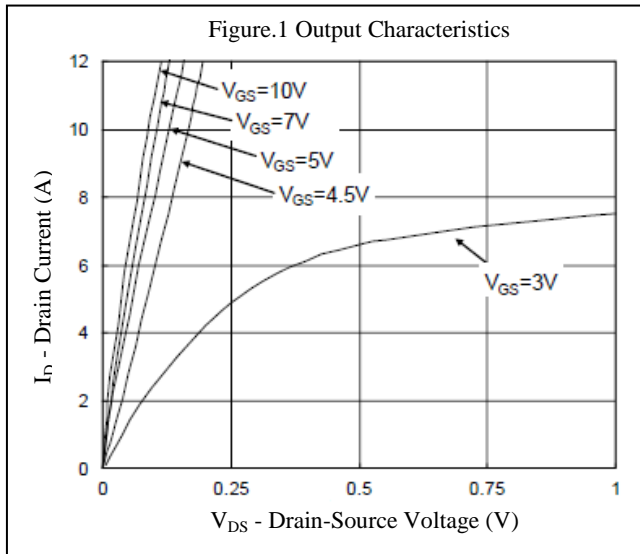
b. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

c. The E_{AS} data shows Max. rating . The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=22A

d. The power dissipation is limited by 175°C junction temperature

e. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Characteristics Curve



Characteristics Curve

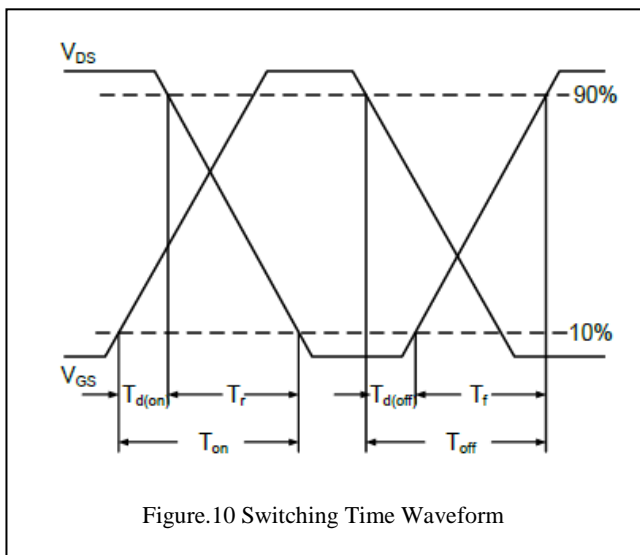
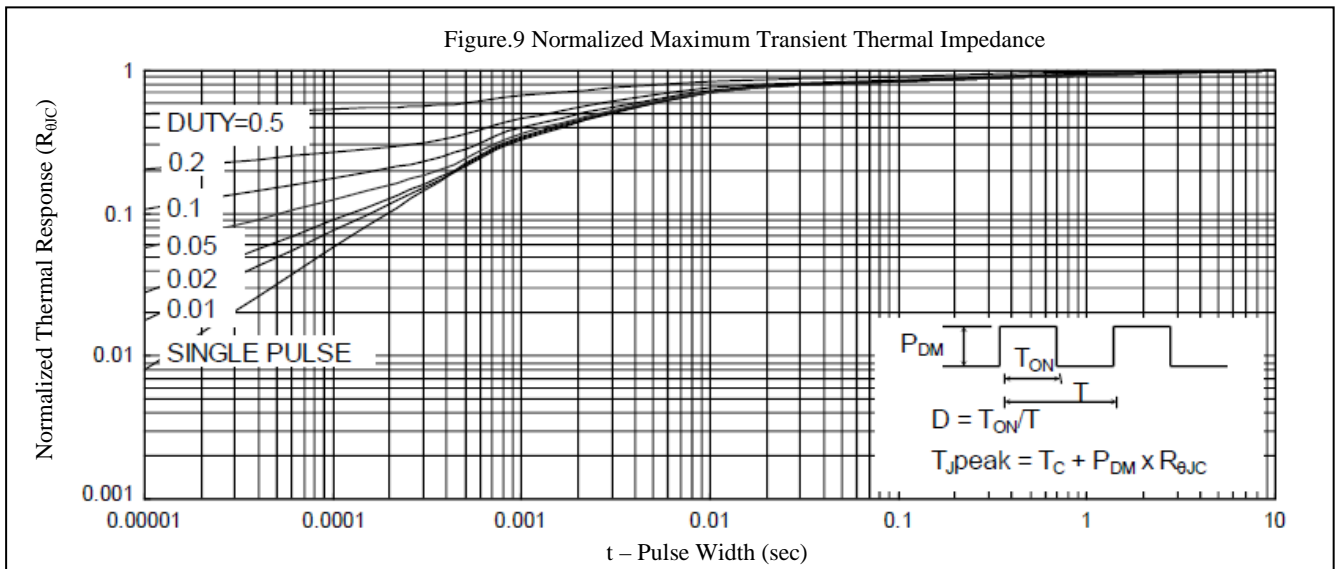
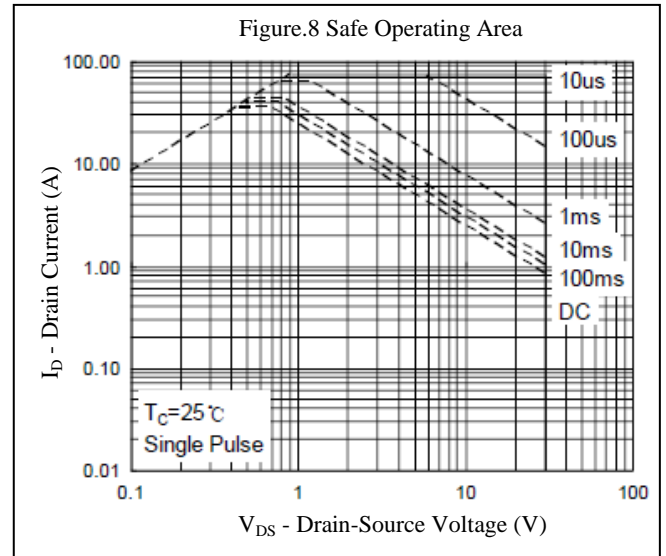
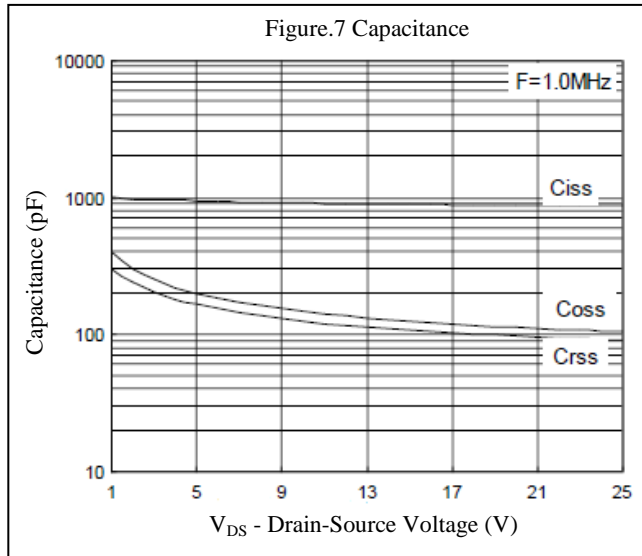


Figure.10 Switching Time Waveform

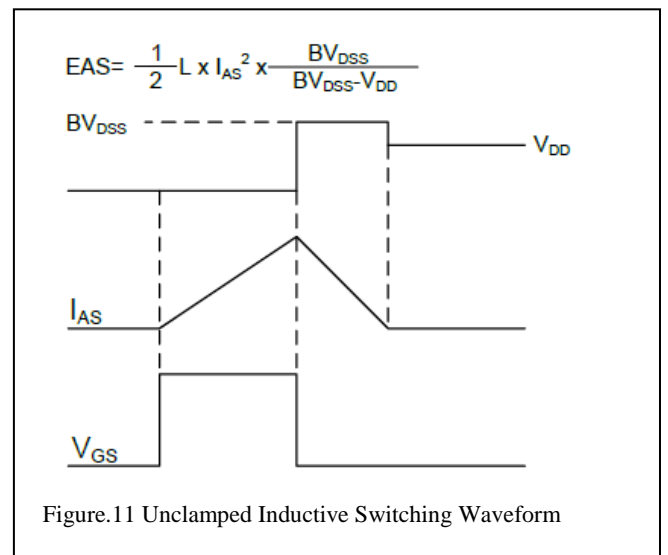
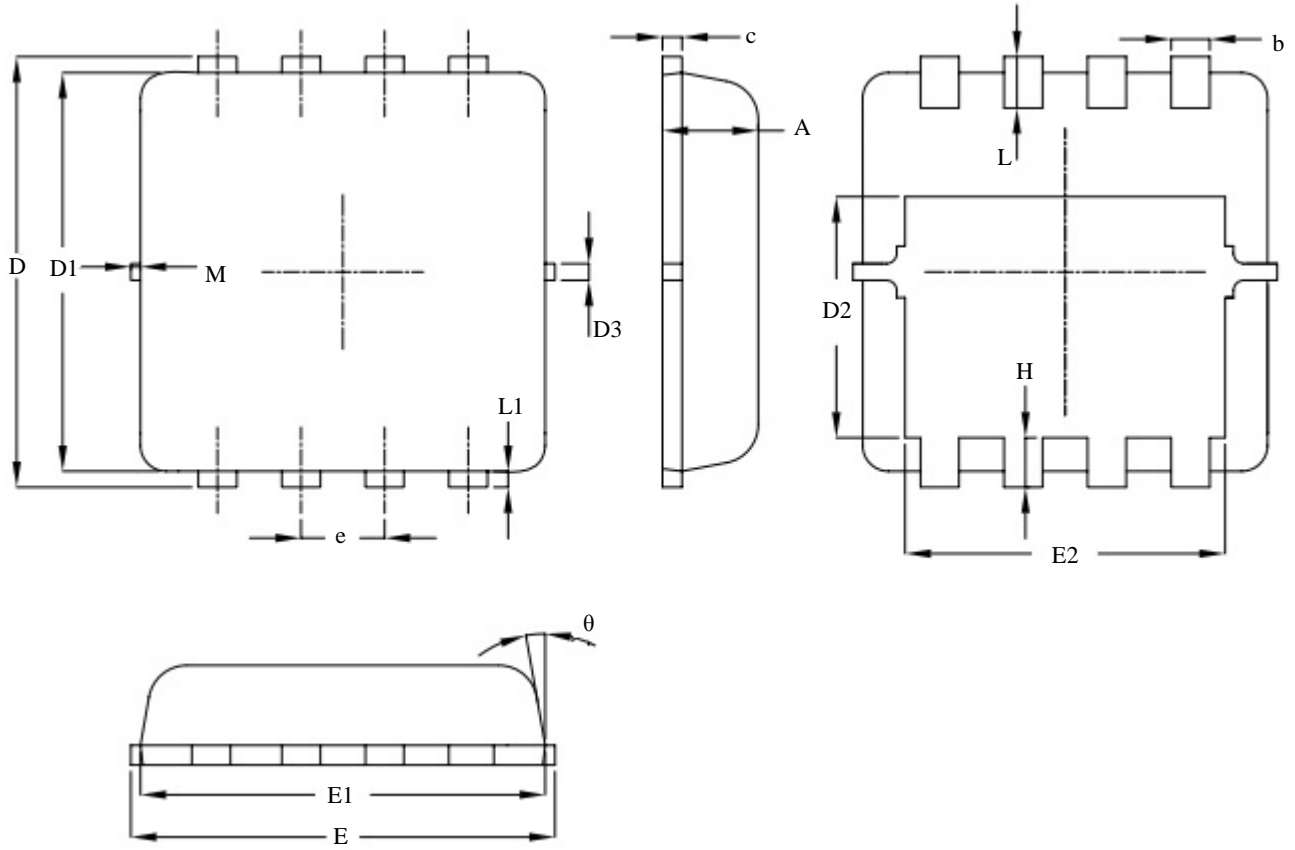


Figure.11 Unclamped Inductive Switching Waveform

PDFN3.3*3.3-8L PACKAGE OUTLINE DIMENSIONS



| Symbol | MM | | | INCH | | |
|--------|---------|------|------|----------|-------|-------|
| | MIN | TYP | MAX | MIN | TYP | MAX |
| A | 0.70 | 0.75 | 0.80 | 0.028 | 0.030 | 0.031 |
| b | 0.25 | 0.30 | 0.35 | 0.010 | 0.012 | 0.014 |
| c | 0.10 | 0.15 | 0.25 | 0.004 | 0.006 | 0.010 |
| D | 3.25 | 3.35 | 3.45 | 0.128 | 0.132 | 0.136 |
| D1 | 3.00 | 3.10 | 3.20 | 0.118 | 0.122 | 0.126 |
| D2 | 1.78 | 1.88 | 1.98 | 0.070 | 0.074 | 0.078 |
| D3 | - | 0.13 | - | - | 0.005 | - |
| E | 3.20 | 3.30 | 3.40 | 0.126 | 0.130 | 0.134 |
| E1 | 3.00 | 3.15 | 3.20 | 0.118 | 0.124 | 0.126 |
| E2 | 2.39 | 2.49 | 2.59 | 0.094 | 0.098 | 0.102 |
| e | 0.65BSC | | | 0.026BSC | | |
| H | 0.30 | 0.39 | 0.50 | 0.012 | 0.015 | 0.020 |
| L | 0.30 | 0.40 | 0.50 | 0.012 | 0.016 | 0.020 |
| L1 | - | 0.13 | - | - | 0.005 | - |
| θ | - | 10° | 12° | - | 10° | 12° |
| M | - | - | 0.15 | - | - | 0.006 |



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2. Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.