



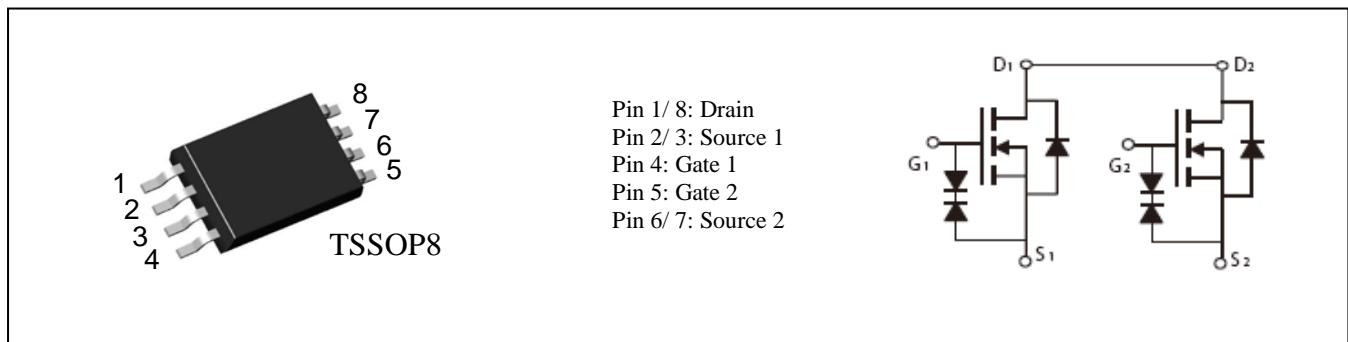
Dual N-Channel High Density Trench MOSFET (20V, 7A)

PRODUCT SUMMARY		
V _{DSS}	I _D	R _{DS(on)} (mΩ) Max
20V	7A	20 @ V _{GS} = 10V, I _D = 7A
		24 @ V _{GS} = 4.5V, I _D = 6.5A
		32 @ V _{GS} = 2.5V, I _D = 5.5A

Features

- Super high dense cell trench design for low R_{DS(on)}
- Rugged and reliable
- Surface Mount package
- Battery Switch ESD Protected 2KV
- Ordering information: GM8820E -G(Lead(Pb)-free and halogen-free)

RoHS+HF



Absolute Maximum Ratings (T_A=25°C, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D	Drain Current (Continuous)	7	A
I _{DM}	Drain Current (Pulsed) ^a	30	A
P _D	Total Power Dissipation @ T _A =25°C	1.5	W
T _j , T _{stg}	Operating Junction and Storage Temperature Range	-55 to +150	°C
R _{θJA}	Thermal Resistance Junction to Ambient (PCB mounted) ^b	83	°C/W

Note: a: Repetitive Rating: Pulse width limited by the maximum junction temperature

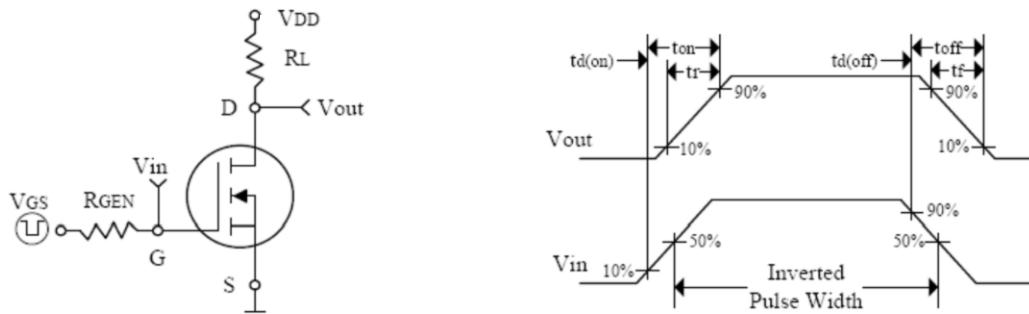
b: 1-in² 2oz Cu PCB board

Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=16\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 10	uA
• On Characteristics ^c						
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	0.5	-	1.0	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-State Resistance	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=7\text{A}$	-	16	20	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=6.5\text{A}$	-	20	24	
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_D=5.5\text{A}$	-	26	32	
g_{FS}	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=6.5\text{A}$	-	19	-	S
• Dynamic Characteristics ^d						
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=10\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	321	-	pF
C_{oss}	Output Capacitance		-	101	-	
C_{rss}	Reverse Transfer Capacitance		-	24	-	
• Switching Characteristics ^d						
Q_{g}	Total Gate Charge	$\text{V}_{\text{DS}}=12\text{V}, \text{I}_D=6\text{A}, \text{V}_{\text{GS}}=4.5\text{V}$	-	9.2	-	nC
Q_{gs}	Gate-Source Charge		-	2.7	-	
Q_{gd}	Gate-Drain Charge		-	3.7	-	
$t_{\text{d(on)}}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=10\text{V}, \text{R}_{\text{L}}=5\Omega, \text{I}_D=2\text{A}, \text{V}_{\text{GS}}=4.5\text{V}$ $\text{R}_{\text{GEN}}=6\Omega$	-	307	-	nS
t_{r}	Turn-on Rise Time		-	725	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	3492	-	
t_{f}	Turn-off Fall Time		-	2135	-	
• Drain-Source Diode Characteristics						
V_{SD}	Drain-Source Diode Forward Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=1\text{A}$	-	-	1	V

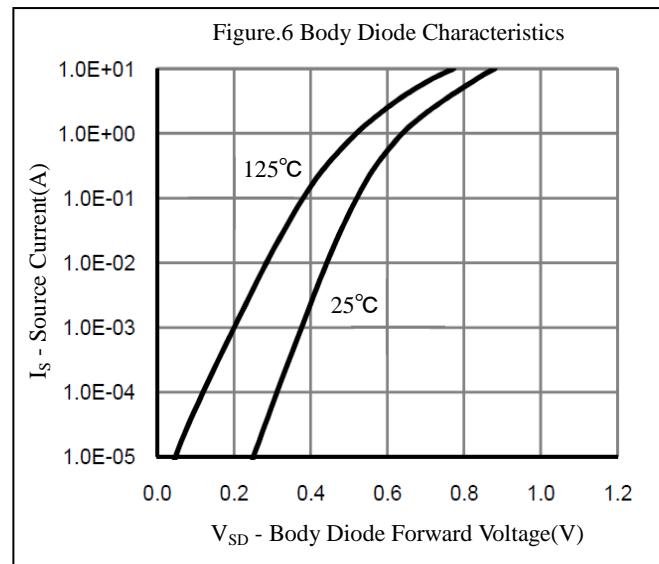
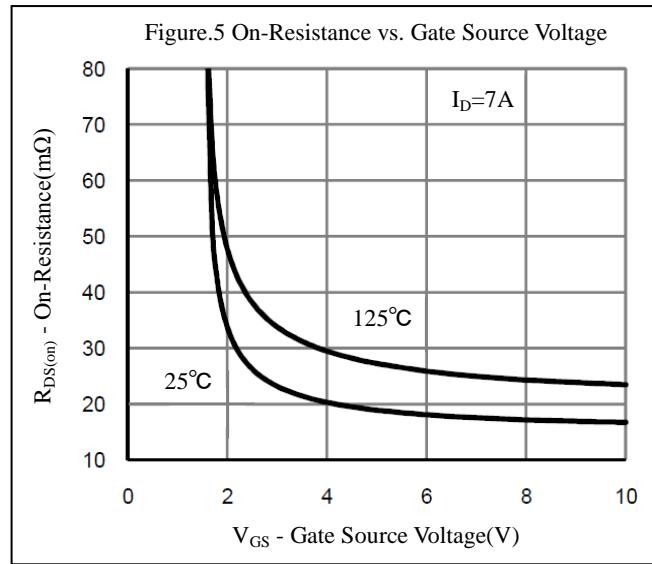
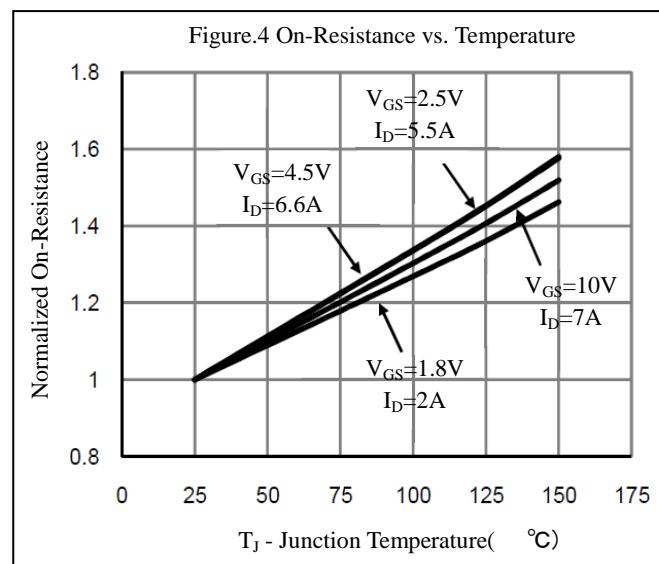
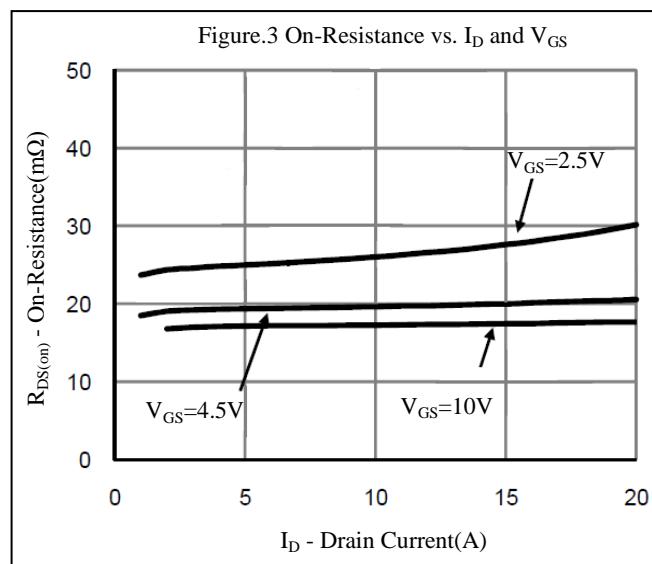
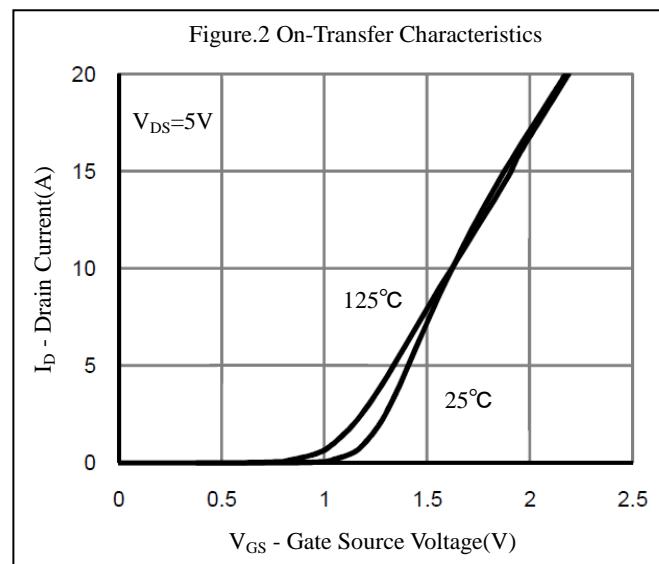
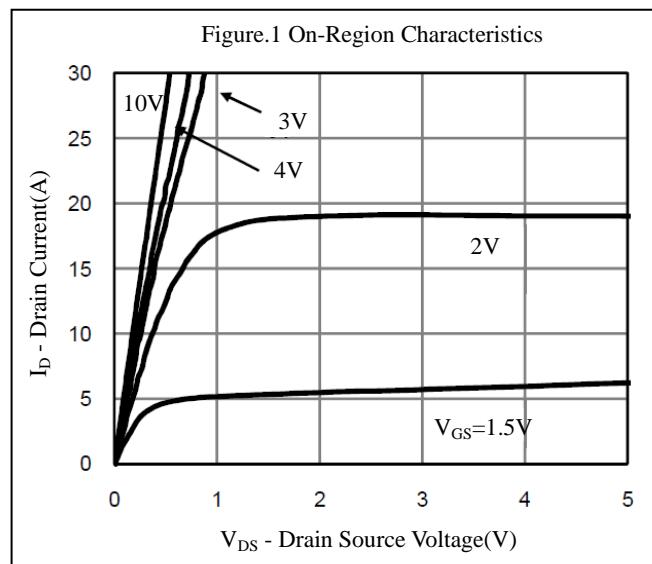
Note: c : Pulse Test : Pulse Width < 300 μs , Duty Cycle < 2%

d: Guaranteed by design, not subject to production testing.

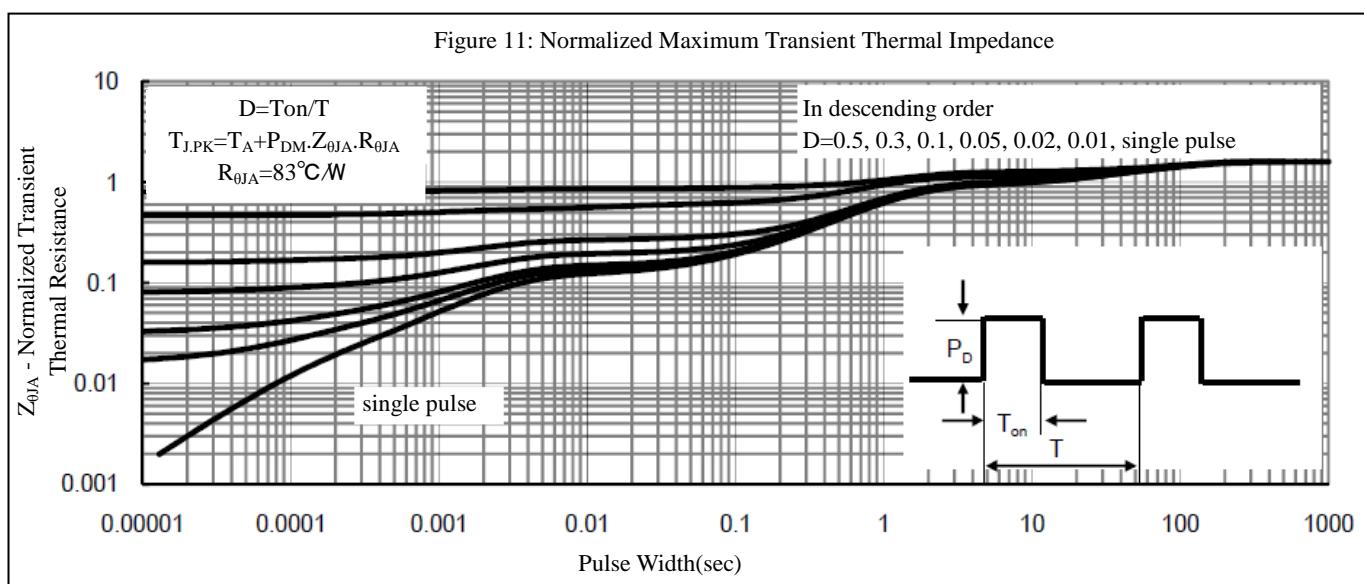
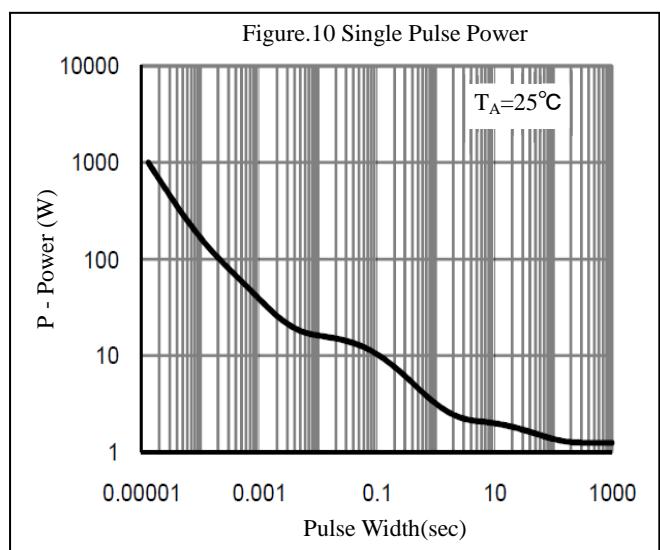
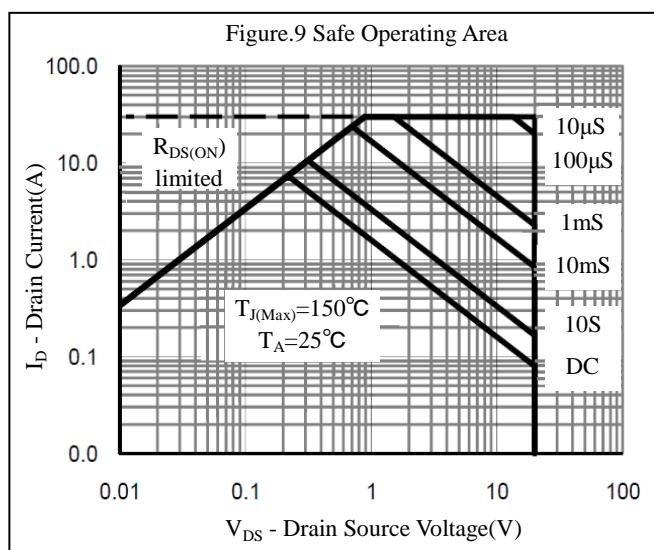
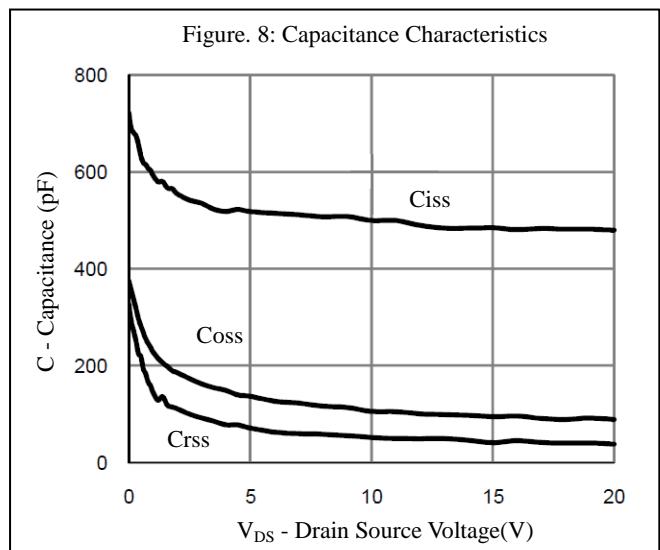
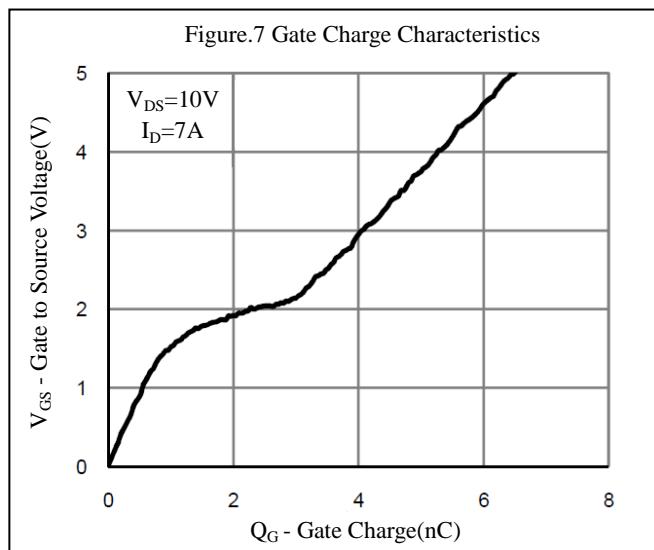


Switching Test Circuit and Switching Waveforms

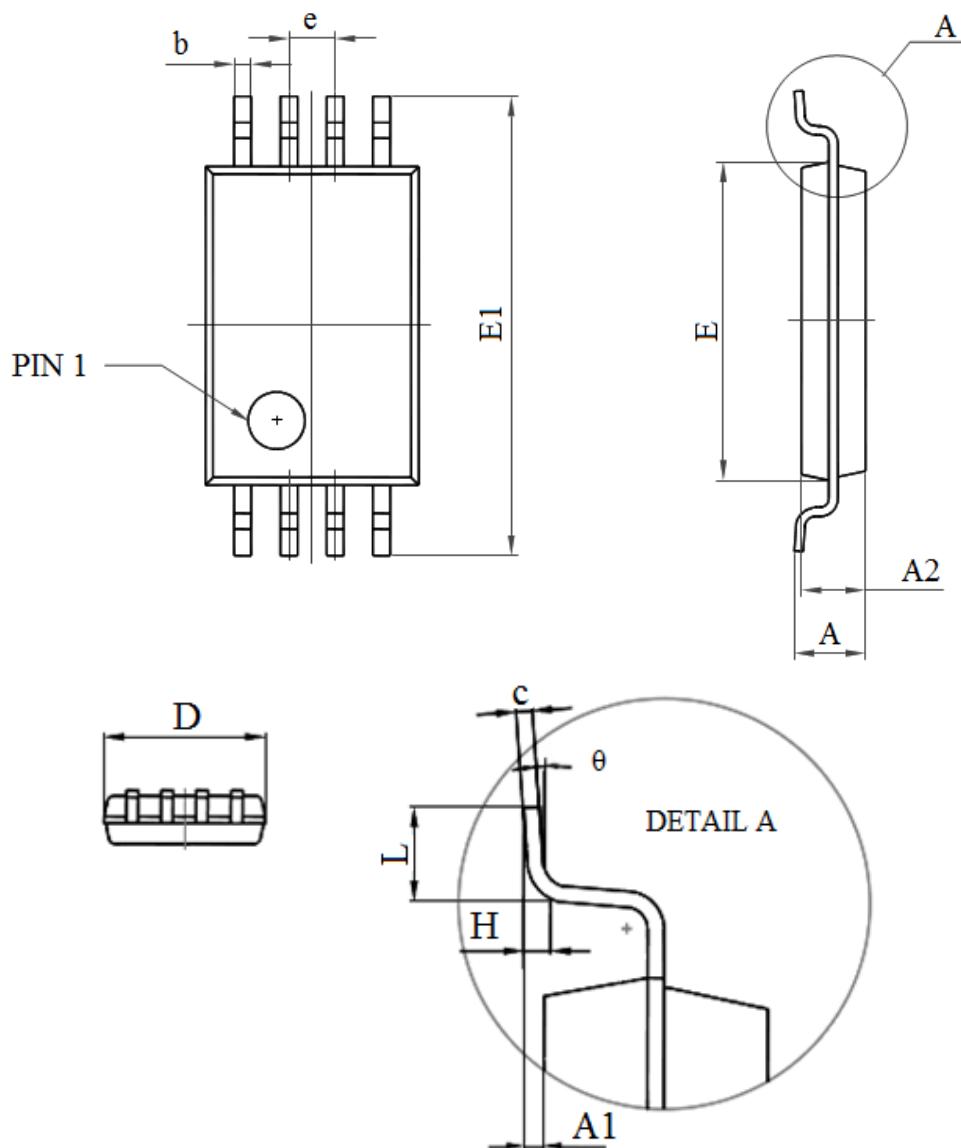
Characteristics Curve



Characteristics Curve



TSSOP8 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters(MM)		Dimensions In Inches(MIL)	
	Min	Max	Min	Max
D	2.900	3.160	0.114	0.124
E	4.300	4.500	0.169	0.177
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
E1	6.250	6.550	0.246	0.258
A		1.200		0.047
A2	0.800	1.000	0.031	0.039
A1	0.050	0.150	0.002	0.006
e	0.65(BSC)		0.026(BSC)	
L	0.500	0.700	0.020	0.028
H	0.25(TYP)		0.01(TYP)	
θ	1°	7°	1°	7°

Notice

1. Specification of the products displayed herein is subject to change without notice. Continuous development may necessitate changes in technical data without notice. GEMMICRO or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies.
2. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.