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November 2013

FDPF8N60ZUT

N-Channel UniFETTM II Ultra FRFETTM MOSFET 600 V, 6.5 A, 1.35 Ω

Features

- $R_{DS(on)} = 1.15 \Omega (Typ.) @ V_{GS} = 10 V, I_D = 3.25 A$
- Low Gate Charge (Typ. 20 nC)
- Low C_{rss} (Typ. 10 pF)
- · 100% Avalanche Tested
- · Improved dv/dt Capability
- · RoHS Compliant

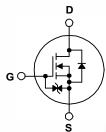
Applications

- LCD/LED TV
- Lighting
- · Uninterruptible Power Supply
- · AC-DC Power Supply

Description

UniFETTM II MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on advanced planar stripe and DMOS technology. This advanced MOSFET family has the smallest on-state resistance among the planar MOSFET, and also provides superior switching performance and higher avalanche energy strength. In addition, internal gate-source ESD diode allows UniFET II MOSFET to withstand over 2kV HBM surge stress. UniFET II Ultra FRFETTM MOSFET has much superior body diode reverse recovery performance. Its $t_{\rm rr}$ is less than 50nsec and the reverse dv/dt immunity is 20V/nsec while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore UniFET II Ultra FRFET MOSFET can remove additional component and improve system reliability in certain applications that require performance improvement of the MOSFET's body diode. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FDPF8N60ZUT	Unit
V_{DSS}	Drain to Source Voltage			600	V
V _{GSS}	Gate to Source Voltage			±30	V
	Drain Current	- Continuous (T _C = 25°C)		6.5*	А
I _D	Diam Current	- Continuous (T _C = 100°C)		3.9*	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	26*	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)			420	mJ
I _{AR}	Avalanche Current		(Note 1)	6.5	Α
E _{AR}	Repetitive Avalanche Energy		(Note 1)	13.5	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	20	V/ns
В	Power Discipation	(T _C = 25°C)		34.5	W
P_D	Power Dissipation	- Derate Above 25°C		0.28	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
T _L	Maximum Lead Temperature	for Soldering, 1/8" from Case for	5 Seconds	300	°C

^{*}Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter FDPF8N60ZUT		Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	3.6	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. 62.5		*C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDPF8N60ZUT	FDPF8N60ZUT	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Chara	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 25^{\circ} C$	600	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.7	-	V/°C
l	Zero Gate Voltage Drain Current	$V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	25	μA
IDSS	Zero Gate Voltage Drain Guirent	$V_{DS} = 480 \text{ V}, T_{C} = 125^{\circ}\text{C}$	-	-	250	μΛ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±10	μΑ

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu\text{A}$	3.0	- ,	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 3.25 \text{ A}$	-	1.15	1.35	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 3.25 A	-	7	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05 V V 0 V		-	950	1265	pF
C _{oss}	Output Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz		110	150	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 101112		-\	10	15	pF
Qg	Total Gate Charge at 10V	V _{DS} = 480 V, I _D = 6.5 A,		-	20	26	nC
Q _{gs}	Gate to Source Gate Charge	V _{GS} = 10 V		-	5	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		(Note 4)	-	8	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	20	50	ns
t _r	Turn-On Rise Time	$V_{DD} = 300 \text{ V}, I_D = 6.5 \text{ A},$	-	30	70	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 25 \Omega$, $V_{GS} = 10 V$	-	55	120	ns
t _f	Turn-Off Fall Time	(Note 4)	-	35	80	ns
ESR	Equivalent Series Resistance	f = 1 MHz	0.5	5	11	Ω

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Dioc	Maximum Continuous Drain to Source Diode Forward Current		-	6.5*	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	26	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{SD} = 6.5 \text{ A}$	-	-	1.6	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 6.5 A,	-	40	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	-	42	-	nC

Notes

- 1: Repetitive rating: pulse-width limited by maximum junction temperature.
- 2: L = 20 mH, I $_{AS}$ = 6.5 A, V $_{DD}$ = 50 V, R $_{G}$ = 25 Ω , starting T $_{J}$ = 25°C.
- 3: $I_{SD} \le 6.5$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, starting $T_J = 25^{\circ}$ C.
- 4: Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

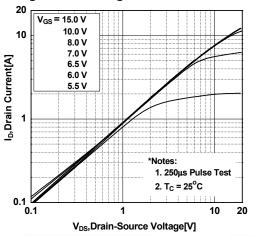


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

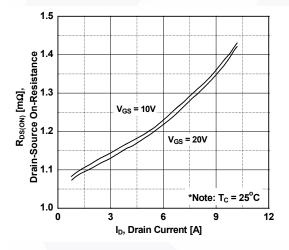


Figure 5. Capacitance Characteristics

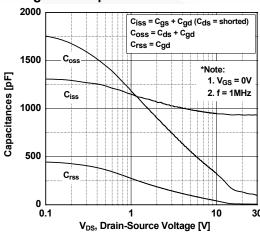


Figure 2. Transfer Characteristics

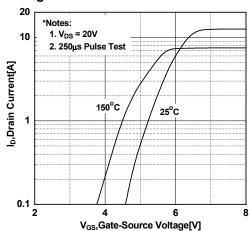


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

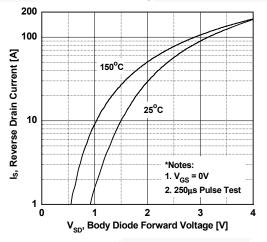
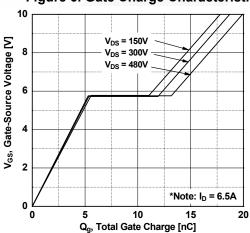


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

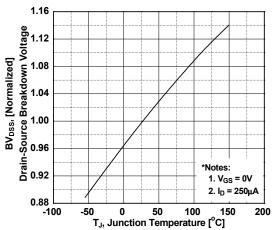


Figure 8. Maximum Safe Operating Area

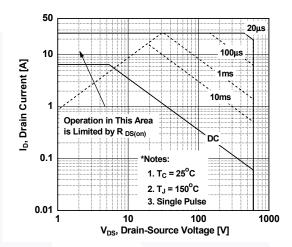


Figure 9. Maximum Drain Current vs. Case Temperature

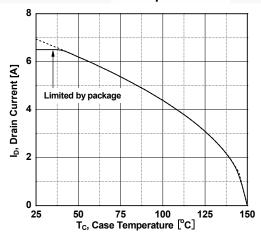
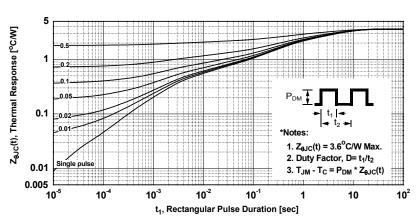


Figure 10. Transient Thermal Response Curve



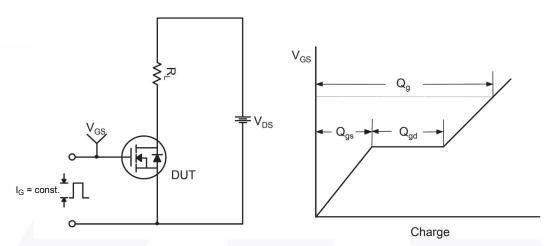


Figure 11. Gate Charge Test Circuit & Waveform



Figure 12. Resistive Switching Test Circuit & Waveforms



Figure 13. Unclamped Inductive Switching Test Circuit & Waveforms

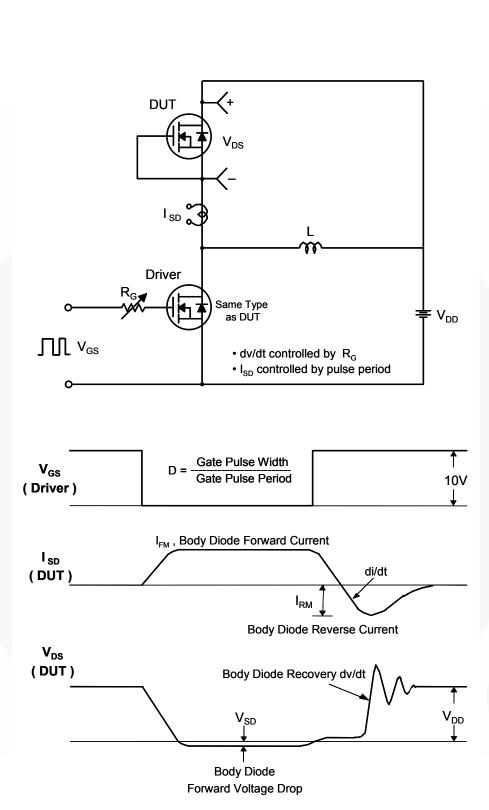


Figure 14. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

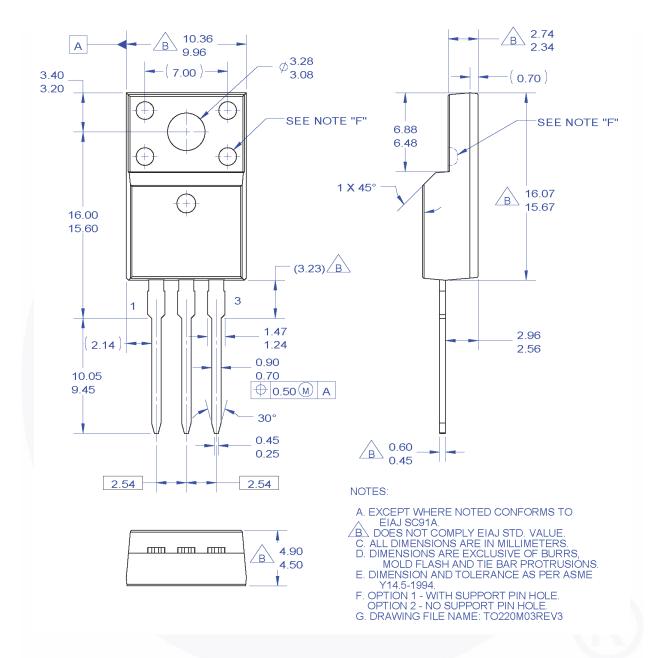


Figure 15. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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