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## FDPF13N50FT N-Channel UniFET<sup>TM</sup> FRFET<sup>®</sup> MOSFET **500 V, 12 A, 540 m**Ω

### **Features**

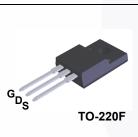
- R<sub>DS(on)</sub> = 420 mΩ (Typ.) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 6 A
- Low Gate Charge (Typ. 30 nC)
- Low C<sub>rss</sub> (Typ. 14.5 pF)
- · 100% Avalanche Tested
- Improved dv/dt Capability
- RoHS Compliant

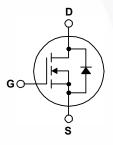
## Applications

- LCD/LED/PDP TV
- Lighting
- Uninterruptible Power Supply

## Description

UniFET<sup>TM</sup> MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET<sup>®</sup> MOSFET has been enhanced by lifetime control. Its trr is less than 100nsec and the reverse dv/ dt immunity is 15V/ns while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





## MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FDPF13N50FT	Unit	
V <sub>DSS</sub>	Drain to Source Voltage		500	V	
V <sub>GSS</sub>	Gate to Source Voltage		±30	V	
ID	Drain Current	- Continuous (T <sub>C</sub> = 25°C)	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)		
		- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C	)	7.2*	— A
I <sub>DM</sub>	Drain Current	- Pulsed	- Pulsed (Note 1)		А
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		684	mJ	
I <sub>AR</sub>	Avalanche Current (Note 1)		(Note 1)	12	А
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		19.5	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		(Note 3)	20	V/ns
P <sub>D</sub>	Dewer Dissinction	(T <sub>C</sub> = 25 <sup>o</sup> C)		42	W
	Power Dissipation	- Derate Above 25°C		0.33	W/ºC
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C	
TI	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C

Drain current limited by maximum junction temperature.

## **Thermal Characteristics**

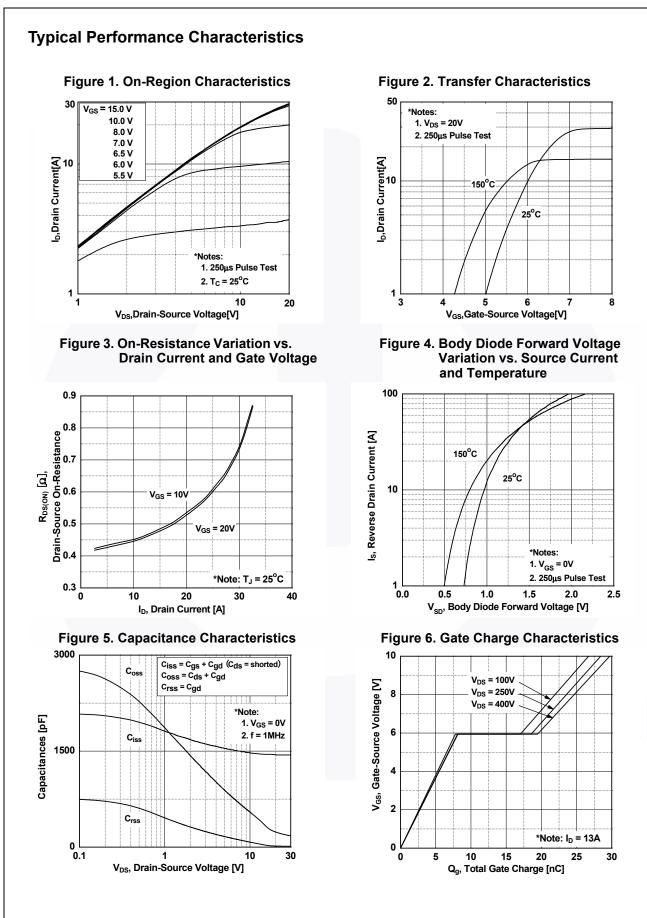
Symbol	Parameter	FDPF13N50FT	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.0	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	- C/VV

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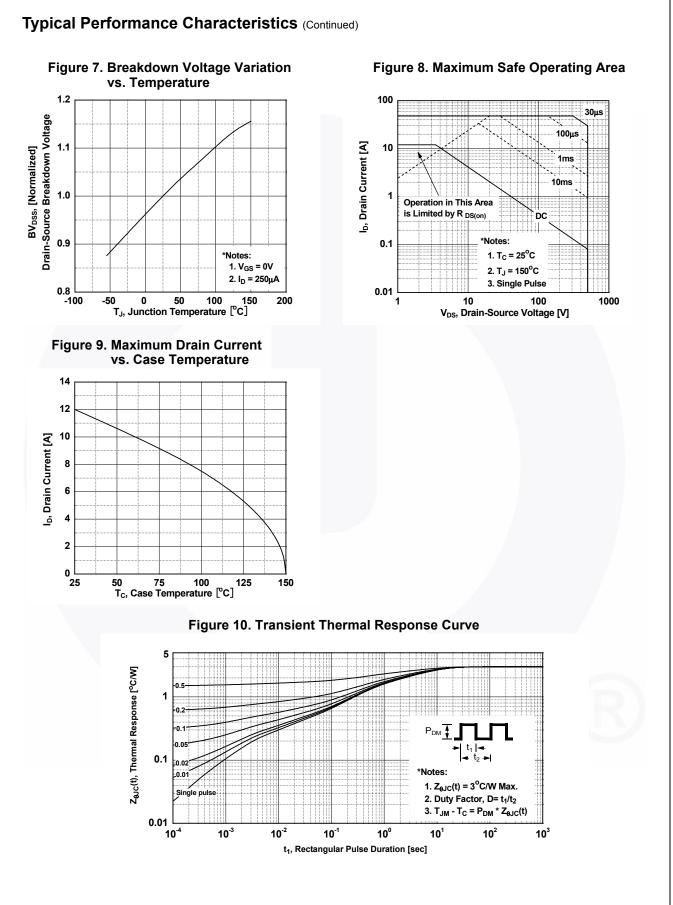
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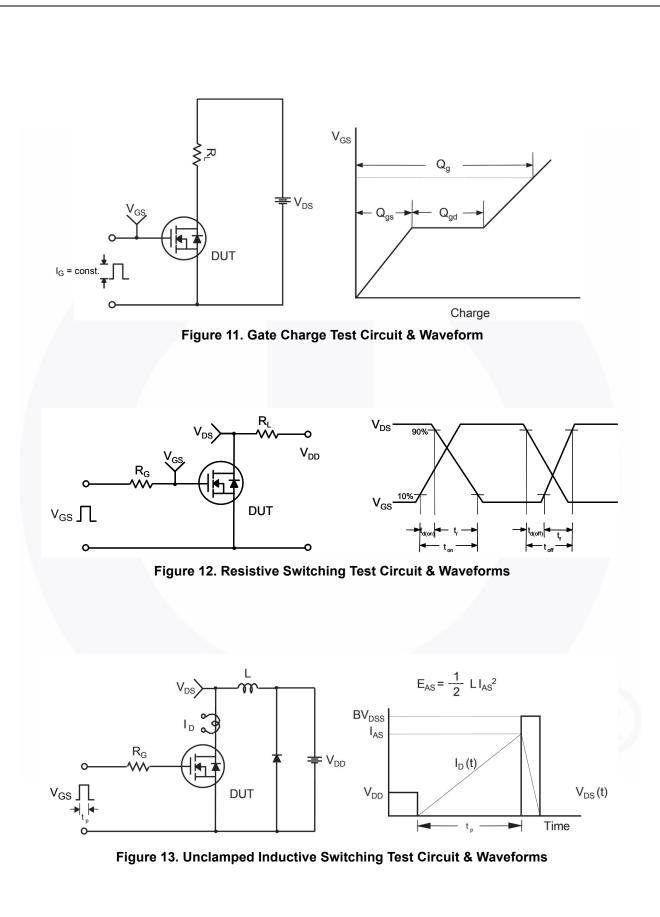
FDPF13N	•		Package	Packing Method	Reel Size	Ta	ape Width	Qu	antity
			TO-220F	<u> </u>			N/A	50	50 units
Electrica	l Chara	<b>Interistics</b> $T_{\rm C} = 25^{\circ}{\rm C}$	unless othe	rwise noted.					
Symbol		Parameter		Test Condition	s	Min.	Тур.	Max.	Unit
Off Charac	teristics								
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage			I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 25 <sup>o</sup> C			-	-	V
ΔBV <sub>DSS</sub>	Breakdown Voltage Temperature			$I_D = 250 \ \mu$ A, Referenced to $25^{\circ}$ C $V_{DS} = 500 \ V, V_{GS} = 0 \ V$		_	0.7		V/ºC
$/\Delta T_J$	Coefficier	Coefficient Zero Gate Voltage Drain Current				-	0.7	- 10	- μA
I <sub>DSS</sub>	Zero Gat								
		_		$= 400 \text{ V}, \text{ T}_{\text{C}} = 125^{\circ}\text{C}$		-	-	100	
GSS	Gate to Body Leakage Current		V <sub>GS</sub>	$s = \pm 20 \text{ V}, \text{ V}_{\text{DS}} = 0 \text{ V}$		-	-	±100	nA
On Charac	teristics								
V <sub>GS(th)</sub>	Gate Thr	eshold Voltage	V <sub>G</sub>	$_{S} = V_{DS}, I_{D} = 250 \ \mu A$		3.0	-	5.0	V
R <sub>DS(on)</sub>	Static Dra	ain to Source On Resistance		<sub>S</sub> = 10 V, I <sub>D</sub> = 6 A		-	0.42	0.54	Ω
9 <sub>FS</sub>	Forward	Transconductance	V <sub>DS</sub>	<sub>s</sub> = 20 V, I <sub>D</sub> = 6 A		-	13.3	-	S
Dynamic C	haracte	ristics							
C <sub>iss</sub>		Capacitance				-	1450	1930	pF
C <sub>oss</sub>	Output C	apacitance		$V_{DS} = 25 V, V_{GS} = 0 V,$ f = 1 MHz $V_{DS} = 400 V, I_D = 13 A,$ $V_{GS} = 10 V$ (Note 4)		-	198	265	pF
C <sub>rss</sub>	Reverse	Transfer Capacitance	T =			-	14.5	22	pF
Q <sub>g(tot)</sub>	Total Gat	e Charge at 10V	Vng			-	30	39	nC
Q <sub>gs</sub>	Gate to S	Source Gate Charge				-	8	-	nC
Q <sub>gd</sub>	Gate to D	Frain "Miller" Charge				-	12	-	nC
Switching	Characte	eristics							
t <sub>d(on)</sub>		Delay Time				-	28	65	ns
t <sub>r</sub>		Rise Time	V <sub>DD</sub> = 250 V, I <sub>D</sub> = 13 A,			-	54	120	ns
t <sub>d(off)</sub>	Turn-Off I	Delay Time	V <sub>G</sub> s	$V_{GS} = 10 \text{ V}, \text{ R}_{G} = 25 \Omega$		-	75	160	ns
t <sub>f</sub>	Turn-Off	Fall Time			(Note 4)	-	47	105	ns
Drain Sour		e Characteristics					<u> </u>		
		Continuous Drain to Source	Diada Far	word Current		-		10	•
I <sub>S</sub>		Pulsed Drain to Source Dio					-	12 48	A
I <sub>SM</sub>		Source Diode Forward Voltag				-	-	1.5	V
V <sub>SD</sub> t <sub>rr</sub>		Recovery Time		$\begin{array}{llllllllllllllllllllllllllllllllllll$		-	154	-	ns
Q <sub>rr</sub>		Recovery Charge				-	0.45		μC

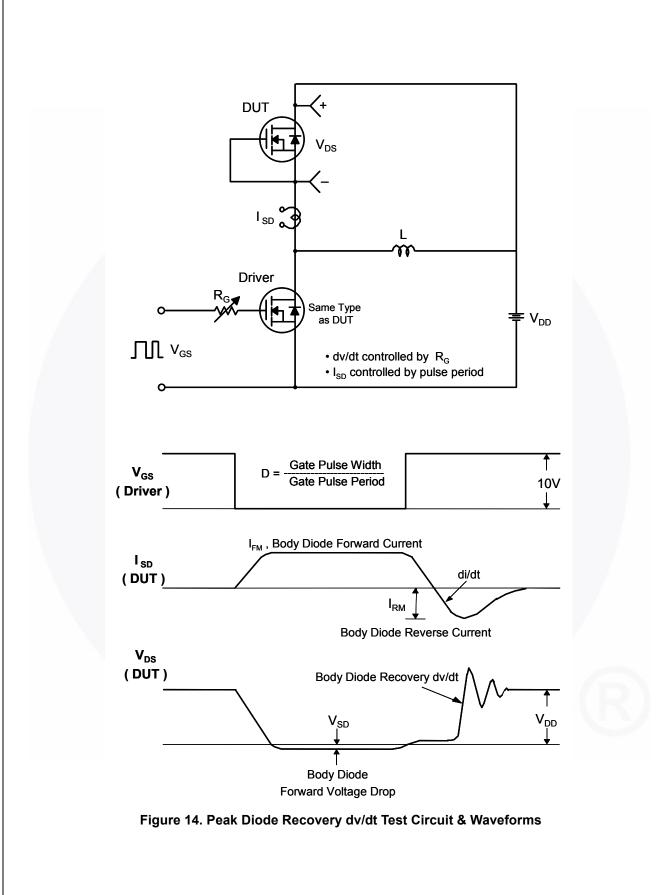
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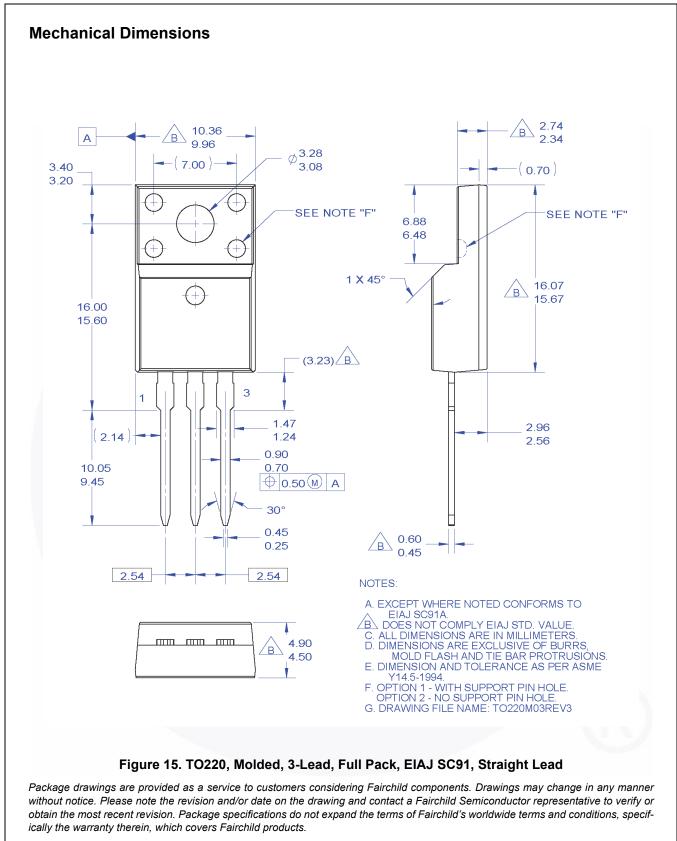


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