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November 2013

#### **FDP80N06**

# N-Channel UniFET<sup>TM</sup> MOSFET 60 V, 80 A, 10 m $\Omega$

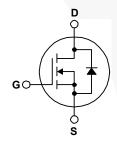
#### **Features**

- $R_{DS(on)}$  = 8.5 m $\Omega$  (Typ.) @  $V_{GS}$  = 10 V,  $I_D$  = 40 A
- Low Gate Charge (Typ. 57nC)
- Low C<sub>rss</sub> (Typ. 145pF)
- · Fast Switching
- · Improved dv/dt Capability
- · RoHS Compliant

#### **Description**

UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





### $\begin{tabular}{lll} \textbf{Absolute Maximum Ratings} & $T_C = 25^{\circ}C$ unless otherwise noted. \end{tabular}$

Symbol		Parameter		Ratings	Units	
V <sub>DSS</sub>	Drain to Source Voltage			60	V	
V <sub>GSS</sub>	Gate to Source Voltage			±20	V	
	Drain Current	- Continuous (T <sub>C</sub> = 25°C)		80	Λ.	
ID	DrainCurrent	- Continuous (T <sub>C</sub> = 100°C)		65	Α	
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	320	Α	
E <sub>AS</sub>	Single Pulsed Avalanche	Energy	(Note 2)	480	mJ	
I <sub>AR</sub>	Avalanche Current		(Note 1)	80	Α	
E <sub>AR</sub>	Repetitive Avalanche Ene	rgy	(Note 1)	17.6	mJ	
dv/dt	Peak Diode Recovery dv/	dt	(Note 3)	4.5	V/ns	
n	Dawer Dissipation	$(T_C = 25^{\circ}C)$		176	W	
$P_{D}$	Power Dissipation	- Derate above 25°C		1.17	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Te	mperature Range		-55 to +175	°C	
TL	Maximum Lead Temperat 1/8" from Case for 5 Seco	<b>O</b> .		300	°C	

#### **Thermal Characteristics**

Symbol	Parameter Ratings		Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.85	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. 62.5		· C/VV

### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP80N06	FDP80N06	TO-220	Tube	N/A	50 units

#### Electrical Characteristics TC = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	cteristics					
$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250\mu A, V_{GS} = 0V, T_J = 25^{\circ}C$	60	-	-	V
ΔBV <sub>DSS</sub> / ΔΤ <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$ , Referenced to $25^{\circ}\text{C}$	-	0.075	-	V/°C
1	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1	μА
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 48V, T_{C} = 150^{\circ}C$	-	-	10	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

#### **On Characteristics**

V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 250\mu A$	2.0		4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 40A$	-	8.5	10	$m\Omega$
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 25V, I_{D} = 40A$	-	67	-	S

#### **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V 25V V 2V	-	2450	3190	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz	-	910	1190	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 – 11011 12	-	145	190	pF

#### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time			- \	32	75	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 30V, I_{D} = 80A$		-	259	528	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_G = 25\Omega$		-	136	282	ns
t <sub>f</sub>	Turn-Off Fall Time		(Note 4)	-	113	236	ns
Q <sub>g(tot)</sub>	Total Gate Charge at 10V	V <sub>DS</sub> = 48V, I <sub>D</sub> = 80A		-	57	74	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>GS</sub> = 10V		-	15	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		(Note 4)	- /	24	-	nC

#### **Drain-Source Diode Characteristics**

I <sub>S</sub>	Maximum Continuous Drain to Source Dioc	Maximum Continuous Drain to Source Diode Forward Current		-	80	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	320	Α
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{SD} = 80A$	-	-	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{GS} = 0V, I_{SD} = 80A$	-	64	/	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	-	127	-	nC

- **Notes:**1: Repetitive Rating: Pulse width limited by maximum junction temperature.
  2: L = 0.15mH,  $I_{AS} = 80A$ ,  $V_{DD} = 50V$ ,  $R_C = 25\Omega$ , Starting  $T_J = 25^{\circ}C$ .
  3:  $I_{SD} \le 80A$ ,  $di/dt \le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$ .
  4: Essentially independent of operating temperature typical characteristics.

#### **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

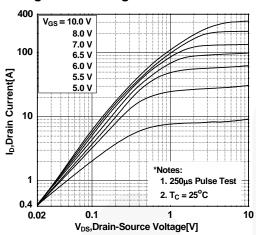
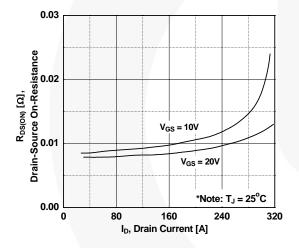


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage



**Figure 5. Capacitance Characteristics** 

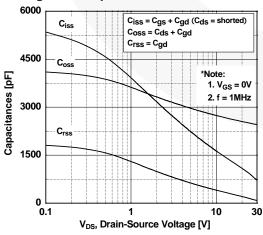


Figure 2. Transfer Characteristics

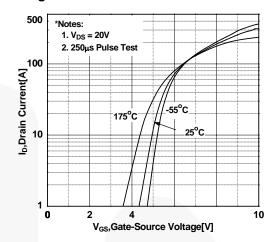
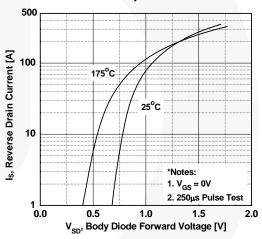
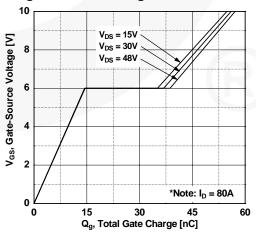


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature



**Figure 6. Gate Charge Characteristics** 



#### Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

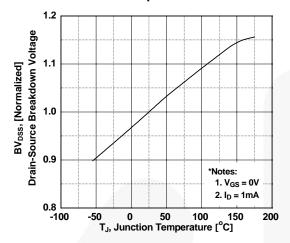


Figure 9. Maximum Safe Operating Area

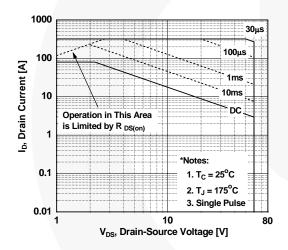


Figure 8. On-Resistance Variation vs. Temperature

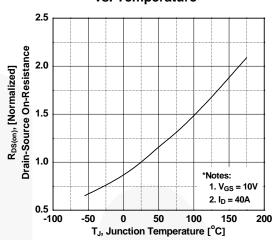


Figure 10. Maximum Drain Current vs. Case Temperature

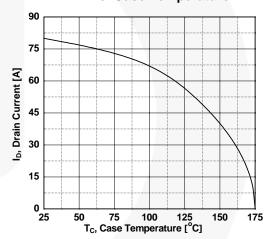


Figure 11. Transient Thermal Response Curve

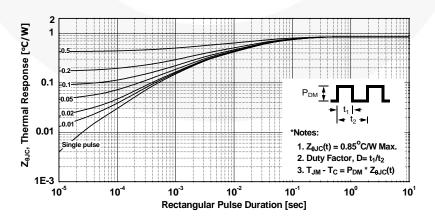


Figure 12. Gate Charge Test Circuit & Waveform

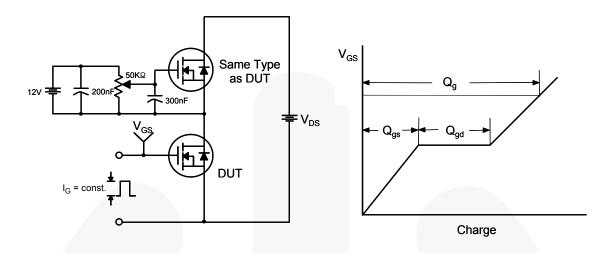


Figure 13. Resistive Switching Test Circuit & Waveforms

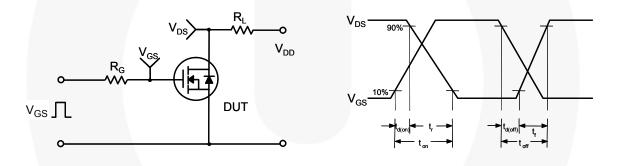
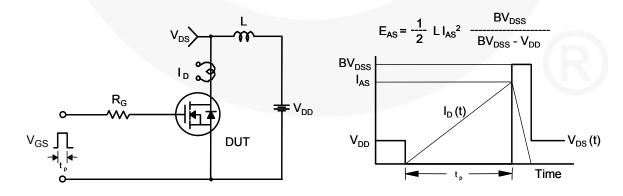


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



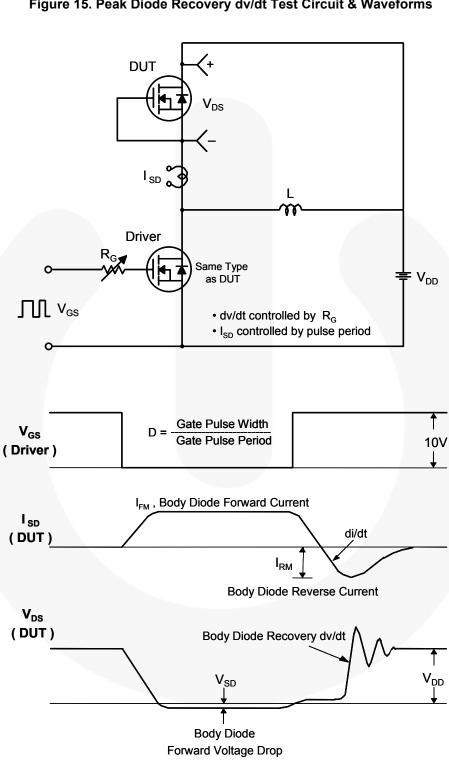


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

#### **Mechanical Dimensions**

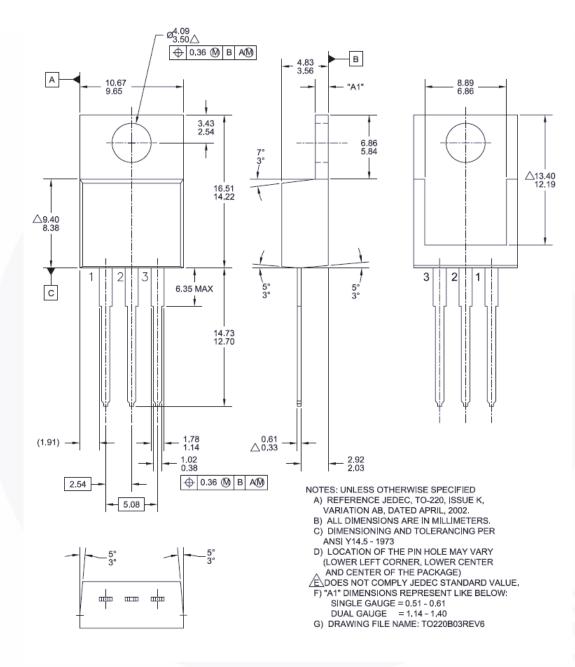


Figure 16. TO-220, Molded, 3Lead, Jedec Variation AB

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