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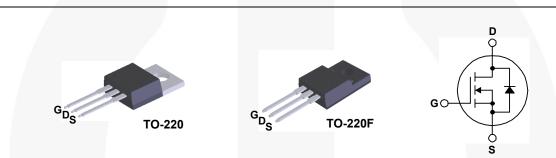
### FQP45N15V2 / FQPF45N15V2 N-Channel QFET<sup>®</sup> MOSFET 150 V, 45 A, 40 mΩ

#### Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

#### Features

- 45 A, 150 V,  ${\sf R}_{{\sf DS}({\sf on})}$  = 40 m $\Omega$  (Max.) @ V\_{{\sf GS}} = 10 V,  ${\sf I}_{\sf D}$  = 22.5 A
- Low Gate Charge (Typ. 72 nC)
- Low Crss (Typ. 135 pF)
- 100% Avalanche Tested



#### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FQP45N15V2	FQPF45N15V2	Unit	
V <sub>DSS</sub>	Drain-Source Voltage			150		
I <sub>D</sub>	Drain Current - Continuous ( $T_C = 25^{\circ}C$	45 45 *		А		
	- Continuous (T <sub>C</sub> = 100	31 31 *		А		
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	180	180 *	А	
V <sub>GSS</sub>	Gate-Source Voltage		± 30		V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	1124		mJ	
I <sub>AR</sub>	Avalanche Current	(Note 1)	45		Α	
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	22		mJ	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5		V/ns	
P <sub>D</sub>	Power Dissipation ( $T_C = 25^{\circ}C$ )		220	66	W	
	- Derate above 25°C	1.47	0.44	W/°C		
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 te	°C		
Τ <sub>L</sub>	Maximum lead temperature for soldering 1/8" from case for 5 seconds	3	°C			

\* Drain current limited by maximum junction temperature

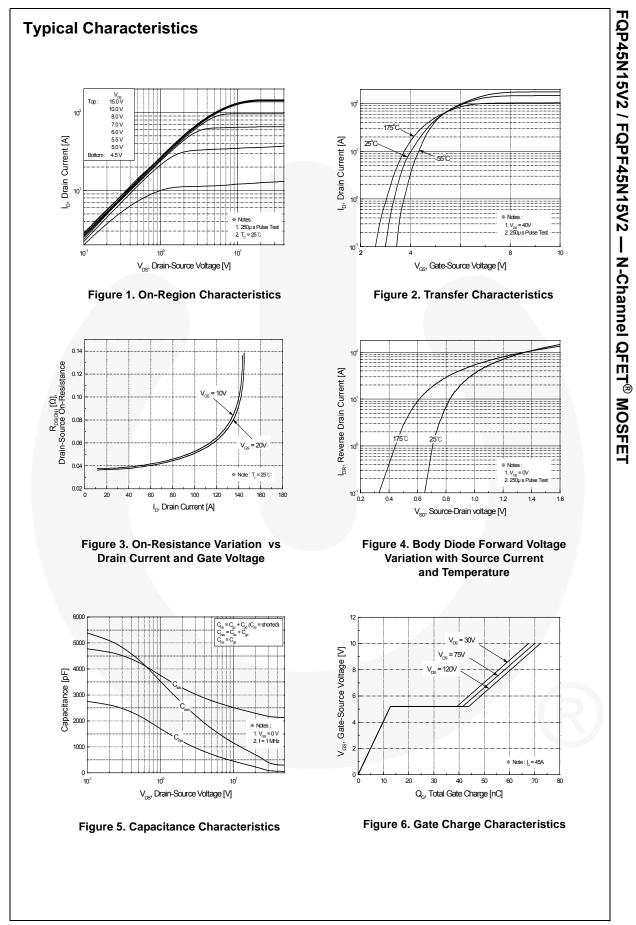
#### **Thermal Characteristics**

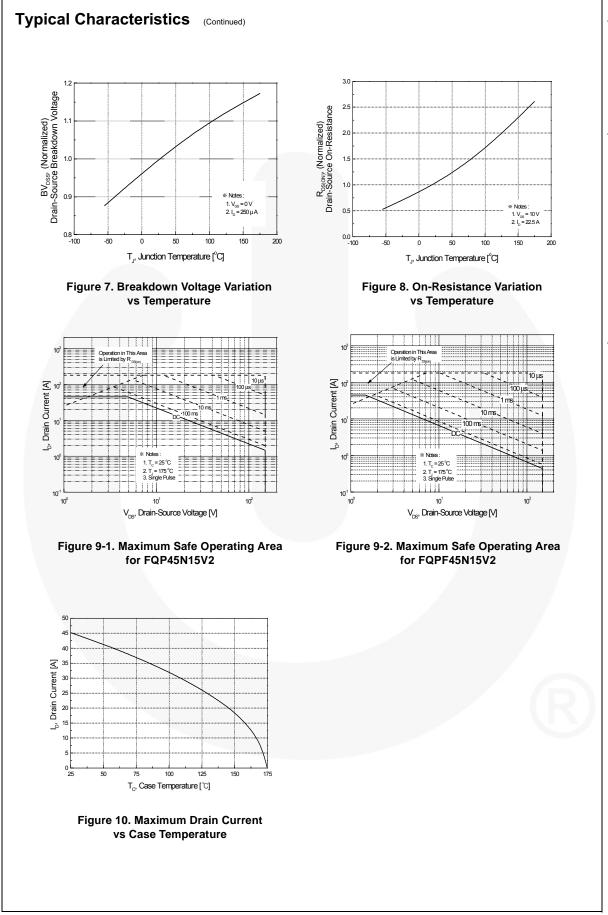
Symbol	Parameter	FQP45N15V2	FQPF45N15V2	Unit °C/W °C/W	
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.68	2.25		
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5			
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	°C/W	

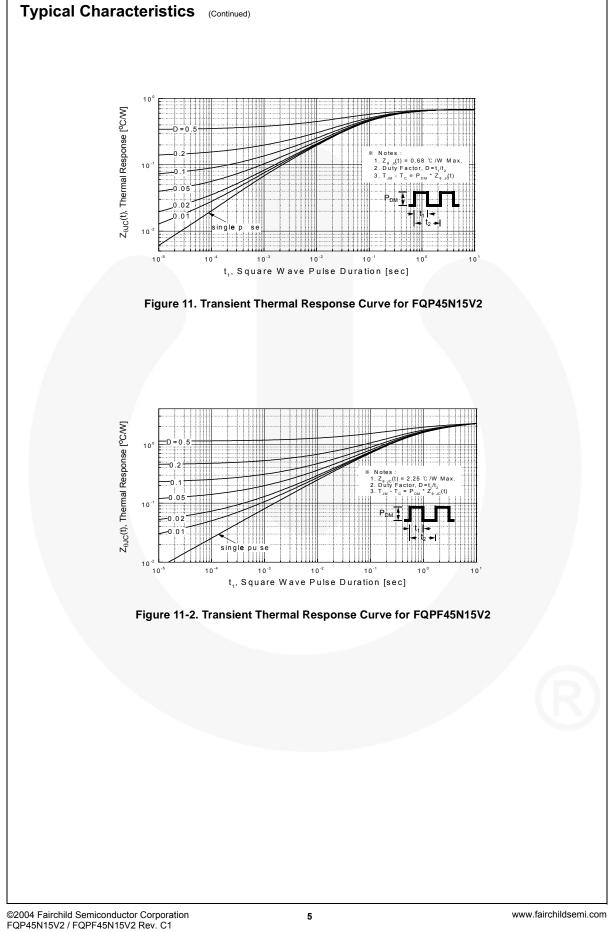
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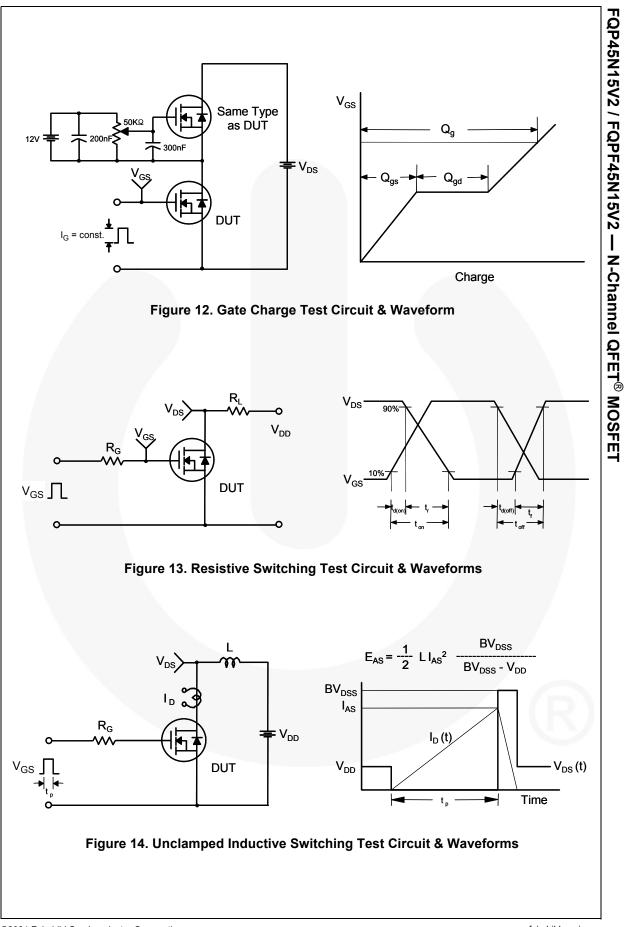
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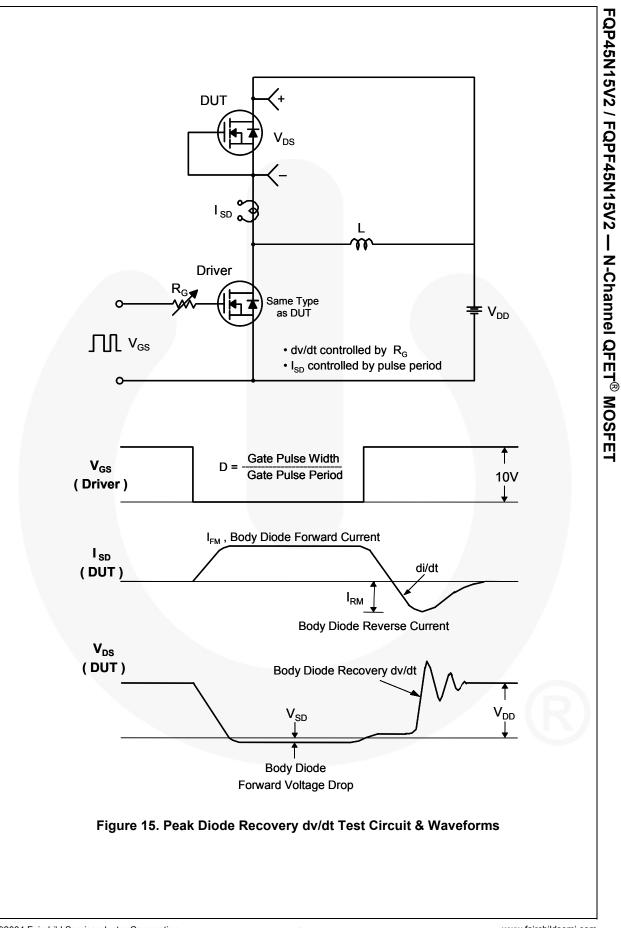
FQP45N15V2 PV245N15 TC		Top Mark	-				Size	Tape Width N/A		Quantity 50 units 50 units	
		PV245N15 TO		-220 Tube		N//	4				
		TO-2	220F Tube N/			Ą	N/A				
lectri	cal C	haracteristics	Γ <sub>c</sub> = 25°C un	less otherw	rise noted.						
Symbol		Parameter			Test Con	ditions		Min	Тур	Max	Unit
Off Cha	racter	istics									
BV <sub>DSS</sub>		Source Breakdown Volt	aqe	V <sub>GS</sub> =	0 V, I <sub>D</sub> = 25	0 μΑ		150			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breako	Breakdown Voltage Temperature Coefficient		$I_D = 250 \ \mu\text{A}$ , Referenced to 25°C				0.21		V/°C	
DSS	Zero Gate Voltage Drain Current		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V					1	μA		
035			$V_{DS} = 120 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$					10	μΑ		
GSSF	Gate-E	Gate-Body Leakage Current, Forward			30 V, V <sub>DS</sub> =					100	nA
GSSR		Body Leakage Current,			-30 V, V <sub>DS</sub>					-100	nA
On Cha	racter	istics			-				II		
V <sub>GS(th)</sub>		hreshold Voltage	_	V <sub>DS</sub> =	V <sub>GS</sub> , I <sub>D</sub> = 2	50 μΑ		2.0		4.0	V
R <sub>DS(on)</sub>	Static I	Drain-Source sistance			10 V, I <sub>D</sub> = 2				0.034	0.04	Ω
JFS	Forwar	d Transconductance		V <sub>DS</sub> =	40 V, I <sub>D</sub> = 2	2.5 A			40		S
Dvnam	ic Cha	racteristics									
C <sub>iss</sub>	1	Capacitance	_	\/	25 V, V <sub>GS</sub> =	· 0 \/			2330	3030	pF
Coss		Capacitance	_	f = 1.0		· U V,			510	670	pF
Crss		e Transfer Capacitance	e	1 1.0					135	176	pF
			_								-
Switchi	ing Ch	aracteristics									
d(on)		n Delay Time		Vpp =	75 V, I <sub>D</sub> = 4	5 A.			22	54	ns
r	Turn-C	n Rise Time		$R_G = 2$		,			232	474	ns
d(off)	Turn-C	ff Delay Time		0					224	458	ns
f	Turn-C	ff Fall Time					(Note 4)		246	502	ns
ე <sub>g</sub>	Total G	ate Charge		V <sub>DS</sub> =	120 V, I <sub>D</sub> =	45 A,			72	94	nC
2 <sub>gs</sub>	Gate-S	ource Charge		V <sub>GS</sub> =	-				13		nC
ጋ <sub>gd</sub>	Gate-D	rain Charge					(Note 4)		31		nC
	ource	Diode Characteri	stics a	nd Mar	vimum R	atings			<u> </u>		
s	-	um Continuous Drain-S								45	Α
SM		um Pulsed Drain-Sourc								180	A
SM SD		Source Diode Forward			0 V, I <sub>S</sub> = 45	A				1.4	V
rsD		e Recovery Time	voltage		0 V, I <u>S</u> = 45				176		ns
n 2m		e Recovery Charge		00	:= 100 A/μs				1.19	_	μC
×rr	Reveis	e necovery charge		ur <sub>F</sub> / ut	100 Aipa				1.19		μΟ

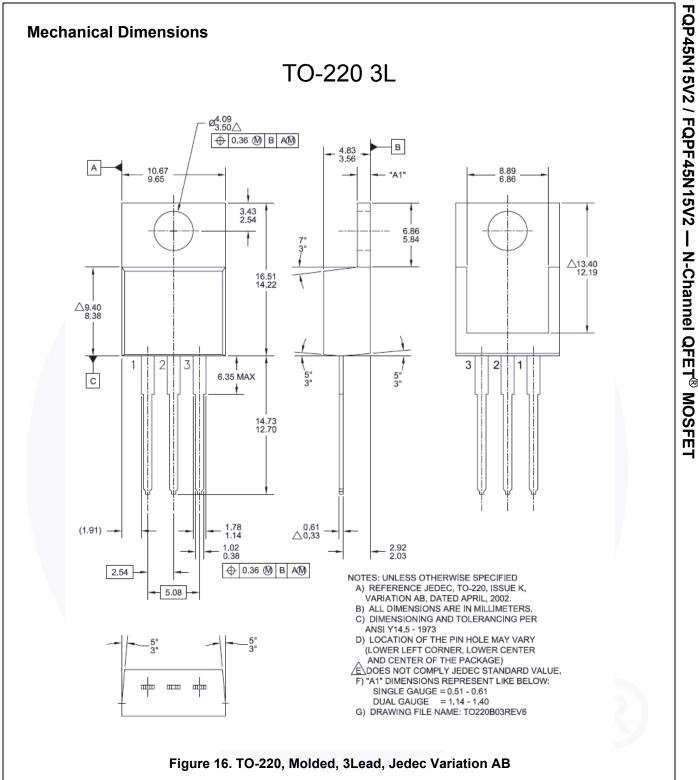










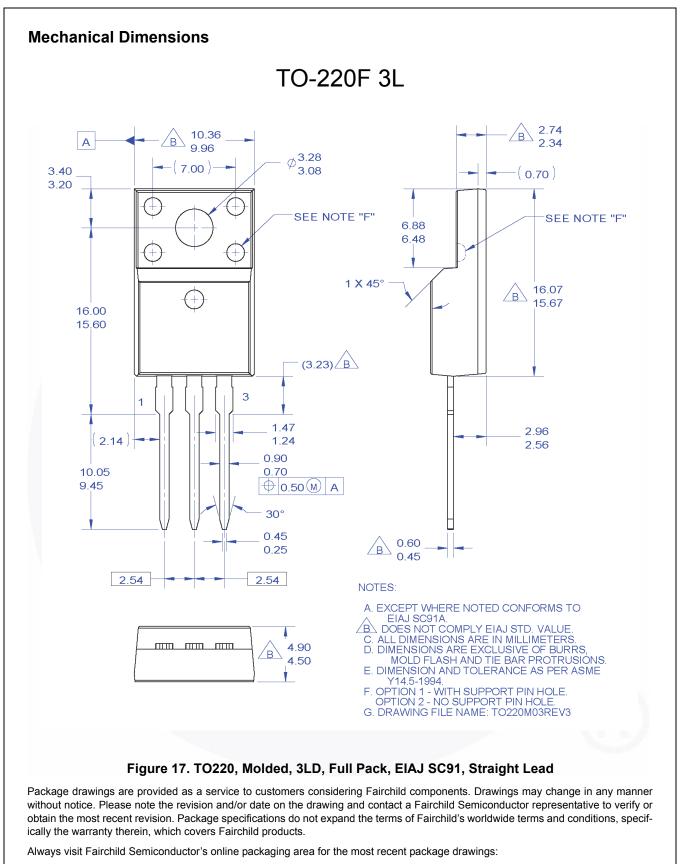


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**Dimension in Millimeters** 



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**Dimension in Millimeters** 

FQP45N15V2 / FQPF45N15V2 —

**N-Channel QFET<sup>®</sup> MOSFET** 



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