

#### Description

The PSM712 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

# 

**PSM712** 

**ESD** Protector

#### Feature

- 400W peak pulse power per line (t<sub>P</sub> = 8/20µs)
- SOT-23 package
- Bidirectional configurations
- Protects one power or I/O port
- ESD protection > 15 kV
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)±15KV(air), ±8KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

#### **Mechanical Characteristics**

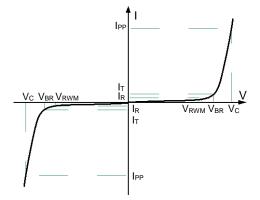
- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

#### **Electronics Parameter**

Symbol	Parameter		
V <sub>RWM</sub>	Peak Reverse Working Voltage		
IR	Reverse Leakage Current @ VRWM		
V <sub>BR</sub>	Breakdown Voltage @ $I_T$		
Iτ	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P <sub>PP</sub>	Peak Pulse Power		
CJ	Junction Capacitance		
lF	Forward Current		
VF	Forward Voltage @ I⊧		

## Applications

- > RS-485
- Security systems
- Automatic teller machines
- HFC systems



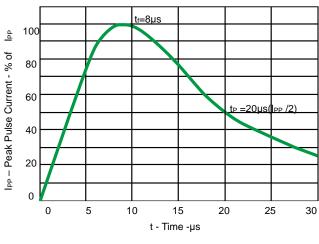
## **PSM712**

## Electrical characteristics per line@25°C( unless otherwise specified)

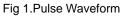
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-off Voltage	Vrwm	Pin 1 or Pin 2 to Pin 3			12	v
		Pin 3 to Pin 1 or Pin 2			7	
Reverse Breakdown Voltage	N/	Pin 1 or Pin 2 to Pin 3 $I_t = 1mA$	13.3			V
	V <sub>BR</sub>	Pin 3 to Pin 1 or Pin 2 $I_t = 1mA$	7.5			
Reverse Leakage Current		Pin 1 or Pin 2 to Pin 3 V <sub>RWM</sub> =12V			1.0	μA
	IR	Pin 3 to Pin 1 or Pin 2 $V_{RWM} = 7V$			1.0	
Max. Peak Pulse Current		Pin 1 or Pin 2 to Pin 3		15		
	IPP	Pin 3 to Pin 1 or Pin 2		15		A
Clamping Voltage	Vc	Pin 1 or Pin 2 to Pin 3 $I_{PP} = 12A$		28	32	v
		Pin 3 to Pin 1 or Pin 2 $I_{PP} = 12A$		19	22	
Junction Capacitance	Cj	$V_R=0V$ f = 1MHz		30	45	pF

## Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Unidirectional Peak Pulse Power	P <sub>pp</sub>	400	W
Operating Temperature	TJ	-55 to +150	°C
Storage Temperature	Tstg	-55 to +150	°C



**Typical Characteristics** 



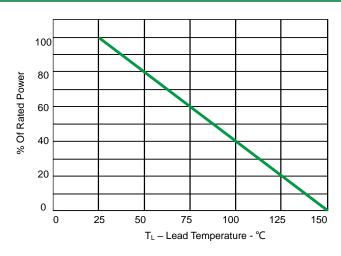


Fig 2.Power Derating Curve

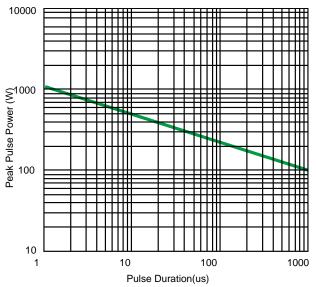
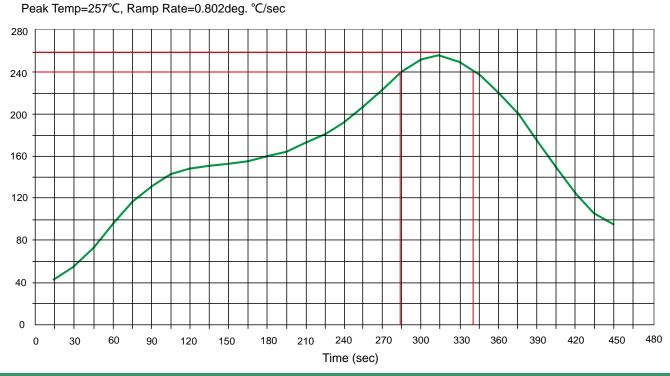


Fig 3. Non Repetitive Peak Pulse Power vs. Pulse time

#### **Solder Reflow Recommendation**



### **PCB Design**

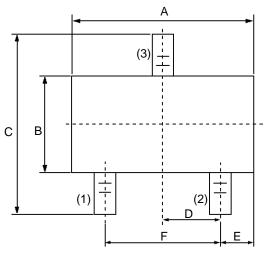
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

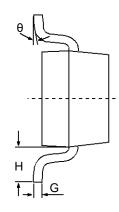
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

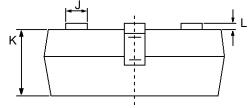
**PSM712** 

## PSM712

## Product dimension(SOT-23)

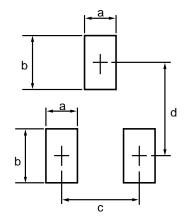






Dim	Millimeters		Inches	
Dim	MIN	MAX	MIN	MAX
А	2.80	3.00	0.1102	0.1197
В	1.20	1.40	0.0472	0.0551
С	2.10	2.50	0.0830	0.0984
D	0.89	1.02	0.0350	0.0401
E	0.45	0.60	0.0177	0.0236
F	1.78	2.04	0.0701	0.0807
G	0.085	0.177	0.0034	0.0070
н	0.45	0.60	0.0180	0.0236
J	0.37	0.50	0.0150	0.0200
к	0.89	1.11	0.0350	0.0440
L	0.013	0.100	0.0005	0.0040
θ	0°	10°	0°	10°

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Dim	Millimeters			
Dim	MIN	МАХ		
а		0.7		
b		1.2		
с		2.04		
d		2.2		

# Ordering information

Device	Package	Shipping
PSM712	SOT-23 (Pb-Free)	3000 / Tape & Reel

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