

# LR1117C

# DESCRIPTION

LR1117C is a series of low dropout three terminal regulators with a dropout of 1.3V at 1A load current. LR1117C features a very low standby current 2mA compared to 5mA of competitor.

Other than a fixed version, Vout = 1.2V, 1.5V, 1.8V, 2.5V, 3.3V, 5V, and 12V, LR1117C has an adjustable version, which can provide an output voltage from 1.25 to 12V with only two external resistors.

LR1117C offers thermal shut down and current limit functions, to assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within  $\pm 2\%$ . Other output voltage accuracy can be customized on demand, such as  $\pm 1\%$ 

LR1117C is available in SOT-223,TO-252 power package.

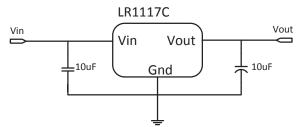
# **FEATURES**

- Other than a fixed version and an adjustable version, output value can be customized on demand.
- Maximum output current is 1A
- Range of operation input voltage: Max 12V
- Standby current: 2mA (typ.)
- Line regulation: 0.1%/V (typ.)
- Load regulation: 10mV (typ.)
- Environment Temperature: -40°C~85°C

#### **APPLICATIONS**

- Power Management for Computer Mother Board, Graphic Card
- BLD Monitor and BLD TV
- DVD Decode Board
- ADSL Modem
- Post Regulators for Switching Supplies

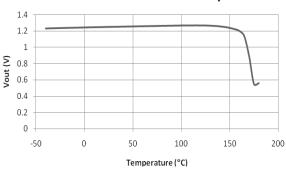
# **TYPICAL APPLICATION**



Application circuit of LR1117C fixed version

NOTE: Input capacitor (Cin=10uF) and Output capacitor (Cout=10uF) are recommended in all application circuit. Tantalum capacitor is recommended.

# TYPICAL ELECTRICAL CHARACTERISTIC



#### LR1117C-ADJ Vout Vs. Temp



#### **ORDERING INFORMATION**

LR1117C <u>X</u> <u>X</u>	<ul> <li>X X</li> <li>⊥ Temp. Range &amp; Rohs Std.</li> <li>X: 85C &amp; Pb-free Rohs Std,Output voltage accuracy within ±2%</li> </ul>
	Output Voltage: 12·····1.2V 15·····1.5V 18·····1.8V 25·····2.5V 33·····3.3V 50·····5.0V ·····ADJ
	Package Type: S: SOT-223 D: TO-252

# PIN CONFIGURATION AND MARKING Pin Description:

	SOT-223					
1117 B	Pin No. Symbol Definitio		Definition			
XX YYZZ	1	1 Vss Ground				
	2	Vout	Output			
$\begin{array}{c} \square \square \square \\ 1 & 2 & 3 \end{array}$	3	Vin	Input			
		TO-2	252			
	Pin No.	Symbol	Definition			
1117 B	1	Vss/ADJ	Ground/Adjustable			
XX YYZZ	2	Vout	Output			
	3	Vin	Input			
$\begin{bmatrix} 1 & 2 \\ 1 & 3 \end{bmatrix}$						

1117: Product code B: Fab code XX: Output voltage YY: Lot No. ZZ: Date code

# Marking information when SOT-223 adjustable Version:

	SOT-223			
<b>B</b> 1117	Pin No.	Symbol	Definition	
AABB CCD	1	ADJ	Adjustable	
	2	Vout	Output	
1 2 3	3	Vin	Input	

#### B: Fab code 1117: Product code

AA: Manufacture weeks BB: Manufacture LOT No.

CC: Output Voltage Value D: T emp. Range&Rohs Std

#### **ABSOLUTE MAXIMUM RATING**

Parameter		Value	
Max Input Voltage		15V <sup>①</sup>	
Max Operating Junction Temperature(Tj)		150°C	
Ambient Operating Temperature(Ta)		-40°C – 85°C	
Package Thermal Resistance	SOT-223	20°C / W	
	TO-252	10°C / W	
Storage Temperature(Ts)		-40°C - 150°C	
Lead Temperature & Time		260°C, 10S	

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

#### **RECOMMENDED WORK CONDITIONS**

Parameter	Value
Input Voltage Range	Max. 12V <sup>®</sup>
Operating Junction Temperature(Tj)	-20°C –125°C

<sup>®</sup>Exceptional for LR1117C -12V, the maximum input voltage for LR1117C-12V is 20V.



#### **ELECTRICAL CHARACTERISTICS**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Vref	Reference Voltage	LR1117-ADJ	1.225	1.25	1.275	V
viei	Reference voltage	10mA≤lout≤1A , Vin=3.25V	1.225	1.25	1.275	v
		LR1117-1.2V	1.176	1.2	1.224	V
		0≤lout≤1A , Vin=3.2V	1.170	1.2	1.221	
		LR1117-1.5V	1.47	1.5	1.53	v
		0≤lout≤1A , Vin=3.5V LR1117-1.8V				
		0≤lout≤1A , Vin=3.8V	1.764	1.8	1.836	V
		LR1117-2.5V				
Vout	Output Voltage	0≤lout≤1A , Vin=4.5V	2.45	2.5	2.55	V
		LR1117-3.3V	2.224		2.266	.,
		0≤lout≤1A , Vin=5.3V	3.234	3.3	3.366	V
		LR1117-5.0V	4.9	5	5.1	V
		0≤lout≤1A , Vin=7.0V	4.9	5	5.1	v
		LR1117-12.0V	11.76	12	12.24	v
		0≤lout≤1A , Vin=14V				
		LR1117-1.2V		0.1	0.2	%/\
		lout=10mA, 2.7V≤Vin≤10V				,
		LR1117-ADJ		0.1	0.2	%/\
		Iout=10mA, 2.75V≤Vin≤12V				
		LR1117-1.5V out=10mA, $3.0V \le Vin \le 12V$		0.1	0.2	%/\
		LR1117-1.8V				
		lout=10mA, $3.3V \le Vin \le 12V$		0.1	0.2	%/\
ΔVout	Line Regulation	LR1117-2.5V				
Avout				0.1	0.2	%/\
		lout=10mA, $4.0V \le Vin \le 12V$				
		LR1117-3.3V		0.1	0.2	%/\
		lout=10mA, 4.8V $\leq$ Vin $\leq$ 12V				
		LR1117-5.0V		0.1	0.2	%/\
		lout=10mA, $6.5V \le Vin \le 12V$		-		
		LR1117-12.0V		0.1	0.2	%/\
		lout=10mA, 13.5V $\leq$ Vin $\leq$ 20V		0.1	0.2	/0/ \
		LR1117-1.2V		10	30	m)
		Vin =2.7V, 10mA $\leq$ lout $\leq$ 1A		10	30	m۱
		LR1117-ADJ		10	20	
		Vin =2.75V, 10mA $\leq$ Iout $\leq$ 1A		10	30	mV
		LR1117-1.5V		10	20	
		Vin=3.0V, 10mA $\leq$ Iout $\leq$ 1A		10	30	mV
		LR1117-1.8V		10	30	mV
		Vin=3.3V, 10mA $\leq$ lout $\leq$ 1A		10	50	IIIV
ΔVout	Load Regulation	LR1117-2.5V				
		Vin=4.0V, 10mA $\leq$ lout $\leq$ 1A		10	30	mV
		LR1117-3.3V			1	
		Vin=4.8V, 10mA $\leq$ lout $\leq$ 1A		10	30	mV
		LR1117-5.0V		10	30	mV
		Vin=6.5V, $10mA \le lout \le 1A$				
		LR1117-12.0V		10	30	mV
	1	Vin=13.5V, $10mA \le Iout \le 1A$			50	



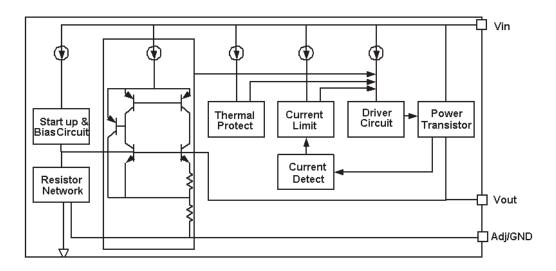


## **ELECTRICAL CHARACTERISTICS continued**

						Tj=25°C
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
	lout=100mA		1.23	1.3	V	
Vdrop	Vdrop Dropout Voltage	lout=1A		1.3	1.5	V
Ilimit	Current Limit	Vin-Vout=2V, Tj=25°C	1			А
SVR	Supply Voltage Rejection	f = 120Hz, VIN – VOUT = 3V + 1VPP Ripple		60		dB
Imin	Minimum Load Current	LR1117-ADJ		2	10	mA
		LR1117-1.2V, Vin =10V	1	2	5	mA
		LR1117-1.5V, Vin =11V	1	2	5	mA
		LR1117-1.8V, Vin =12V	1	2	5	mA
lq	Quiescent Current	LR1117-2.5V,Vin =12V	1	2	5	mA
		LR1117-3.3V, Vin =12V	1	2	5	mA
		LR1117-5.0V, Vin =12V	1	2	5	mA
		LR1117-12.0V, Vin =20V	1	2	5	mA
IAdj	Adjust Pin Current	LR1117-ADJ Vin =5V, 10mA $\leq$ Iout $\leq$ 1A	35	55	120	uA
Ichange	ladj change	LR1117-ADJ Vin =5V, 10mA $\leq$ Iout $\leq$ 1A		0.2	10	uA
ΔV/ΔΤ	Temperature coefficient			±100		ppm
		SOT-223		20		
$\theta_{\text{JC}}$	Thermal Resistance	TO-252		10		°C/W
	Thermal Resistance	SOT-223 (No heat sink)		136		
$\theta_{JA}$	Junction-to-Ambient (No air flow)	TO-252 (No heat sink)		92		°C/W

Note1: All test are conducted under ambient temperature 25°C and within a short period of time 20ms

Note2: Load current smaller than minimum load current of LR1117C-ADJ will lead to unstable or oscillation output. **BLOCK DIAGRAM** 





#### **DETAILED DESCRIPTION**

LR1117C is a series of low dropout voltage, three terminal regulators. Its application circuit is very simple: the fixed version only needs two capacitors and the adjustable version only needs two resistors and two capacitors to work. It is composed of some modules including start-up circuit, bias circuit, bandgap, thermal shutdown, current limit, power transistors and its driver circuit and so on.

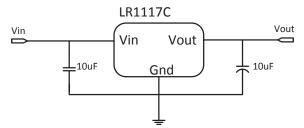
The thermal shut down modules can assure chip and its application system working safety when the junction temperature is larger than 140°C.

The bandgap module provides stable reference voltage, whose temperature coefficient is compensated by careful design considerations. The temperature coefficient is under 100 ppm/°C. And the accuracy of output voltage is guaranteed by trimming technique.

#### **TYPICAL APPLICATION**

LR1117C has an adjustable version and ix fixed versions (1.2V, 1.5V, 1.8V, 2.5V, 3.3V, 5V and 12V)

#### **Fixed Output Voltage Version**

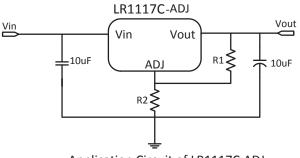


Application circuit of LR1117C fixed version

- 1) Recommend using 10uF tan capacitor or MLCC capacitor as bypass capacitor (C1) for all application circuit.
- 2) Recommend using 10uF tan capacitor MLCC capacitor to assure circuit stability.
- 3) Capacitor ESR range:  $3m\Omega \sim 22\Omega$

#### Adjustable Output Voltage Version

LR1117C-ADJ provides a 1.25V reference voltage. Any output voltage between 1.25V~12V can be achievable by choosing two external resistors (schematic is shown below), R1 and R2



Application Circuit of LR1117C-ADJ

The output voltage of adjustable version follows the equation: Vout= $1.25 \times (1+R2/R1)+IAdj \times R2$ . We can ignore IAdj because IAdj (about 50uA) is much less than the current of R1 (about 2~10mA).



- 1) To meet the minimum load current (>10mA) requirement, R1 is recommended to be 125ohm or lower. As LR1117C-ADJ can keep itself stable at load current about 2mA, R1 is not allowed to be higher than 625ohm.
- 2) Using a bypass capacitor ( $C_{ADJ}$ ) between the ADJ pin and ground can improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. The impedance of  $C_{ADJ}$  should be less than R1 to prevent ripple from being amplified. As R1 is normally in the range of  $100\Omega^{\sim}500\Omega$ , the value of  $C_{ADJ}$  should satisfy this equation:  $1/(2\pi x f_{ripple} \times C_{ADJ})$ <R1.

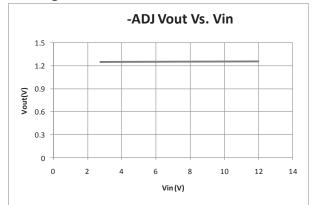
#### THERMAL CONSIDERATIONS

We have to take heat dissipation into great consideration when output current or differential voltage of input and output voltage is large. Because in such cases, the power dissipation consumed by LR1117 is very large. LR1117 series uses SOT-223 package type and its thermal resistance is about  $20^{\circ}$ C/W. And the copper area of application board can affect the total thermal resistance. If copper area is 5cm\*5cm (two sides), the resistance is about  $30^{\circ}$ C/W. So the total thermal resistance is about  $20^{\circ}$ C/W +  $30^{\circ}$ C/W. We can decrease total thermal resistance by increasing copper area in application board. When there is no good heat dissipation copper are in PCB, the total thermal resistance will be as high as  $120^{\circ}$ C/W, then the power dissipation of LR1117 could allow on itself is less than 1W. And furthermore, LR1117 will work at junction temperature higher than  $125^{\circ}$ C under such condition and no lifetime is guaranteed.

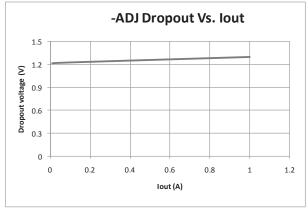
#### **TYPICAL PERFORMANCE CHARACTERISTICS**

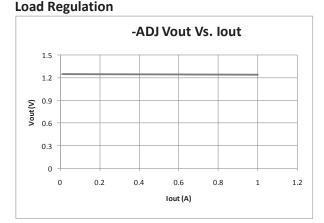
T=25°C unless specified.

#### Line Regulation

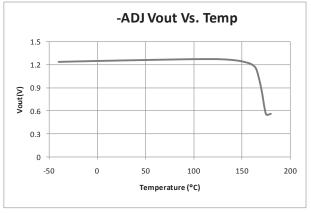


#### **Dropout Voltage**



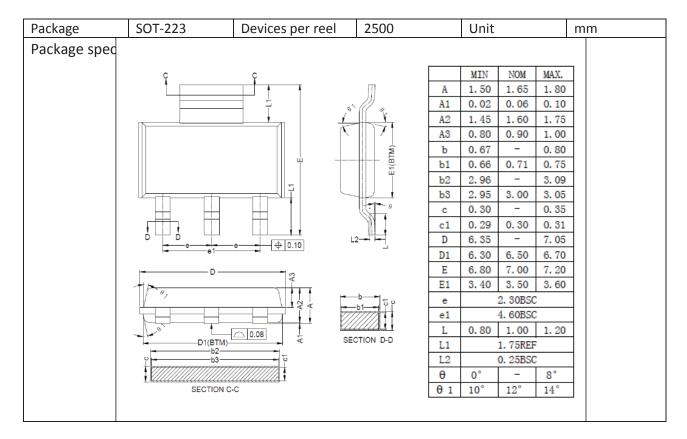


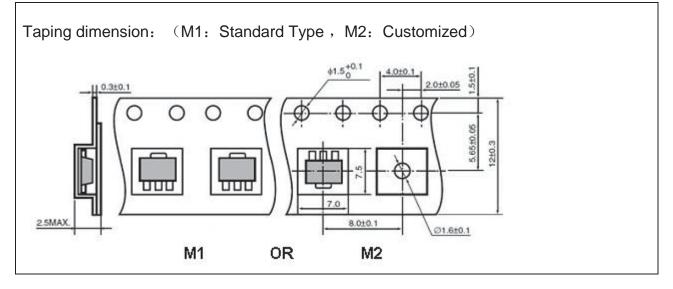
#### Thermal performance with OTP



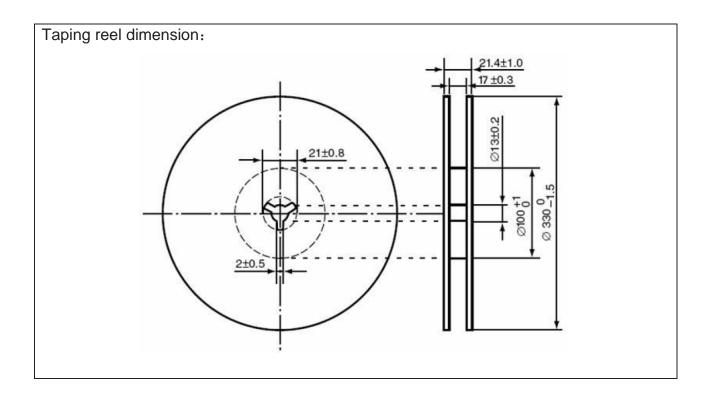


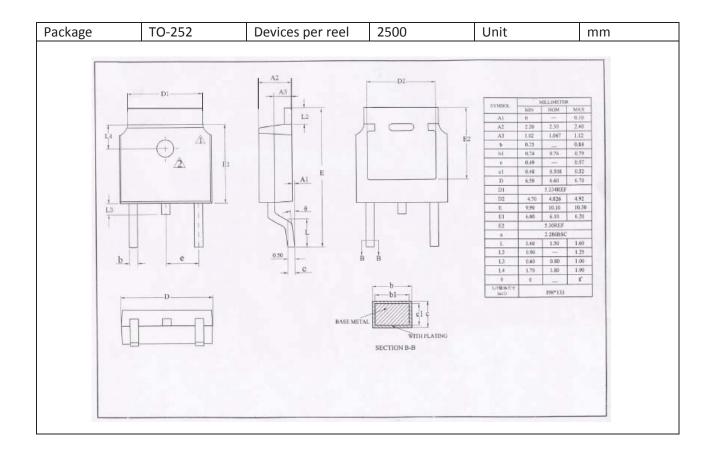
## PACKAGE OUTLINE





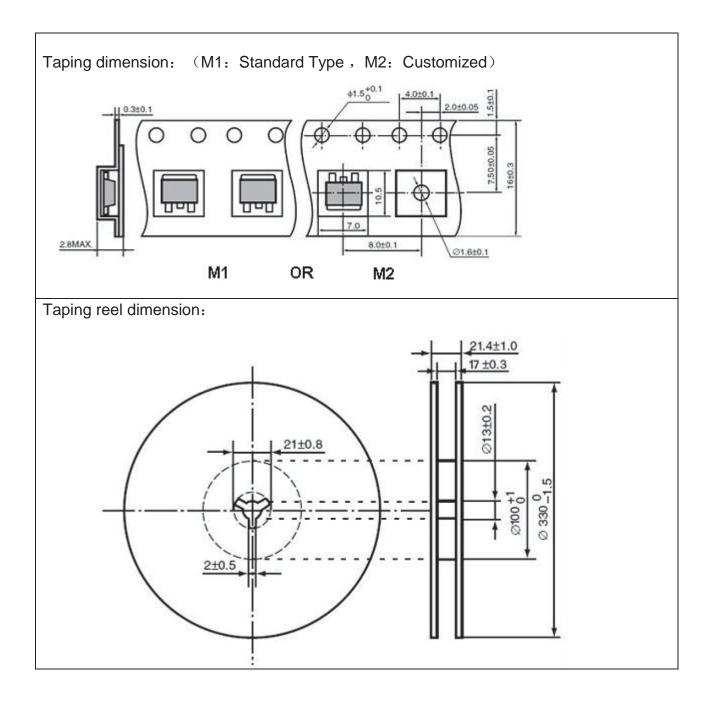














## Disclaimer

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