

# Dual Unbuffered Inverter

## NL27WZU04

The NL27WZU04 is a high performance dual unbuffered inverter operating from a 1.65 to 5.5 V supply.

### Features

- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation
- Input Overvoltage Tolerant up to 5.5 V
- $I_{OFF}$  Supports Partial Power Down Protection
- Sink 24 mA at 3.0 V
- Available in SC-88, SC-74 and UDFN6 Packages
- Chip Complexity < 100 FETs
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

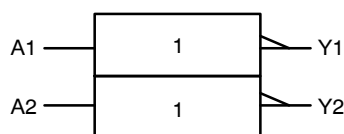
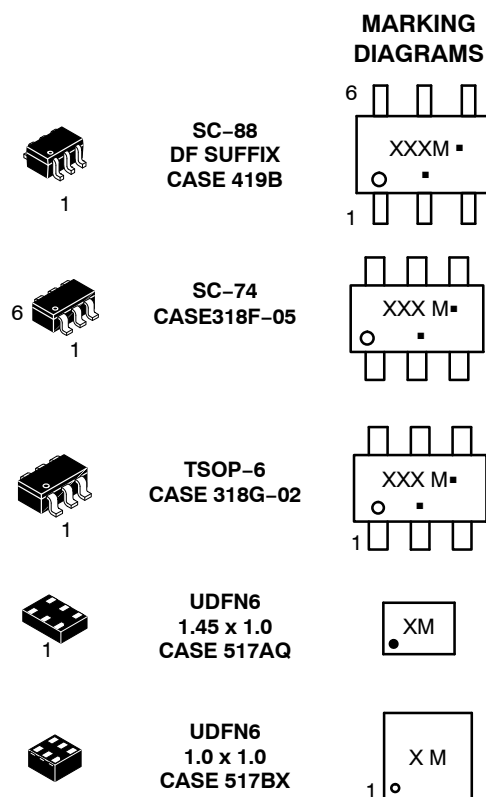


Figure 1. Logic Symbol



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X, XXX = Specific Device Code  
 M = Date Code\*  
 A = Assembly Location  
 Y = Year  
 W = Work Week  
 ■ = Pb-Free Package

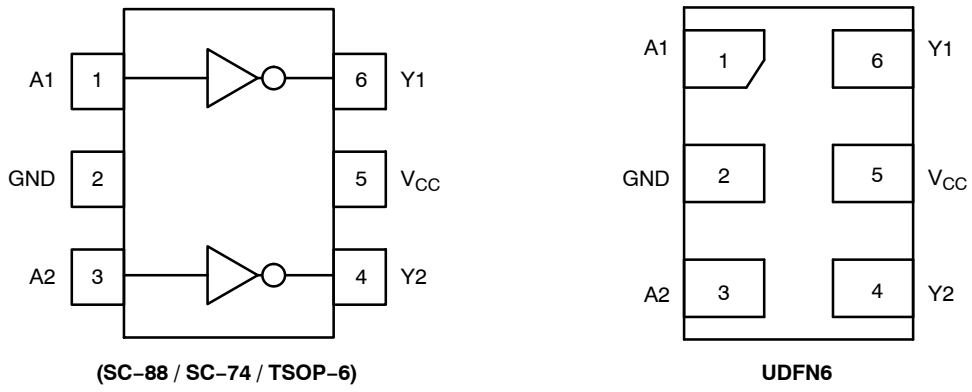
(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

### ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

# NL27WZU04



(SC-88 / SC-74 / TSOP-6)

UDFN6

Figure 2. Pinout (Top View)

## PIN ASSIGNMENT

| Pin | Function        |
|-----|-----------------|
| 1   | A1              |
| 2   | GND             |
| 3   | A2              |
| 4   | Y2              |
| 5   | V <sub>CC</sub> |
| 6   | Y1              |

## FUNCTION TABLE

| A Input | Y Output |
|---------|----------|
| L       | H        |
| H       | L        |

# NL27WZU04

## MAXIMUM RATINGS

| Symbol                              | Characteristics  | Value                         | Unit |
|-------------------------------------|--|-------------------------------|------|
| V <sub>CC</sub>                     | DC Supply Voltage<br>SC-88 (NLV), TSOP-6<br>SC-88, SC-74, UDFN6  | -0.5 to +7.0<br>-0.5 to +6.5  | V    |
| V <sub>IN</sub>                     | DC Input Voltage<br>SC-88 (NLV), TSOP-6<br>SC-88, SC-74, UDFN6   | -0.5 to +7.0<br>-0.5 to +6.5  | V    |
| V <sub>OUT</sub>                    | DC Output Voltage  | -0.5 to V <sub>CC</sub> + 0.5 | V    |
| I <sub>IK</sub>                     | DC Input Diode Current<br>V <sub>IN</sub> < GND  | -50                           | mA   |
| I <sub>OK</sub>                     | DC Output Diode Current  | ±50                           | mA   |
| I <sub>OUT</sub>                    | DC Output Source/Sink Current  | ±50                           | mA   |
| I <sub>CC</sub> or I <sub>GND</sub> | DC Supply Current per Supply Pin or Ground Pin   | ±100                          | mA   |
| T <sub>STG</sub>                    | Storage Temperature Range  | -65 to +150                   | °C   |
| T <sub>L</sub>                      | Lead Temperature, 1 mm from Case for 10 secs   | 260                           | °C   |
| T <sub>J</sub>                      | Junction Temperature Under Bias  | +150                          | °C   |
| θ <sub>JA</sub>                     | Thermal Resistance (Note 2)<br>SC-88<br>SC-74<br>UDFN6   | 377<br>320<br>154             | °C/W |
| P <sub>D</sub>                      | Power Dissipation in Still Air<br>SC-88<br>SC-74<br>UDFN6  | 332<br>300<br>812             | mW   |
| MSL                                 | Moisture Sensitivity   | Level 1                       | -    |
| F <sub>R</sub>                      | Flammability Rating<br>Oxygen Index: 28 to 34  | UL 94 V-0 @ 0.125 in          | -    |
| V <sub>ESD</sub>                    | ESD Withstand Voltage (Note 3)<br>Human Body Model<br>Charged Device Model<br>(NLV) Charged Device Model | 2000<br>1000<br>N/A           | V    |
| I <sub>Latchup</sub>                | Latchup Performance (Note 4)<br>(NLV)  | ±500<br>±100                  | mA   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri-stated.
2. Measured with minimum pad spacing on an FR4 board, using 10mm-by-1inch, 2 ounce copper trace no air flow per JESD51-7.
3. HBM tested to ANSI/ESDA/JEDEC JS-001-2017. CDM tested to EIA/JESD22-C101-F. JEDEC recommends that ESD qualification to EIA/JESD22-A115-A (Machine Model) be discontinued per JEDEC/JEP172A.
4. Tested to EIA/JESD78 Class II.

## RECOMMENDED OPERATING CONDITIONS

| Symbol                          | Characteristics  | Min              | Max                 | Unit |
|---------------------------------|--|------------------|---------------------|------|
| V <sub>CC</sub>                 | Positive DC Supply Voltage   | 1.65             | 5.5                 | V    |
| V <sub>IN</sub>                 | DC Input Voltage   | 0                | 5.5                 | V    |
| V <sub>OUT</sub>                | DC Output Voltage  | 0                | V <sub>CC</sub>     |      |
| T <sub>A</sub>                  | Operating Temperature Range  | -55              | +125                | °C   |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise and Fall Time<br>V <sub>CC</sub> = 1.65 V to 1.95 V<br>V <sub>CC</sub> = 2.3 V to 2.7 V<br>V <sub>CC</sub> = 3.0 V to 3.6 V<br>V <sub>CC</sub> = 4.5 V to 5.5 V | 0<br>0<br>0<br>0 | 20<br>20<br>10<br>5 | ns/V |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

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## DC ELECTRICAL CHARACTERISTICS

| Symbol           | Parameter                 | Condition   | V <sub>CC</sub> (V) | T <sub>A</sub> = 25°C |                 |                      | -55°C ≤ T <sub>A</sub> ≤ 125°C |                      | Units |
|------------------|---------------------------|---|---------------------|-----------------------|-----------------|----------------------|--------------------------------|----------------------|-------|
|                  |                           |   |                     | Min                   | Typ             | Max                  | Min                            | Max                  |       |
| V <sub>IH</sub>  | High-Level Input Voltage  |   | 1.65 to 1.95        | 0.85 V <sub>CC</sub>  | -               | -                    | 0.85 V <sub>CC</sub>           | -                    | V     |
|                  |                           |   | 2.3 to 5.5          | 0.80 V <sub>CC</sub>  | -               | -                    | 0.80 V <sub>CC</sub>           | -                    |       |
| V <sub>IL</sub>  | Low-Level Input Voltage   |   | 1.65 to 1.95        | -                     | -               | 0.15 V <sub>CC</sub> | -                              | 0.15 V <sub>CC</sub> | V     |
|                  |                           |   | 2.3 to 5.5          | -                     | -               | 0.20 V <sub>CC</sub> | -                              | 0.20 V <sub>CC</sub> |       |
| V <sub>OH</sub>  | High-Level Output Voltage | V <sub>IN</sub> = GND<br>I <sub>OH</sub> = -100 μA<br>I <sub>OH</sub> = -4 mA<br>I <sub>OH</sub> = -8 mA<br>I <sub>OH</sub> = -12 mA<br>I <sub>OH</sub> = -16 mA<br>I <sub>OH</sub> = -24 mA<br>I <sub>OH</sub> = -32 mA      | 1.65 to 5.5         | V <sub>CC</sub> - 0.1 | V <sub>CC</sub> | -                    | V <sub>CC</sub> - 0.1          | -                    | V     |
|                  |                           |   | 1.65                | 1.29                  | 1.4             | -                    | 1.29                           | -                    |       |
|                  |                           |   | 2.3                 | 1.9                   | 2.1             | -                    | 1.9                            | -                    |       |
|                  |                           |   | 2.7                 | 2.2                   | 2.4             | -                    | 2.2                            | -                    |       |
|                  |                           |   | 3                   | 2.4                   | 2.7             | -                    | 2.4                            | -                    |       |
|                  |                           |   | 3                   | 2.3                   | 2.5             | -                    | 2.3                            | -                    |       |
|                  |                           |   | 4.5                 | 3.8                   | 4.0             | -                    | 3.8                            | -                    |       |
| V <sub>OL</sub>  | Low-Level Output Voltage  | V <sub>IN</sub> = V <sub>CC</sub><br>I <sub>OH</sub> = 100 μA<br>I <sub>OH</sub> = 4 mA<br>I <sub>OH</sub> = 8 mA<br>I <sub>OH</sub> = 12 mA<br>I <sub>OH</sub> = 16 mA<br>I <sub>OH</sub> = 24 mA<br>I <sub>OH</sub> = 32 mA | 1.65 to 5.5         | -                     | -               | 0.1                  | -                              | 0.1                  | V     |
|                  |                           |   | 1.65                | -                     | 0.08            | 0.24                 | -                              | 0.24                 |       |
|                  |                           |   | 2.3                 | -                     | 0.2             | 0.3                  | -                              | 0.3                  |       |
|                  |                           |   | 2.7                 | -                     | 0.22            | 0.4                  | -                              | 0.4                  |       |
|                  |                           |   | 3                   | -                     | 0.28            | 0.4                  | -                              | 0.4                  |       |
|                  |                           |   | 3                   | -                     | 0.38            | 0.55                 | -                              | 0.55                 |       |
|                  |                           |   | 4.5                 | -                     | 0.42            | 0.55                 | -                              | 0.55                 |       |
| I <sub>IN</sub>  | Input Leakage Current     | V <sub>IN</sub> = 5.5 V or GND  | 1.65 to 5.5         | -                     | -               | ±0.1                 | -                              | ±1.0                 | μA    |
| I <sub>OFF</sub> | Power Off Leakage Current | V <sub>IN</sub> = 5.5 V   | 0                   | -                     | -               | 1.0                  | -                              | 10                   | μA    |
| I <sub>CC</sub>  | Quiescent Supply Current  | V <sub>IN</sub> = V <sub>CC</sub> or GND  | 5.5                 | -                     | -               | 1.0                  | -                              | 10                   | μA    |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## AC ELECTRICAL CHARACTERISTICS

| Symbol                               | Parameter                                       | Condition                                      | V <sub>CC</sub> (V) | T <sub>A</sub> = 25°C |     |     | -55°C ≤ T <sub>A</sub> ≤ 125°C |      | Units |
|--------------------------------------|---|--|---------------------|-----------------------|-----|-----|--------------------------------|------|-------|
|                                      |   |  |                     | Min                   | Typ | Max | Min                            | Max  |       |
| t <sub>PLH</sub><br>t <sub>PHL</sub> | Propagation Delay Input A to Y (Figure 3 and 4) | R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF  | 1.65 to 1.95        | -                     | 5.5 | 9.8 | -                              | 11.0 | ns    |
|                                      |   | R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF  | 2.3 to 2.7          | -                     | 3.3 | 5.7 | -                              | 6.3  |       |
|                                      |   | R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF  | 3.0 to 3.6          | -                     | 2.7 | 4.1 | -                              | 4.5  |       |
|                                      |   | R <sub>L</sub> = 500 Ω, C <sub>L</sub> = 50 pF |                     | -                     | 4.0 | 6.4 | -                              | 7.0  |       |
|                                      |   | R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF  | 4.5 to 5.5          | -                     | 2.2 | 3.3 | -                              | 3.6  |       |
|                                      |   | R <sub>L</sub> = 500 Ω, C <sub>L</sub> = 50 pF |                     | -                     | 3.4 | 5.6 | -                              | 6.2  |       |

## CAPACITIVE CHARACTERISTICS

| Symbol           | Parameter                              | Condition   | Typical | Units |
|------------------|--|---|---------|-------|
| C <sub>IN</sub>  | Input Capacitance                      | V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>         | 2.5     | pF    |
| C <sub>OUT</sub> | Output Capacitance                     | V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>         | 4.0     | pF    |
| C <sub>PD</sub>  | Power Dissipation Capacitance (Note 5) | 10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub> | 4.0     | pF    |

5. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

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$C_L$  includes probe and jig capacitance  
 $R_T$  is  $Z_{OUT}$  of pulse generator (typically 50  $\Omega$ )  
 $f = 1$  MHz

**Figure 3. Test Circuit**

| Test                | Switch Position   | $C_L$ , pF                   | $R_L$ , $\Omega$ | $R_1$ , $\Omega$ |
|---------------------|-------------------|------------------------------|------------------|------------------|
| $t_{PLH} / t_{PHL}$ | Open              | See AC Characteristics Table |                  |                  |
| $t_{PLZ} / t_{PZL}$ | $2 \times V_{CC}$ | 50                           | 500              | 500              |
| $t_{PHZ} / t_{PZH}$ | GND               | 50                           | 500              | 500              |

X = Don't Care



**Figure 4. Switching Waveforms**

| $V_{CC}$ , V | $V_{mi}$ , V | $V_{mo}$ , V          |   | $V_Y$ , V |
|--------------|--------------|-----------------------|---|-----------|
|              |              | $t_{PLH}$ , $t_{PHL}$ | $t_{PZL}$ , $t_{PLZ}$ , $t_{PZH}$ , $t_{PHZ}$ |           |
| 1.65 to 1.95 | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.15      |
| 2.3 to 2.7   | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.15      |
| 3.0 to 3.6   | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.3       |
| 4.5 to 5.5   | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.3       |

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## DEVICE ORDERING INFORMATION

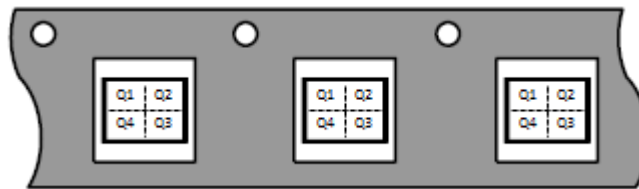
| Device                              | Packages                | Specific Device Code | Pin 1 Orientation<br>(See below) | Shipping <sup>†</sup> |
|-------------------------------------|-------------------------|----------------------|----------------------------------|-----------------------|
| NL27WZU04DFT2G                      | SC-88                   | M6                   | Q4                               | 3000 / Tape & Reel    |
| NLV27WZU04DFT2G*                    | SC-88                   | M6                   | Q4                               | 3000 / Tape & Reel    |
| NL27WZU04DBVT1G                     | SC-74                   | M6                   | Q4                               | 3000 / Tape & Reel    |
| NL27WZU04DTT1G                      | TSOP-6                  | M6                   | Q4                               | 3000 / Tape & Reel    |
| NL27WZU04MU1TCG<br>(In Development) | UDFN6, 1.45 x 1.0, 0.5P | TBD                  | Q4                               | 3000 / Tape & Reel    |
| NL27WZU04MU3TCG<br>(In Development) | UDFN6, 1.0 x 1.0, 0.35P | TBD                  | Q4                               | 3000 / Tape & Reel    |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

### Pin 1 Orientation in Tape and Reel

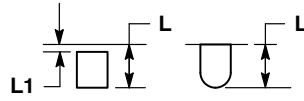
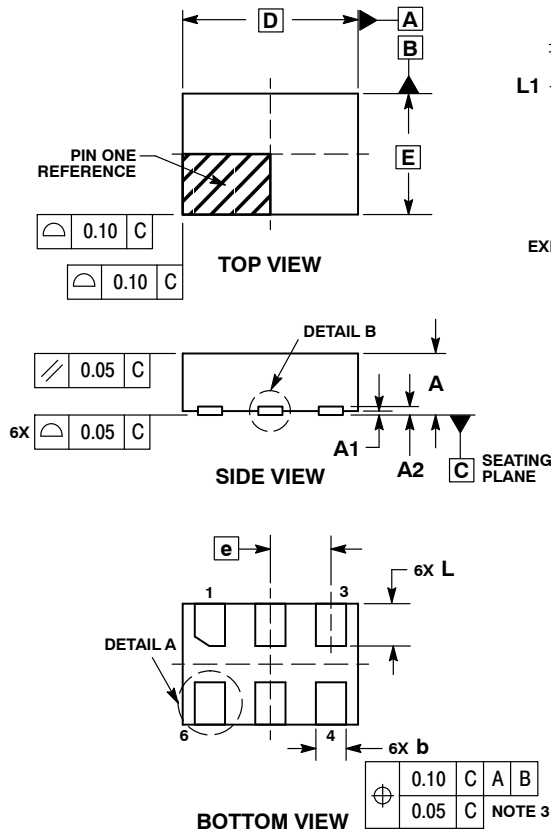
Direction of Feed



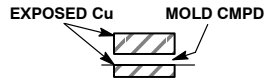
# NL27WZU04

## PACKAGE DIMENSIONS

UDFN6, 1.45x1.0, 0.5P  
CASE 517AQ  
ISSUE O



**DETAIL A**  
OPTIONAL  
CONSTRUCTIONS



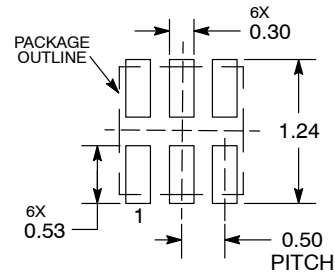
**DETAIL B**  
OPTIONAL  
CONSTRUCTIONS

**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.

| MILLIMETERS |      |      |
|-------------|------|------|
| DIM         | MIN  | MAX  |
| A           | 0.45 | 0.55 |
| A1          | 0.00 | 0.05 |
| A2          | 0.07 | REF  |
| b           | 0.20 | 0.30 |
| D           | 1.45 | BSC  |
| E           | 1.00 | BSC  |
| e           | 0.50 | BSC  |
| L           | 0.30 | 0.40 |
| L1          | ---  | 0.15 |

### MOUNTING FOOTPRINT



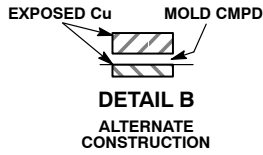
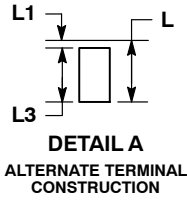
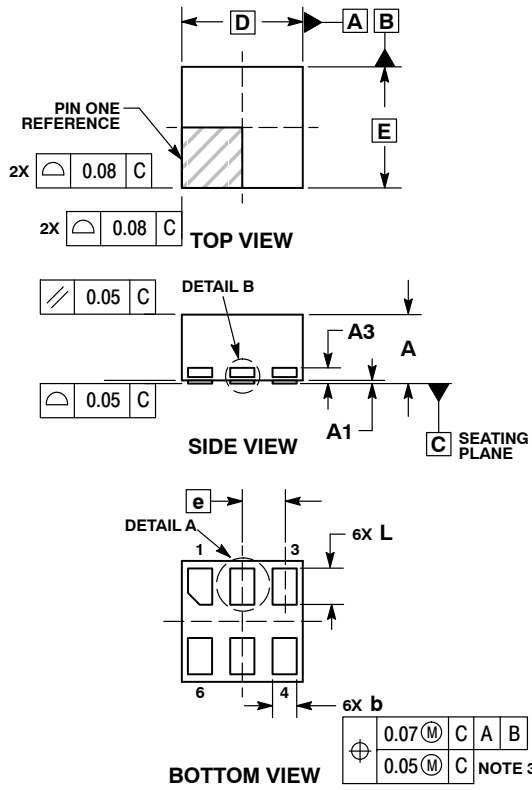
DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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## PACKAGE DIMENSIONS

UDFN6, 1x1, 0.35P  
CASE 517BX  
ISSUE O

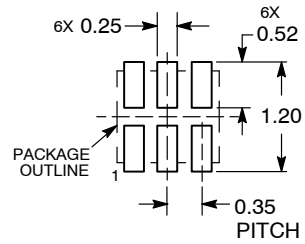


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

| DIM | MILLIMETERS |      |
|-----|-------------|------|
|     | MIN         | MAX  |
| A   | 0.50        | 0.65 |
| A1  | 0.00        | 0.05 |
| A3  | 0.13 REF    |      |
| b   | 0.17        | 0.23 |
| D   | 1.00 BSC    |      |
| E   | 1.00 BSC    |      |
| e   | 0.35        |      |
| L   | 0.20        | 0.40 |
| L1  | ---         | 0.15 |
| L3  | 0.26        | 0.33 |

### RECOMMENDED SOLDERING FOOTPRINT\*



DIMENSION: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

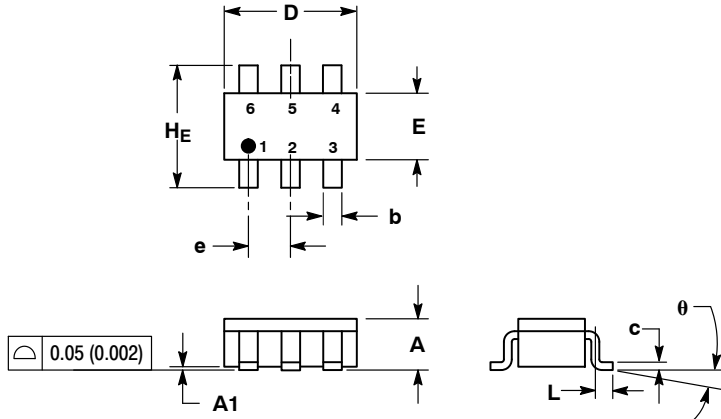
ON Semiconductor®



### SC-74 CASE 318F-05 ISSUE N

DATE 08 JUN 2012

SCALE 2:1

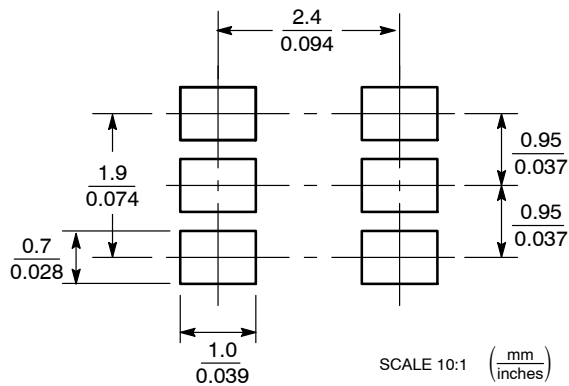


**NOTES:**

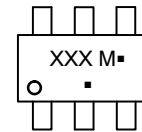
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2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318F-01, -02, -03, -04 OBSOLETE. NEW STANDARD 318F-05.

| DIM | MILLIMETERS |      |      | INCHES |       |       |
|-----|-------------|------|------|--------|-------|-------|
|     | MIN         | NOM  | MAX  | MIN    | NOM   | MAX   |
| A   | 0.90        | 1.00 | 1.10 | 0.035  | 0.039 | 0.043 |
| A1  | 0.01        | 0.06 | 0.10 | 0.001  | 0.002 | 0.004 |
| b   | 0.25        | 0.37 | 0.50 | 0.010  | 0.015 | 0.020 |
| c   | 0.10        | 0.18 | 0.26 | 0.004  | 0.007 | 0.010 |
| D   | 2.90        | 3.00 | 3.10 | 0.114  | 0.118 | 0.122 |
| E   | 1.30        | 1.50 | 1.70 | 0.051  | 0.059 | 0.067 |
| e   | 0.85        | 0.95 | 1.05 | 0.034  | 0.037 | 0.041 |
| L   | 0.20        | 0.40 | 0.60 | 0.008  | 0.016 | 0.024 |
| HE  | 2.50        | 2.75 | 3.00 | 0.099  | 0.108 | 0.118 |
| θ   | 0°          | -    | 10°  | 0°     | -     | 10°   |

**SOLDERING FOOTPRINT\***



**GENERIC MARKING DIAGRAM\***



- XXX = Specific Device Code
- M = Date Code
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

- |  |   |  |   |  |  |
|--|---|--|---|--|--|
| <p><b>STYLE 1:</b><br/>PIN 1. CATHODE<br/>2. ANODE<br/>3. CATHODE<br/>4. CATHODE<br/>5. ANODE<br/>6. CATHODE</p>     | <p><b>STYLE 2:</b><br/>PIN 1. NO CONNECTION<br/>2. COLLECTOR<br/>3. EMITTER<br/>4. NO CONNECTION<br/>5. COLLECTOR<br/>6. BASE</p> | <p><b>STYLE 3:</b><br/>PIN 1. EMITTER 1<br/>2. BASE 1<br/>3. COLLECTOR 2<br/>4. EMITTER 2<br/>5. BASE 2<br/>6. COLLECTOR 1</p> | <p><b>STYLE 4:</b><br/>PIN 1. COLLECTOR 2<br/>2. EMITTER 1/EMITTER 2<br/>3. COLLECTOR 1<br/>4. EMITTER 3<br/>5. BASE 1/BASE 2/COLLECTOR 3<br/>6. BASE 3</p> | <p><b>STYLE 5:</b><br/>PIN 1. CHANNEL 1<br/>2. ANODE<br/>3. CHANNEL 2<br/>4. CHANNEL 3<br/>5. CATHODE<br/>6. CHANNEL 4</p> | <p><b>STYLE 6:</b><br/>PIN 1. CATHODE<br/>2. ANODE<br/>3. CATHODE<br/>4. CATHODE<br/>5. CATHODE<br/>6. CATHODE</p> |
| <p><b>STYLE 7:</b><br/>PIN 1. SOURCE 1<br/>2. GATE 1<br/>3. DRAIN 2<br/>4. SOURCE 2<br/>5. GATE 2<br/>6. DRAIN 1</p> | <p><b>STYLE 8:</b><br/>PIN 1. EMITTER 1<br/>2. BASE 2<br/>3. COLLECTOR 2<br/>4. EMITTER 2<br/>5. BASE 1<br/>6. COLLECTOR 1</p>    | <p><b>STYLE 9:</b><br/>PIN 1. EMITTER 2<br/>2. BASE 2<br/>3. COLLECTOR 1<br/>4. EMITTER 1<br/>5. BASE 1<br/>6. COLLECTOR 2</p> | <p><b>STYLE 10:</b><br/>PIN 1. ANODE/CATHODE<br/>2. BASE<br/>3. EMITTER<br/>4. COLLECTOR<br/>5. ANODE<br/>6. CATHODE</p>                                    | <p><b>STYLE 11:</b><br/>PIN 1. EMITTER<br/>2. BASE<br/>3. ANODE/CATHODE<br/>4. ANODE<br/>5. CATHODE<br/>6. COLLECTOR</p>   |  |

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# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

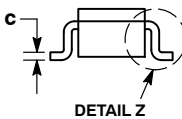
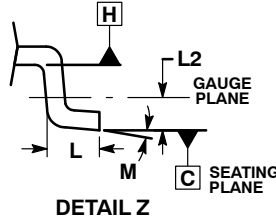
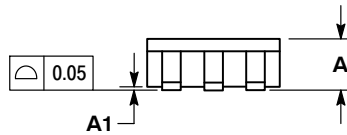
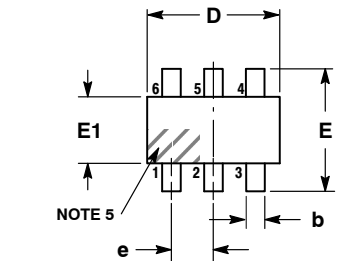
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SCALE 2:1

### TSOP-6 CASE 318G-02 ISSUE V

DATE 12 JUN 2012



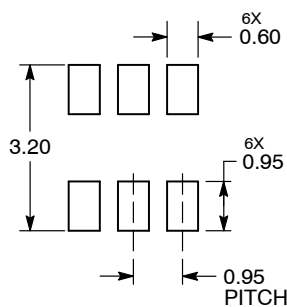
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSIONS D AND E1 ARE DETERMINED AT DATUM H.
5. PIN ONE INDICATOR MUST BE LOCATED IN THE INDICATED ZONE.

| DIM | MILLIMETERS |      |      |
|-----|-------------|------|------|
|     | MIN         | NOM  | MAX  |
| A   | 0.90        | 1.00 | 1.10 |
| A1  | 0.01        | 0.06 | 0.10 |
| b   | 0.25        | 0.38 | 0.50 |
| c   | 0.10        | 0.18 | 0.26 |
| D   | 2.90        | 3.00 | 3.10 |
| E   | 2.50        | 2.75 | 3.00 |
| E1  | 1.30        | 1.50 | 1.70 |
| e   | 0.85        | 0.95 | 1.05 |
| L   | 0.20        | 0.40 | 0.60 |
| L2  | 0.25 BSC    |      |      |
| M   | 0°          | -    | 10°  |

- |  |  |   |   |   |  |
|--|--|---|---|---|--|
| <p>STYLE 1:<br/>PIN 1. DRAIN<br/>2. DRAIN<br/>3. GATE<br/>4. SOURCE<br/>5. DRAIN<br/>6. DRAIN</p>              | <p>STYLE 2:<br/>PIN 1. EMITTER 2<br/>2. BASE 1<br/>3. COLLECTOR 1<br/>4. EMITTER 1<br/>5. BASE 2<br/>6. COLLECTOR 2</p>    | <p>STYLE 3:<br/>PIN 1. ENABLE<br/>2. N/C<br/>3. R BOOST<br/>4. Vz<br/>5. V in<br/>6. V out</p>                            | <p>STYLE 4:<br/>PIN 1. N/C<br/>2. V in<br/>3. NOT USED<br/>4. GROUND<br/>5. ENABLE<br/>6. LOAD</p>                | <p>STYLE 5:<br/>PIN 1. EMITTER 2<br/>2. BASE 2<br/>3. COLLECTOR 1<br/>4. EMITTER 1<br/>5. BASE 1<br/>6. COLLECTOR 2</p> | <p>STYLE 6:<br/>PIN 1. COLLECTOR<br/>2. COLLECTOR<br/>3. BASE<br/>4. EMITTER<br/>5. COLLECTOR<br/>6. COLLECTOR</p> |
| <p>STYLE 7:<br/>PIN 1. COLLECTOR<br/>2. COLLECTOR<br/>3. BASE<br/>4. N/C<br/>5. COLLECTOR<br/>6. EMITTER</p>   | <p>STYLE 8:<br/>PIN 1. Vbus<br/>2. D(in)<br/>3. D(in)+<br/>4. D(out)+<br/>5. D(out)<br/>6. GND</p>                         | <p>STYLE 9:<br/>PIN 1. LOW VOLTAGE GATE<br/>2. DRAIN<br/>3. SOURCE<br/>4. DRAIN<br/>5. DRAIN<br/>6. HIGH VOLTAGE GATE</p> | <p>STYLE 10:<br/>PIN 1. D(OUT)+<br/>2. GND<br/>3. D(OUT)-<br/>4. D(IN)-<br/>5. VBUS<br/>6. D(IN)+</p>             | <p>STYLE 11:<br/>PIN 1. SOURCE 1<br/>2. DRAIN 2<br/>3. DRAIN 2<br/>4. SOURCE 2<br/>5. GATE 1<br/>6. DRAIN 1/GATE 2</p>  | <p>STYLE 12:<br/>PIN 1. I/O<br/>2. GROUND<br/>3. I/O<br/>4. I/O<br/>5. VCC<br/>6. I/O</p>                          |
| <p>STYLE 13:<br/>PIN 1. GATE 1<br/>2. SOURCE 2<br/>3. GATE 2<br/>4. DRAIN 2<br/>5. SOURCE 1<br/>6. DRAIN 1</p> | <p>STYLE 14:<br/>PIN 1. ANODE<br/>2. SOURCE<br/>3. GATE<br/>4. CATHODE/DRAIN<br/>5. CATHODE/DRAIN<br/>6. CATHODE/DRAIN</p> | <p>STYLE 15:<br/>PIN 1. ANODE<br/>2. SOURCE<br/>3. GATE<br/>4. DRAIN<br/>5. N/C<br/>6. CATHODE</p>                        | <p>STYLE 16:<br/>PIN 1. ANODE/CATHODE<br/>2. BASE<br/>3. EMITTER<br/>4. COLLECTOR<br/>5. ANODE<br/>6. CATHODE</p> | <p>STYLE 17:<br/>PIN 1. EMITTER<br/>2. BASE<br/>3. ANODE/CATHODE<br/>4. ANODE<br/>5. CATHODE<br/>6. COLLECTOR</p>       |  |

### RECOMMENDED SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

### GENERIC MARKING DIAGRAM\*



- |  |   |
|--|---|
| <p>XXX = Specific Device Code<br/>A = Assembly Location<br/>Y = Year<br/>W = Work Week<br/>▪ = Pb-Free Package</p> | <p>XXX = Specific Device Code<br/>M = Date Code<br/>▪ = Pb-Free Package</p> |
|--|---|

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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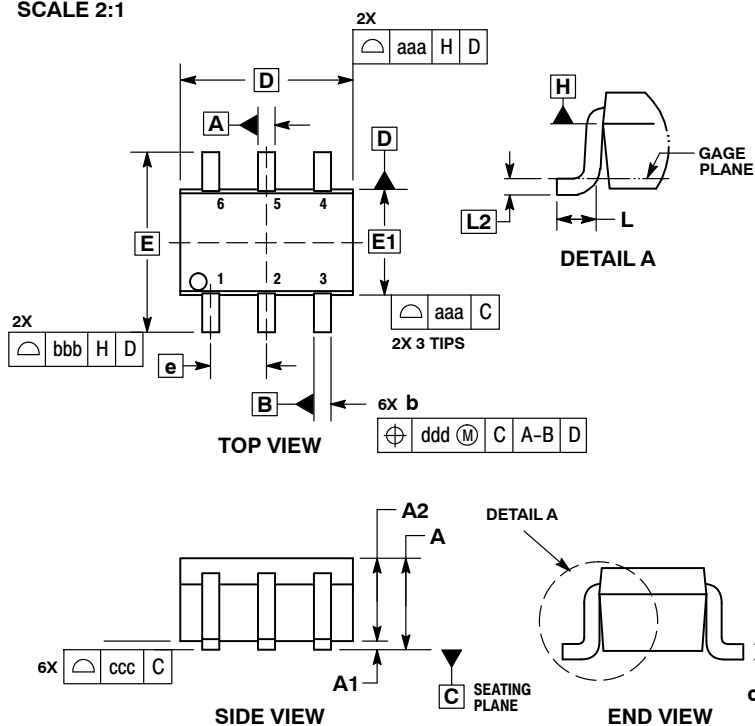
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 SCALE 2:1

SC-88/SC70-6/SOT-363  
 CASE 419B-02  
 ISSUE Y

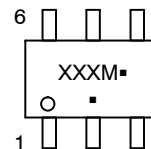
DATE 11 DEC 2012



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
  4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
  5. DATUMS A AND B ARE DETERMINED AT DATUM H.
  6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
  7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

| DIM | MILLIMETERS |      |      | INCHES    |       |       |
|-----|-------------|------|------|-----------|-------|-------|
|     | MIN         | NOM  | MAX  | MIN       | NOM   | MAX   |
| A   | ---         | ---  | 1.10 | ---       | ---   | 0.043 |
| A1  | 0.00        | ---  | 0.10 | 0.000     | ---   | 0.004 |
| A2  | 0.70        | 0.90 | 1.00 | 0.027     | 0.035 | 0.039 |
| b   | 0.15        | 0.20 | 0.25 | 0.006     | 0.008 | 0.010 |
| C   | 0.08        | 0.15 | 0.22 | 0.003     | 0.006 | 0.009 |
| D   | 1.80        | 2.00 | 2.20 | 0.070     | 0.078 | 0.086 |
| E   | 2.00        | 2.10 | 2.20 | 0.078     | 0.082 | 0.086 |
| E1  | 1.15        | 1.25 | 1.35 | 0.045     | 0.049 | 0.053 |
| e   | 0.65 BSC    |      |      | 0.026 BSC |       |       |
| L   | 0.26        | 0.36 | 0.46 | 0.010     | 0.014 | 0.018 |
| L2  | 0.15 BSC    |      |      | 0.006 BSC |       |       |
| aaa | 0.15        |      |      | 0.006     |       |       |
| bbb | 0.30        |      |      | 0.012     |       |       |
| ccc | 0.10        |      |      | 0.004     |       |       |
| ddd | 0.10        |      |      | 0.004     |       |       |

**GENERIC MARKING DIAGRAM\***



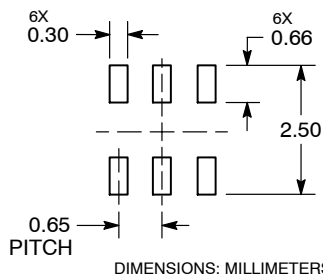
XXX = Specific Device Code  
 M = Date Code\*  
 ▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

**RECOMMENDED SOLDERING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**STYLES ON PAGE 2**

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**SC-88/SC70-6/SOT-363**  
**CASE 419B-02**  
**ISSUE Y**

DATE 11 DEC 2012

|   |   |  |  |  |  |
|---|---|--|--|--|--|
| <b>STYLE 1:</b><br>PIN 1. EMITTER 2<br>2. BASE 2<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. BASE 1<br>6. COLLECTOR 2 | <b>STYLE 2:</b><br>CANCELLED  | <b>STYLE 3:</b><br>CANCELLED   | <b>STYLE 4:</b><br>PIN 1. CATHODE<br>2. CATHODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. ANODE               | <b>STYLE 5:</b><br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | <b>STYLE 6:</b><br>PIN 1. ANODE 2<br>2. N/C<br>3. CATHODE 1<br>4. ANODE 1<br>5. N/C<br>6. CATHODE 2          |
| <b>STYLE 7:</b><br>PIN 1. SOURCE 2<br>2. DRAIN 2<br>3. GATE 1<br>4. SOURCE 1<br>5. DRAIN 1<br>6. GATE 2           | <b>STYLE 8:</b><br>CANCELLED  | <b>STYLE 9:</b><br>PIN 1. EMITTER 2<br>2. EMITTER 1<br>3. COLLECTOR 1<br>4. BASE 1<br>5. BASE 2<br>6. COLLECTOR 2  | <b>STYLE 10:</b><br>PIN 1. SOURCE 2<br>2. SOURCE 1<br>3. GATE 1<br>4. DRAIN 1<br>5. DRAIN 2<br>6. GATE 2           | <b>STYLE 11:</b><br>PIN 1. CATHODE 2<br>2. CATHODE 2<br>3. ANODE 1<br>4. CATHODE 1<br>5. CATHODE 1<br>6. ANODE 2   | <b>STYLE 12:</b><br>PIN 1. ANODE 2<br>2. ANODE 2<br>3. CATHODE 1<br>4. ANODE 1<br>5. ANODE 1<br>6. CATHODE 2 |
| <b>STYLE 13:</b><br>PIN 1. ANODE<br>2. N/C<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | <b>STYLE 14:</b><br>PIN 1. VREF<br>2. GND<br>3. GND<br>4. IOUT<br>5. VEN<br>6. VCC                            | <b>STYLE 15:</b><br>PIN 1. ANODE 1<br>2. ANODE 2<br>3. ANODE 3<br>4. CATHODE 3<br>5. CATHODE 2<br>6. CATHODE 1     | <b>STYLE 16:</b><br>PIN 1. BASE 1<br>2. EMITTER 2<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 1<br>6. COLLECTOR 1 | <b>STYLE 17:</b><br>PIN 1. BASE 1<br>2. EMITTER 1<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 2<br>6. COLLECTOR 1 | <b>STYLE 18:</b><br>PIN 1. VIN1<br>2. VCC<br>3. VOUT2<br>4. VIN2<br>5. GND<br>6. VOUT1                       |
| <b>STYLE 19:</b><br>PIN 1. IOUT<br>2. GND<br>3. GND<br>4. V CC<br>5. V EN<br>6. V REF                             | <b>STYLE 20:</b><br>PIN 1. COLLECTOR<br>2. COLLECTOR<br>3. BASE<br>4. EMITTER<br>5. COLLECTOR<br>6. COLLECTOR | <b>STYLE 21:</b><br>PIN 1. ANODE 1<br>2. N/C<br>3. ANODE 2<br>4. CATHODE 2<br>5. N/C<br>6. CATHODE 1               | <b>STYLE 22:</b><br>PIN 1. D1 (i)<br>2. GND<br>3. D2 (j)<br>4. D2 (c)<br>5. VBUS<br>6. D1 (c)                      | <b>STYLE 23:</b><br>PIN 1. Vn<br>2. CH1<br>3. Vp<br>4. N/C<br>5. CH2<br>6. N/C                                     | <b>STYLE 24:</b><br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE<br>4. CATHODE<br>5. CATHODE<br>6. CATHODE       |
| <b>STYLE 25:</b><br>PIN 1. BASE 1<br>2. CATHODE<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER<br>6. COLLECTOR 1    | <b>STYLE 26:</b><br>PIN 1. SOURCE 1<br>2. GATE 1<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 2<br>6. DRAIN 1      | <b>STYLE 27:</b><br>PIN 1. BASE 2<br>2. BASE 1<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. EMITTER 2<br>6. COLLECTOR 2 | <b>STYLE 28:</b><br>PIN 1. DRAIN<br>2. DRAIN<br>3. GATE<br>4. SOURCE<br>5. DRAIN<br>6. DRAIN                       | <b>STYLE 29:</b><br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE/ANODE<br>6. CATHODE          | <b>STYLE 30:</b><br>PIN 1. SOURCE 1<br>2. DRAIN 2<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 1<br>6. DRAIN 1    |

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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