Dual DPDT Low R_{ON}, Low Capacitance Switch

The NLAS3899B is a dual DPDT analog switch designed for low power audio and dual SIM card applications. The low R_{ON} of 3.0 Ω (typical) is ideal for routing audio signals to or from a moderately high impedance load. In addition, the low C_{ON} of 20 pF (typical) gives the NLAS3899B a high bandwidth of 280 MHz, perfect for dual SIM card applications.

Features

- Single Supply Operation 1.65 to 4.3 V V_{CC}
 - Function Directly from Li–Ion Battery
- Low ON Resistance (3.0 Ω Typical Across V_{CC})
- Low C_{ON} (20 pF Typical)
- Bandwidth 280 MHz
- Maximum Breakdown Voltage: 5.5 V
- Low Static Power
- Interfaces with 1.8 V Chipset
- These are Pb–Free Devices

Typical Applications

- Cell Phone Speaker/Microphone Switching
- Ringtone-Chip/Amplifier Switching
- Dual SIM Card Data Switching
- Four Unbalanced (Single-Ended) Switches

Important Information

 ESD Protection: Human Body Model (HBM) 1000 V – All Pins

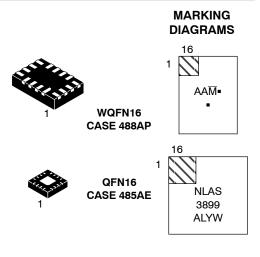
5000 V – I/O to GND

- Continuous Current Rating Through each Switch ±300 mA
- Conforms to: JEDEC MO-220, Issue H, Variation VEED-6
- Package:
 - 1.8 x 2.6 x 0.75 mm WQFN16 Pb-Free
 - 3.0 x 3.0 x 0.9 mm QFN16 Pb-Free



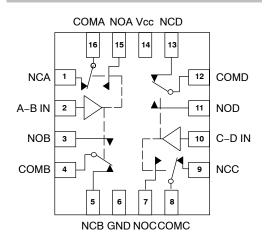
ON Semiconductor®

http://onsemi.com



- XX = Specific Device Code
- A = Assembly Location
- M = Date Code/Assembly Location
- L = Wafer Lot
- Y = Year
- W = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

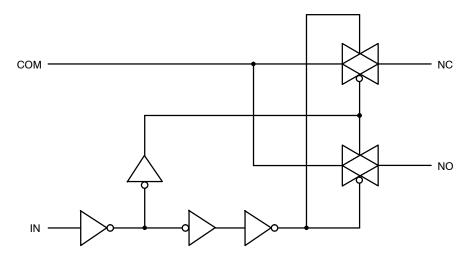


Figure 1. Input Equivalent Circuit

PIN DESCRIPTION

QFN PIN #	Symbol	Name and Function
1, 3, 5, 7, 9, 11, 13, 15	NO A-D, NC A-D	Independent Channels
2, 10	A–B IN, C–D IN	Controls
4, 8, 12, 16	COM A-D	Common Channels
6	GND	Ground (V)
14	V _{CC}	Positive Supply Voltage

TRUTH TABLE

IN	NO	NC
н	ON	OFF*
L	OFF*	ON

*High impedance.

OPERATING CONDITIONS

MAXIMUM RATINGS

Symbol	Pins	Parameter	Value	Condition	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	-0.5 to +5.5		V
V _{IS}	NOx, NCx, or COMx	Analog Signal Voltage	-0.5 to V _{CC} + 0.5		V
V _{IN}	A-B IN, C-D IN	Control Input Voltage	-0.5 to 5.5		V
IIS_CON	NOx, NCx, or COMx	Analog Signal Continuous Current	±300	Closed Switch	mA
I _{IS_PK}	NOx, NCx, or COMx	Analog Signal Peak Current	±500	10% Duty Cycle	mA
I _{IN}	A–B IN, C–D IN	Control Input Current	±20		mA
T _{STG}		Storage Temperature Range	-65 to 150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

Symbol	Pins	Parameter	Value	Condition	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	1.65 to 4.3		V
V _{IS}	NOx, NCx, or COMx	Analog Signal Voltage	GND to V _{CC}		V
V _{IN}	A–B IN, C–D IN	Control Input Voltage	GND to 4.3		V
T _A		Operating Temperature Range	-40 to +85		°C
t _r , t _f		Input Rise or Fall Time	20	V _{CC} = 1.6 V – 2.7 V	ns/V
			10	$V_{CC} = 3.0 \text{ V} - 4.5 \text{ V}$	

Minimum and maximum values are guaranteed through test or design across the **Recommended Operating Conditions**, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for each section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.

ESD PROTECTION

Pins	Description	Minimum Voltage
All Pins	Human Body Model	1 kV
I/O to GND	Human Body Model	5 kV

DC Electrical Characteristics

Typical: T = 25°C; V_{CC} = 3.0 V

CONTROL INPUT (Typical: T = 25° C; V_{CC} = 3.0 V)

				V _{cc}	-40			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
V _{IH}	A–B IN, C–D IN	Control Input High		3.0 4.3	1.3 1.6			V
V _{IL}	A–B IN, C–D IN	Control Input Low		3.0 4.3			0.5 0.6	V
I _{IN}	A–B IN, C–D IN	Control Input Leakage	$0 \leq V_{IN} \leq V_{CC}$	4.3		±0.1	±1.0	μΑ

SUPPLY CURRENT AND LEAKAGE (Typical: T = 25°C; V_{CC} = 3.0 V)

				V _{CC}	-4	0°C to +85	5°C	
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
I _{NO/NC} (OFF)	NCx, NOx	OFF State Leakage		4.3		±10	±300	nA
I _{COM} (ON)	COMx	ON State Leakage	$ \begin{array}{l} V_{IN} = V_{IL} \mbox{ or } V_{IH} \\ V_{NO} = 0.3 \mbox{ V or } 4.0 \mbox{ V with} \\ V_{NC} \mbox{ floating or } \\ V_{NC} = 0.3 \mbox{ V or } 4.0 \mbox{ V with} \\ V_{NO} \mbox{ floating } \\ V_{COM} = 0.3 \mbox{ V or } 4.0 \mbox{ V} \end{array} $	4.3		±10	±300	nA
I _{CC}	V _{CC}	Quiescent Supply	V_{IN} and V_{IS} = V_{CC} or GND I_{D} = 0 A	1.65 – 4.3		±1.0	±2.0	μA
I _{OFF}	A–B IN, C–D IN	Power Off Leakage	$V_{IN} = 4.3 V \text{ or GND}$	0		±0.5	±2.0	μA

ON RESISTANCE (Typical: T = 25°C; V_{CC} = 3.0 V)

				Vcc	V _{CC} -40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
R _{ON}	NOx, NCx COMx	ON Resistance	I_{ON} = -100 mA V _{IS} = 0 to V _{CC}	2.5 3.0 3.6 4.3		3.0 2.6 2.5 2.2	4.0 3.0 3.0 2.5	Ω
R _{FLAT}	NOx, NCx COMx	R _{ON} Flatness	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.3		0.8 1.1		Ω
ΔR_{ON}	NOx, NCx COMx	R _{ON} Matching	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.3		0.8 0.7		Ω

AC ELECTRICAL CHARACTERISTICS

TIMING/FREQUENCY (Typical: T = 25°C; V_{CC} = 3.0 V, R_L = 50 Ω , C_L = 35 p	F, f = 1 MHz)
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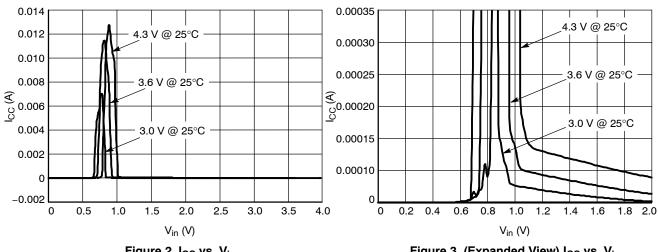
				Vcc	V _{CC} -40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
t _{ON}	IN to NCx or NOx	Turn On Time		2.3 – 4.3		30	40	ns
tOFF	IN to NCx or NOx	Turn Off Time		2.3 - 4.53		20	30	ns
t _{BBM}	IN to NCx or NOx	Break Before Make		3.0	2	15		ns
BW		-3dB Bandwidth	C _L = 5 pF	1.65 – 4.3		280		MHz

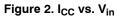
ISOLATION AND THD (Typical: T = 25°C; V_{CC} = 3.0 V, RL = 50 Ω , CL = 5 pF, f = 1 MHz)

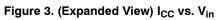
				V _{CC}	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
Q		Charge Injection		1.65 – 4.3		111		рС
THD		Total Harmonic Dis- tortion	$\begin{array}{l} F_{IS} = 20 \text{ Hz to } 20 \text{ kHz} \\ R_L = R_{gen} = 600 \ \Omega, \\ C_L = 1.0 \text{ pF} \\ V_{IS} = 1.0 \text{ V}_{PP} \end{array}$	3.0		0.007		%
V _{ONL}		Maximum Feed- through On Loss	V_{IN} = 0 dBm @ 100 kHz to 50 MHz V_{IN} centered between V_{CC} & GND	1.65 – 4.3		-0.06		dB
O _{IRR}	NOx	Off Isolation	$V_{IN} = 0$ V_{NO} or V_{NC} (pk-pk) = 1.0 V	1.65 – 4.3		-67		dB
Xtalk	COMx to COMy	Non-Adjacent Chan- nel	V_{NO} or V_{NC} (pk-pk) = 1.0 V	1.65 – 4.3		-100		dB

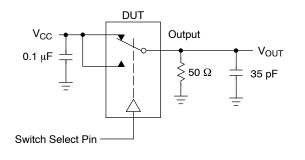
$\textbf{CAPACITANCE} \text{ (Typical: } T = 25^{\circ}\text{C}\text{; } V_{CC} = 3.0 \text{ V}\text{, } R_L = 50 \text{ } \Omega\text{, } C_L = 5 \text{ } p\text{F}\text{, } f = 1 \text{ } \text{MHz}\text{)}$

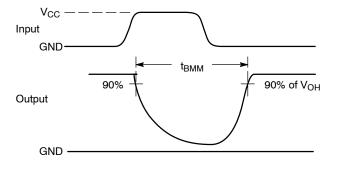
				Vcc	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
C _{IN}	A–B IN, C–D IN	Control Input		0 V		5.0		pF
C _{ON}	NCx to COMx	Through Switch	V _{IN} = 0V	3.0 V		20		pF
C _{OFF}	NCx NOx	Unselected Port	$V_{IS} = 3.0V, V_{IN} = 3.0V$	3.0 V		10		pF













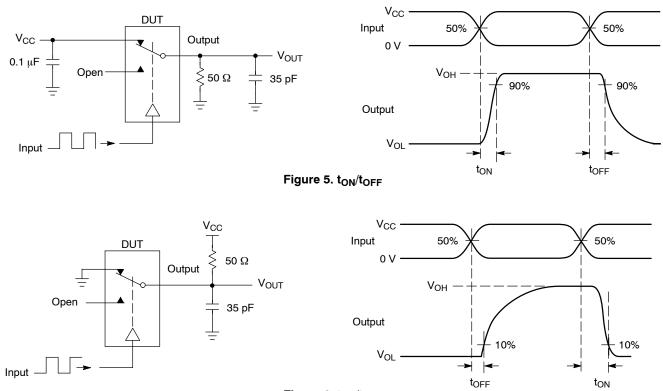
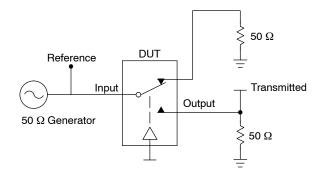


Figure 6. t_{ON}/t_{OFF}

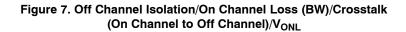


Channel switch control/s test socket is normalized. Off isolation is measured across an off channel. On loss is the bandwidth of an On switch. V_{ISO} , Bandwidth and V_{ONL} are independent of the input signal direction.

$$\begin{split} V_{ISO} &= \text{Off Channel Isolation} = 20 \text{ Log } \left(\frac{V_{OUT}}{V_{IN}} \right) & \text{for } V_{IN} \text{ at } 100 \text{ kHz} \\ V_{ONL} &= \text{On Channel Loss} = 20 \text{ Log } \left(\frac{V_{OUT}}{V_{IN}} \right) & \text{for } V_{IN} \text{ at } 100 \text{ kHz to } 50 \text{ MHz} \end{split}$$

Bandwidth (BW) = the frequency 3 dB below V_{ONL}

 V_{CT} = Use V_{ISO} setup and test to all other switch analog input/outputs terminated with 50 Ω



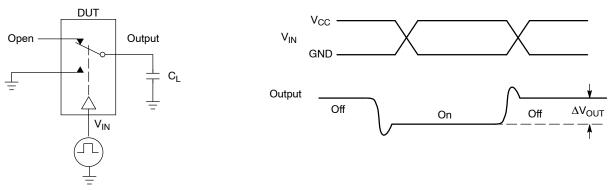


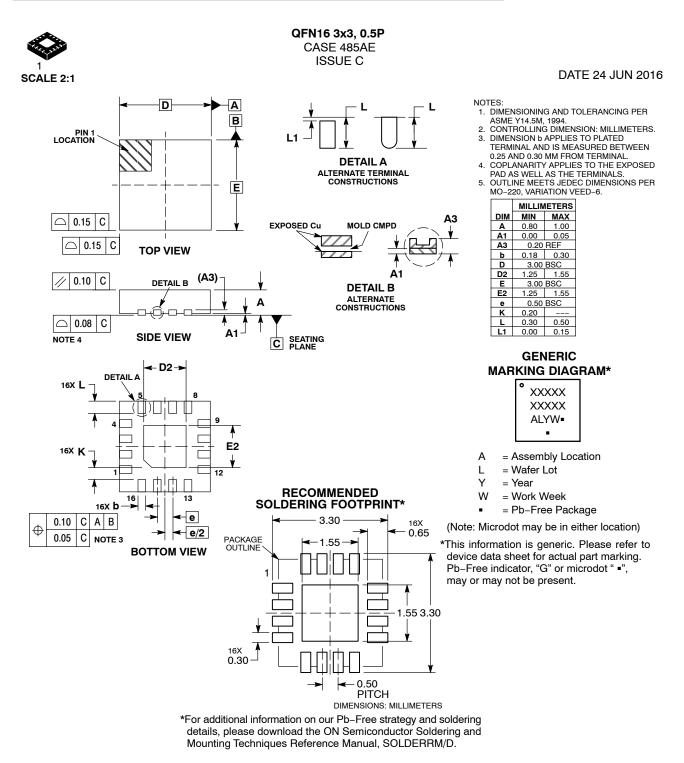
Figure 8. Charge Injection: (Q)

DEVICE ORDERING INFORMATION

Device Order Number	Package Type	Tape & Reel Size [†]	
NLAS3899BMNTBG	WQFN16 (Pb-Free)		
NLAS3899BMNTWG	QFN16 (Pb-Free)	3000 / Tape & Reel	
NLAS3899BMNTXG	QFN16 (Pb-Free)	3000 / Tape & Reel	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



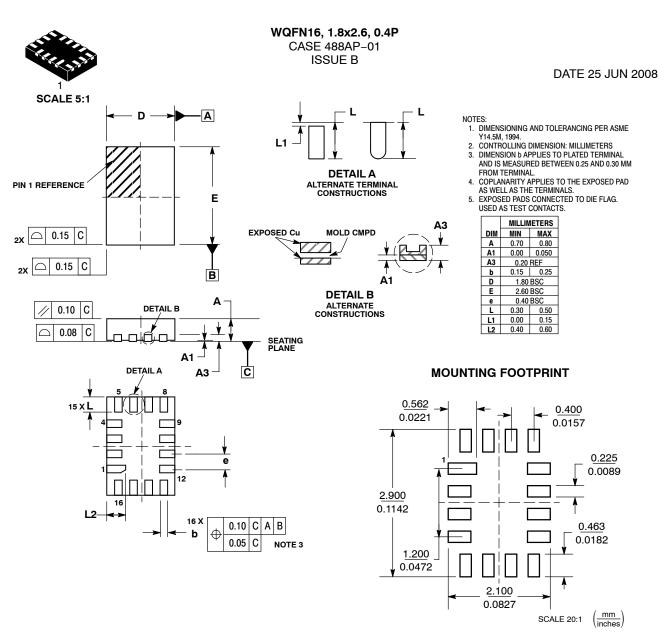


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