NL17SZ06E

Single Inverter with Open Drain Outputs

The NL17SZ06E is a high performance single inverter with open drain outputs operating from a 1.65 to 5.5 V supply.

The Output stage is open drain with Over Voltage Tolerance. This allows the NL17SZ06E to be used to interface 5.0 V circuits to circuits of any voltage between 0 and +5.5 V.

Features

- Tiny SOT-353 Package
- Extremely High Speed: t_{PD} 2.5 ns (typical) at V_{CC} = 5.0 V
- Designed for 1.65 V to 5.5 V V_{CC} Operation, CMOS Compatible
- Over Voltage Tolerant Inputs V_{IN} may be Between 0 and 5.5 V for V_{CC} Between 0.5 and 5.5 V
- TTL Compatible Interface Capability with 5.0 V TTL Logic with V_{CC} = 2.7 V to 3.6 V
- LVCMOS Compatible
- 24 mA Output Sink Capability, Pullup may be between 0 and 5.5 V
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- Chip Complexity: FET = 20
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

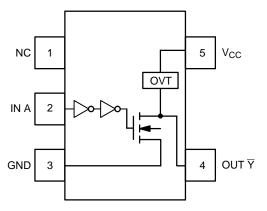


Figure 1. Pinout (Top View)

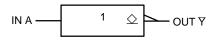
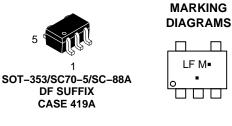


Figure 2. Logic Symbol



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LF = Specific Device Marking

- M = Date Code*
- = Pb–Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or position may vary depending upon manufacturing location.

PIN ASSIGNMENT

Pin	Function
1	NC
2	IN A
3	GND
4	OUT Y
5	V _{CC}

FUNCTION TABLE

A Input	Y Output
L	Z
Н	L

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

MAXIMUM RATINGS

Symbol	Characteristics		Value	Unit
V _{CC}	DC Supply Voltage		-0.5 to +6.5	V
VI	DC Input Voltage		-0.5 to +6.5	V
V _O	DC Output Voltage	Active Mode, LOW State (Note 1) Tri–State Mode Power Down Mode (V _{CC} = 0 V)	−0.5 to V _{CC} + 0.5 −0.5 to +6.5 −0.5 to +6.5	V
I _{IK}	DC Input Diode Current	V _I < GND	-50	mA
I _{OK}	DC Output Diode Current	V _O < GND	-50	mA
Ι _Ο	DC Output Sink Current		±50	mA
I _{CC}	DC Supply Current Per Suppl	ly Pin	±100	mA
I _{GND}	DC Ground Current Per Ground Pin		±100	mA
T _{STG}	Storage Temperature Range		-65 to +150	°C
PD	Power Dissipation in Still Air		186	mW
θ_{JA}	Thermal Resistance		350	°C/W
ΤL	Lead Temperature, 1 mm from	n Case for 10 Seconds	260	°C
TJ	Junction Temperature Under	Bias	+150	°C
MSL	Moisture Sensitivity		Level 1	
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
ESD	ESD Classification	Human Body Model (Note 2) Charged Device Model (Note 3)	4000 1000	V V
I _{Latch-Up}	Latchup Performance	Above V_{CC} and Below GND at 85°C (Note 4)	±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality Io absolute maximum rating must be observed.
Tested to EIA/JESD22-A114-A, rated to EIA/JESD22-A114-B.

Tested to JESD22–C101–A.
Tested to EIA/JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Min	Max	Unit
V _{CC}	Supply Voltage	Operating Data Retention Only	1.65 1.5	5.5 5.5	V
VI	Input Voltage		0	5.5	V
V _O	Output Voltage	Active Mode, LOW State Tri–State Mode Power Down Mode (V _{CC} = 0 V)	0 0 0	V _{CC} 5.5 5.5	V
T _A	Operating Free–Air Temperature		-55	+125	°C
$\Delta t / \Delta V$	Input Transition Rise or Fall Rate	$\begin{array}{l} {\sf V}_{CC} = 2.5 \; {\sf V} \pm 0.2 \; {\sf V} \\ {\sf V}_{CC} = 3.0 \; {\sf V} \pm 0.3 \; {\sf V} \\ {\sf V}_{CC} = 5.0 \; {\sf V} \pm 0.5 \; {\sf V} \end{array}$	0 0 0	20 10 5	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

		V _{CC}		T _A = 25°C		$-55^\circ C \leq T_A \leq 125^\circ C$			
Symbol	Parameter	Condition	(V)	Min	Тур	Max	Min	Max	Unit
V _{IH}	High–Level Input Voltage		1.65 to 1.95 2.3 to 5.5	0.75 V _{CC} 0.7 V _{CC}			0.75 V _{CC} 0.7 V _{CC}		V
V _{IL}	Low–Level Input Voltage		1.65 to 1.95 2.3 to 5.5			0.25 V _{CC} 0.3 V _{CC}		0.25 V _{CC} 0.3 V _{CC}	V
I _{LKG}	Z–State Output Leakage Current	$V_{IN} = V_{IL}$ $V_{OUT} = V_{CC}$ or GND	2.3 to 5.5			±5.0		±10.0	μΑ
V _{OL}	Low–Level Output	I _{OL} = 100 μA	1.65 to 5.5		0.0	0.1		0.1	V
	Voltage V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 4 \text{ mA}$	1.65		0.08	0.24		0.24	
		$I_{OL} = 8 \text{ mA}$	2.3		0.22	0.3		0.3	
		I _{OL} = 12 mA	2.7		0.22	0.4		0.4	
		I _{OL} = 16 mA	3.0		0.28	0.4		0.4	
		I _{OL} = 24 mA	3.0		0.38	0.55		0.55	
		I _{OL} = 32 mA	4.5		0.42	0.55		0.55	
I _{IN}	Input Leakage Current	$V_{IN} = 5.5 \text{ V or GND}$	1.65 to 5.5			±0.1		±1.0	μΑ
I _{OFF}	Power Off Leakage Current	V _{IN} = 5.5 V or V _{OUT} = 5.5 V	0			1.0		10	μΑ
I _{CC}	Quiescent Supply Current	$V_{IN} = 5.5 \text{ V or GND}$	5.5			1.0		10	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

AC ELECTRICAL CHARACTERISTICS $t_R = t_F = 2.5 \text{ ns}$

				Г	r _A = 25°0	C	−55°C ≤ T	[′] A ≤ 125°C	
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
t _{PZL}	Propagation Delay	$R_{L=} R_1 = 500 \Omega, C_L = 50 pF$	1.8 ± 0.15		6.0	9.0		9.5	ns
	(Figure 3 and 4)	igure 3 and 4)	2.5 ± 0.2		3.6	6.1		6.5	
			3.3 ± 0.3		2.7	5.6		6.0	
			5.0 ± 0.5		2.1	4.4		4.8	
t _{PLZ}	Propagation Delay	$R_{L=} R_{1}$ = 500 Ω , C_{L} = 50 pF	1.8 ± 0.15		4.0	9.0		9.5	ns
	(Figure 3 and 4)	(Figure 3 and 4)	2.5 ± 0.2		2.8	6.1		6.5	
			3.3 ± 0.3		2.5	5.6		6.0	
			5.0 ± 0.5		2.2	4.4		4.8	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Unit
C _{IN}	Input Capacitance	V_{CC} = 5.5 V, V_{I} = 0 V or V_{CC}	>2.5	pF
C _{OUT}	Output Capacitance	V_{CC} = 5.5 V, V_{I} = 0 V or V_{CC}	4.0	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	10 MHz, V_{CC} = 5.5 V, V_{I} = 0 V or V_{CC}	4.0	pF

5. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no–load dynamic power consumption; P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

NL17SZ06E

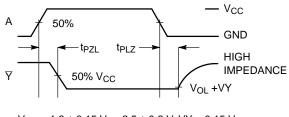
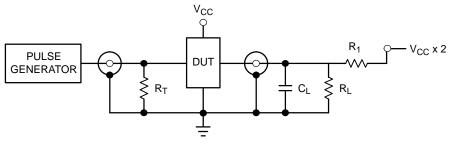


Figure 3. Switching Waveforms



 $R_T = Z_{OUT}$ of pulse generator (typically 50 Ω)

Figure 4. Test Circuit

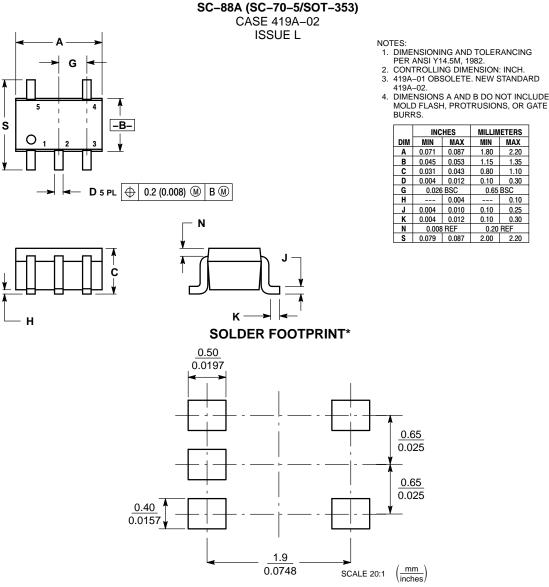
DEVICE ORDERING INFORMATION

Device Order Number	Package Type	Tape and Reel Size †
NL17SZ06EDFT2G	SOT-353/SC70-5/SC-88A (Pb-Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NL17SZ06E

PACKAGE DIMENSIONS



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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