

## Dual Enhancement Mode MOSFET (N- and P-Channel)

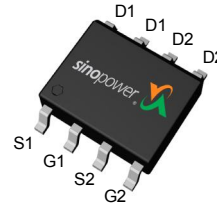
### Features

- N-Channel  
20V/9.5A,  
 $R_{DS(ON)} = 14m\Omega(\text{max.}) @ V_{GS} = 4.5V$   
 $R_{DS(ON)} = 18m\Omega(\text{max.}) @ V_{GS} = 2.5V$
- P-Channel  
-20V/-6A,  
 $R_{DS(ON)} = 45m\Omega(\text{max.}) @ V_{GS} = -4.5V$   
 $R_{DS(ON)} = 65m\Omega(\text{max.}) @ V_{GS} = -2.5V$
- 100% UIS +  $R_g$  Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

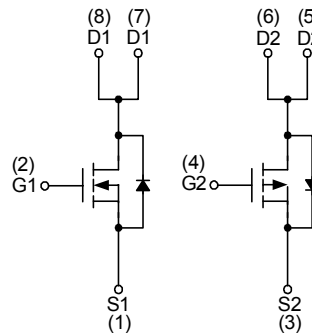
### Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

### Pin Description




Top View of SOP-8



N-Channel MOSFET    P-Channel MOSFET

### Ordering and Marking Information

<p>SM2001CS □□-□□ □</p> <p>Assembly Material Handling Code Temperature Range Package Code</p>	<p>Package Code K : SOP-8 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape &amp; Reel Assembly Material G : Halogen and Lead Free Device</p>
<p>SM2001CS K :  2001 XXXXX</p>	<p>XXXXX - Lot Code</p>

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

## Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Channel 1	Channel 2	Unit	
<b>Common Ratings (<math>T_A=25^\circ\text{C}</math> Unless Otherwise Noted)</b>					
$V_{DSS}$	Drain-Source Voltage	20	-20	V	
$V_{GSS}$	Gate-Source Voltage	$\pm 12$	$\pm 12$		
$T_J$	Maximum Junction Temperature	150		$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150			
$I_S$	Diode Continuous Forward Current	$T_A=25^\circ\text{C}$	1.8	-1.2	A
$I_D$	Continuous Drain Current	$T_A=25^\circ\text{C}$	9.5	-6	
$I_{DM}^a$	Pulsed Drain Current	$T_A=25^\circ\text{C}$	30	-20	
$P_D$	Power Dissipation	$T_A=25^\circ\text{C}$	2	2	W
		$T_A=70^\circ\text{C}$	1.3	1.3	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	45	45	$^\circ\text{C/W}$
		Steady State	62.5	62.5	
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	Steady State	20	20	
$I_{AS}^b$	Avalanche Current, Single pulse (L=0.1mH)		16	-15	A
$E_{AS}^b$	Avalanche Energy, Single pulse (L=0.1mH)		12	11	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature  $150^\circ\text{C}$  (initial temperature  $T_J=25^\circ\text{C}$ ).

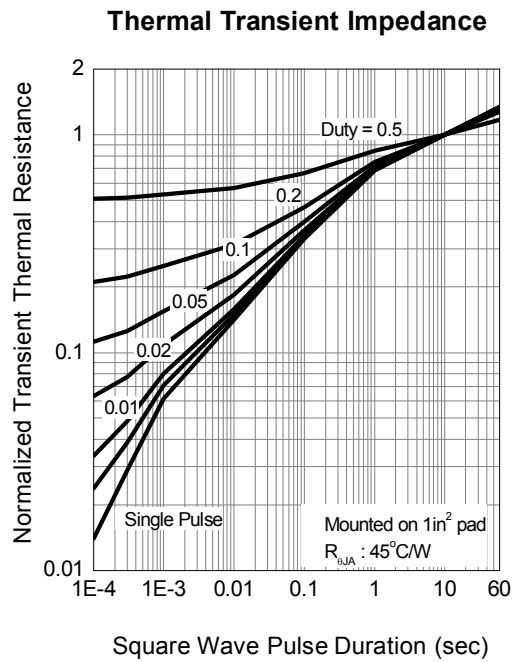
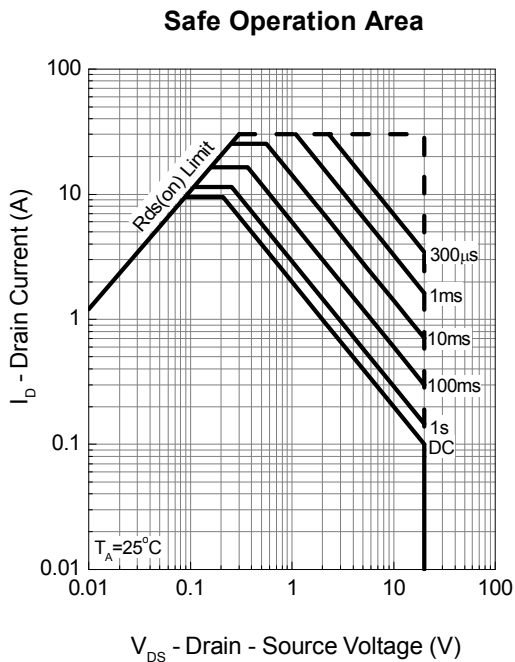
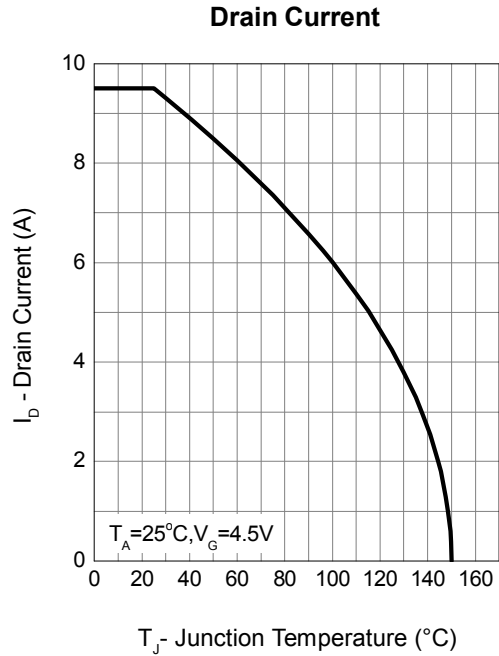
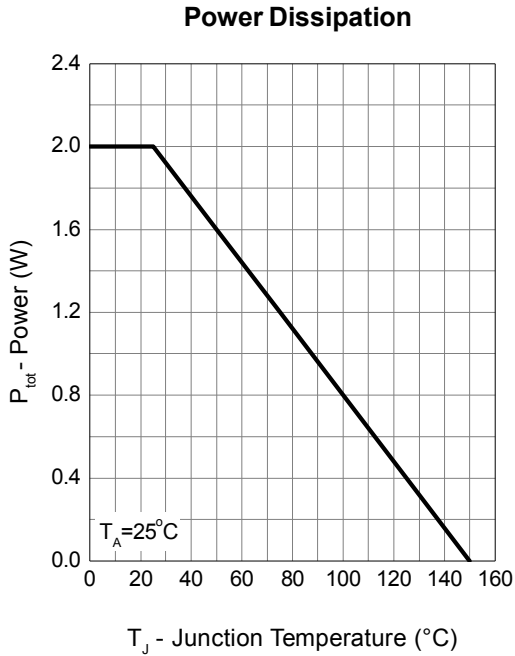
## Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Channel 1			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	20	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V	-	-	1	μA
		T <sub>J</sub> =85°C	-	-	30	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	0.5	0.7	1.0	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	-	-	±100	nA
R <sub>DS(ON)</sub>	Drain-Source On-state Resistance	V <sub>GS</sub> =4.5V, I <sub>DS</sub> =9A	-	12	14	mΩ
		V <sub>GS</sub> =2.5V, I <sub>DS</sub> =6A	-	13.5	18	
<b>Diode Characteristics</b>						
V <sub>SD</sub>	Diode Forward Voltage	I <sub>SD</sub> =1.5A, V <sub>GS</sub> =0V	-	0.67	1.1	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>DS</sub> =6A, dI <sub>SD</sub> /dt=100A/μs	-	11.8	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	3.6	-	nC
<b>Dynamic Characteristics</b>						
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	-	0.7	-	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =10V, Frequency=1.0MHz	-	795	-	pF
C <sub>oss</sub>	Output Capacitance		-	160	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	118	-	
t <sub>d(ON)</sub>	Turn-on Delay Time		V <sub>DD</sub> =10V, R <sub>L</sub> =10Ω, I <sub>DS</sub> =1A, V <sub>GEN</sub> =4.5V, R <sub>G</sub> =1Ω	-	10.2	19
t <sub>r</sub>	Turn-on Rise Time	-		16.1	30	
t <sub>d(OFF)</sub>	Turn-off Delay Time	-		16.6	31	
t <sub>f</sub>	Turn-off Fall Time	-		3.6	7	
<b>Gate Charge Characteristics</b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>DS</sub> =6A	-	8.7	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	1.25	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	3	-	
Q <sub>gth</sub>	Threshold Gate Charge		-	0.54	-	

## Electrical Characteristics (Cont.) ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

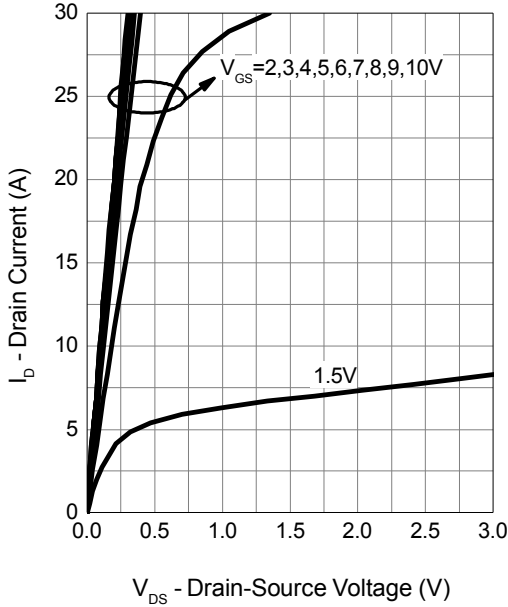
Symbol	Parameter	Test Conditions	Channel 2			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=-250\mu A$	-20	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-16V, V_{GS}=0V$	-	-	-1	$\mu A$
		$T_J=85^\circ C$	-	-	-30	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-0.5	-0.7	-1	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 100$	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=-4.5V, I_{DS}=-6A$	-	36	45	m $\Omega$
		$V_{GS}=-2.5V, I_{DS}=-4A$	-	50	65	
<b>Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage	$I_{SD}=-1.2A, V_{GS}=0V$	-	-0.7	-1	V
$t_{rr}$	Reverse Recovery Time	$I_{sd}=-6A, dI_{SD}/dt=100A/\mu s$	-	12	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	4.5	-	nC
<b>Dynamic Characteristics</b>						
$R_G$	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	3.5	-	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=-10V,$ Frequency=1.0MHz	-	590	-	pF
$C_{oss}$	Output Capacitance		-	122	-	
$C_{riss}$	Reverse Transfer Capacitance		-	92	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=-10V, R_L=10\Omega,$ $I_{DS}=-1A, V_{GEN}=-4.5V,$ $R_G=1\Omega$	-	8.4	16	ns
$t_r$	Turn-on Rise Time		-	14.1	26	
$t_{d(OFF)}$	Turn-off Delay Time		-	16	30	
$t_f$	Turn-off Fall Time		-	10.7	20	
<b>Gate Charge Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{DS}=-10V, V_{GS}=-4.5V,$ $I_{DS}=-6A$	-	7.68	-	nC
$Q_{gs}$	Gate-Source Charge		-	0.66	-	
$Q_{gd}$	Gate-Drain Charge		-	2.72	-	
$Q_{gth}$	Threshold Gate Charge		-	0.3	-	

## N Channel Typical Operating Characteristics

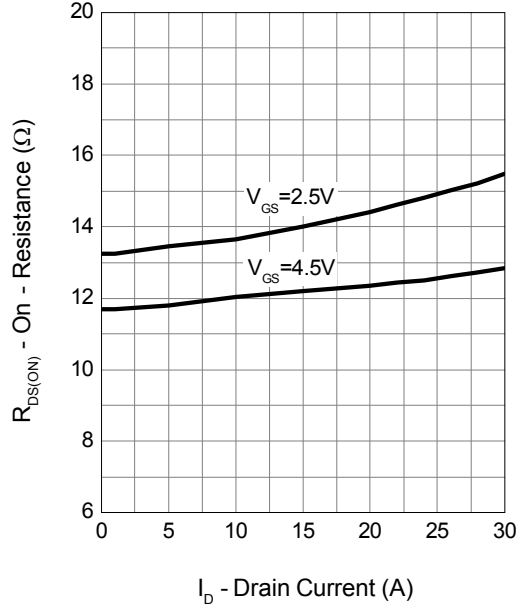


## N Channel Typical Operating Characteristics (Cont.)

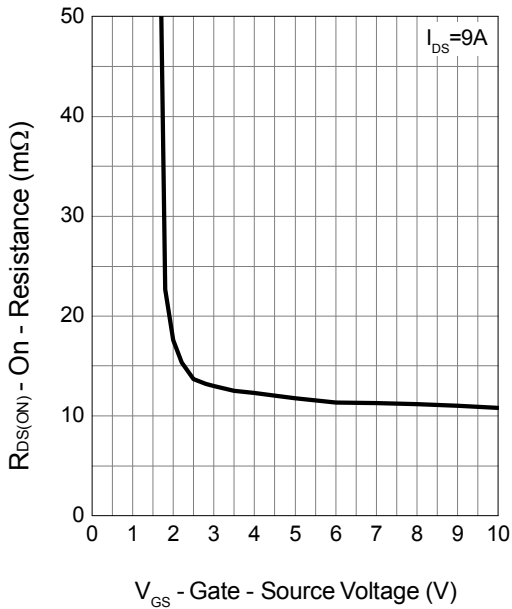
Output Characteristics



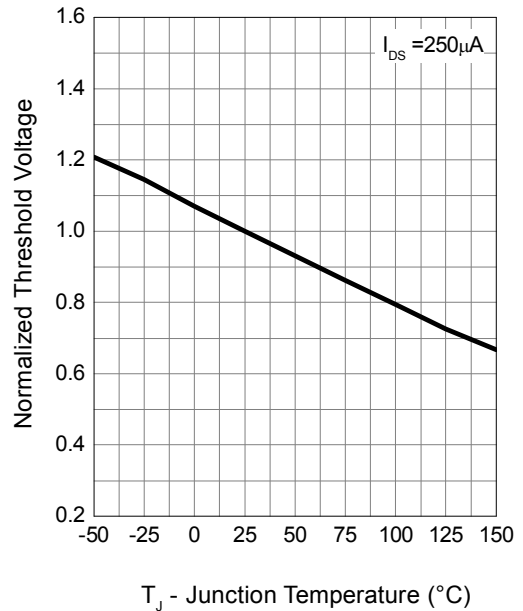
Drain-Source On Resistance



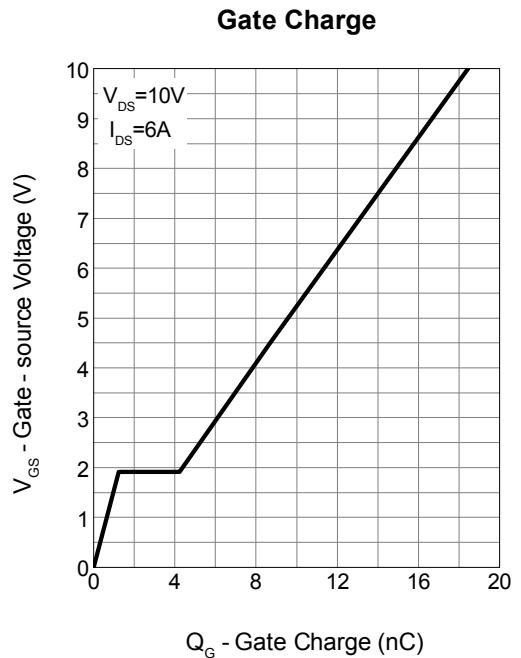
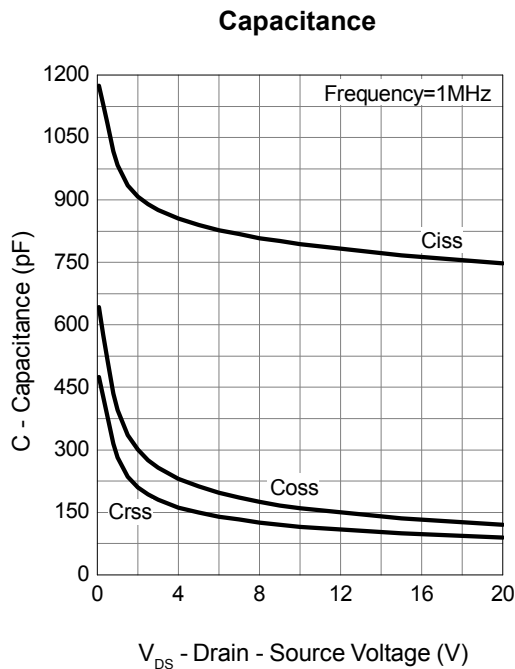
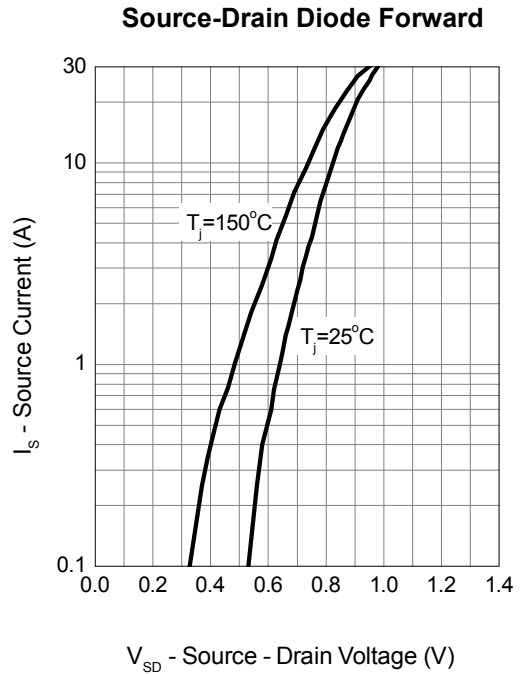
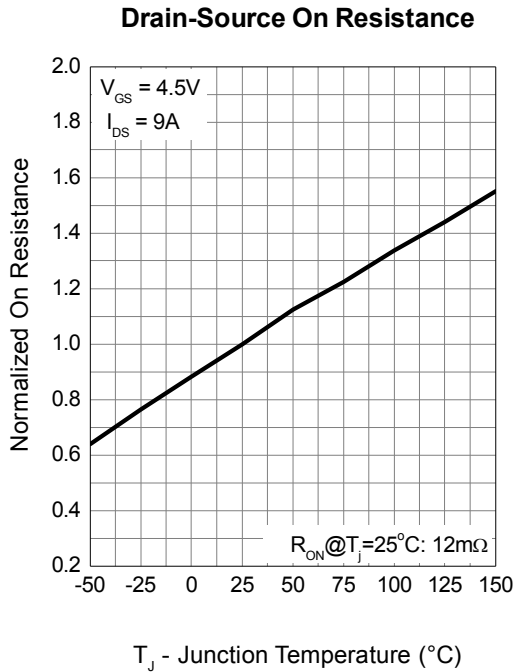
Transfer Characteristics



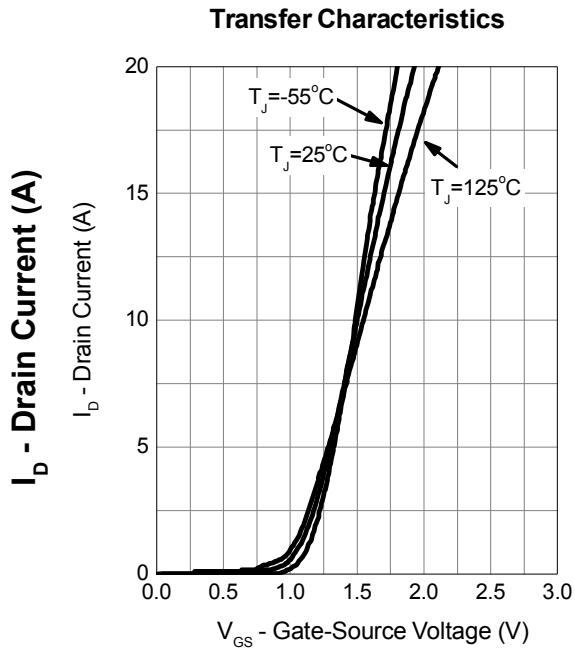
Gate Threshold Voltage



## N Channel Typical Operating Characteristics (Cont.)

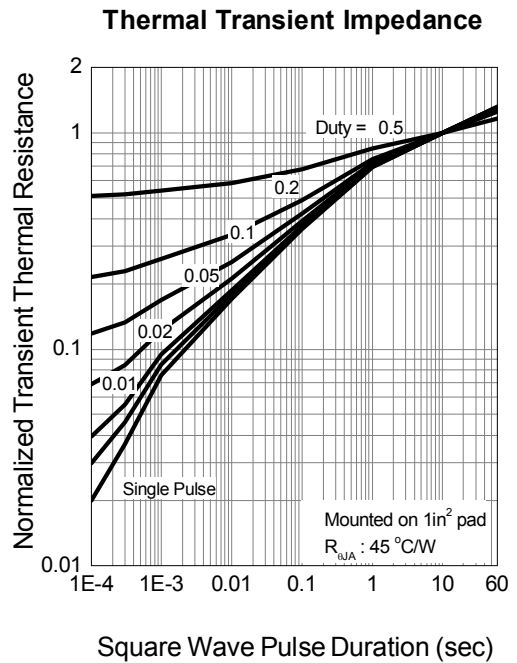
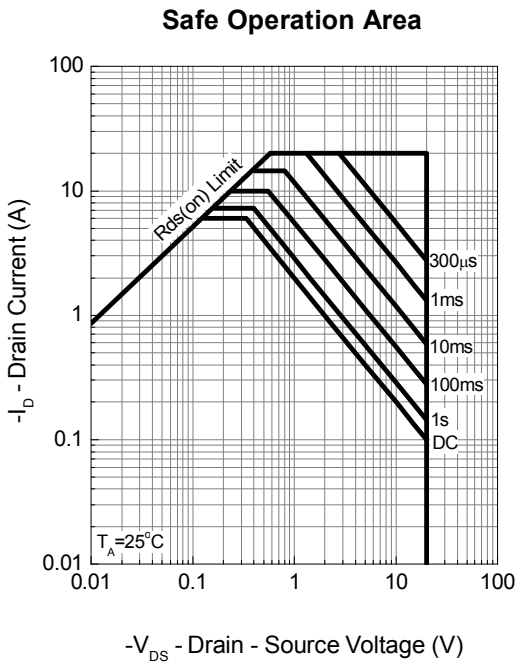
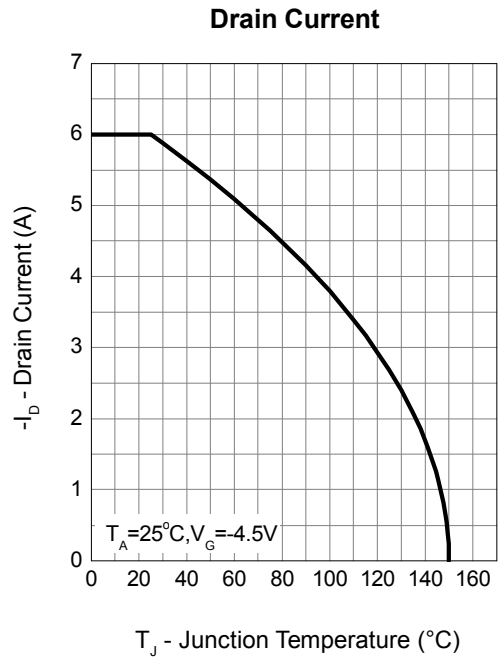
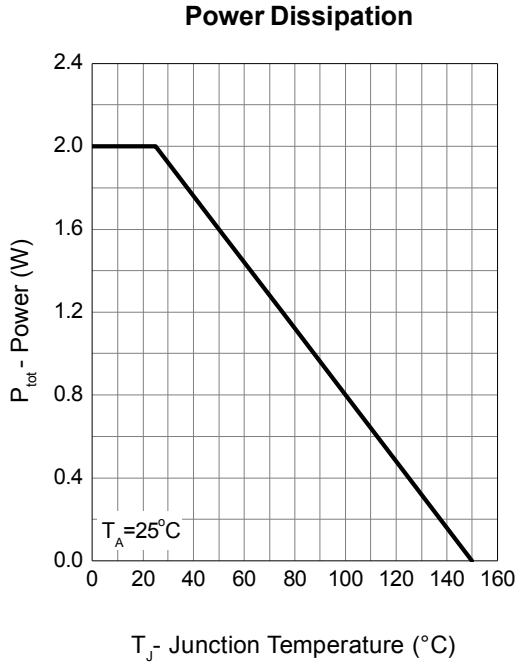


N Channel Typical Operating Characteristics (Cont.)

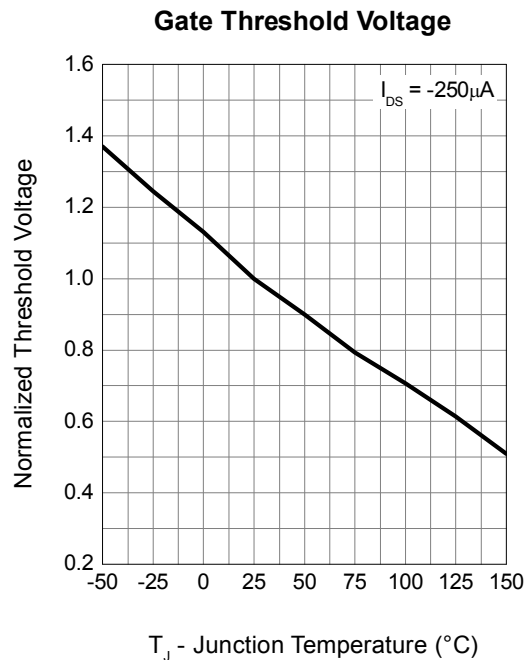
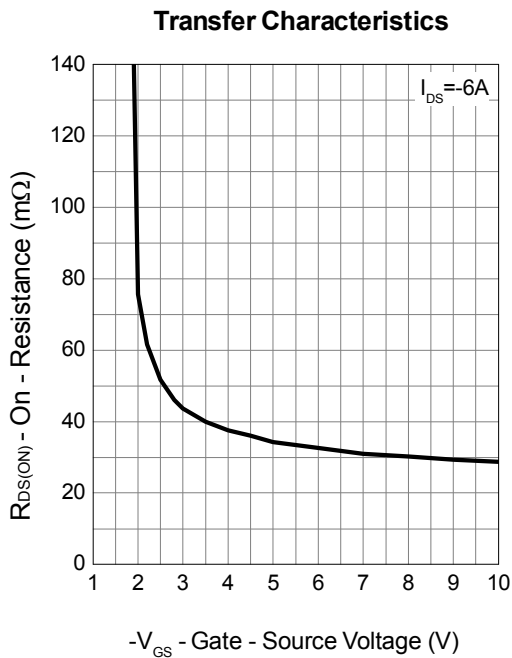
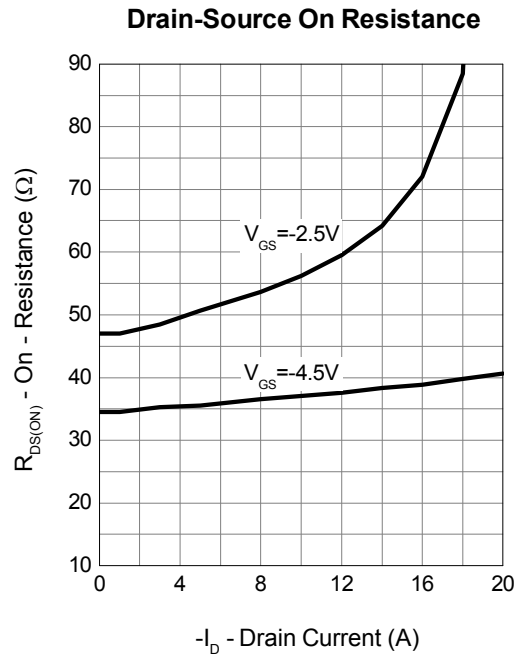
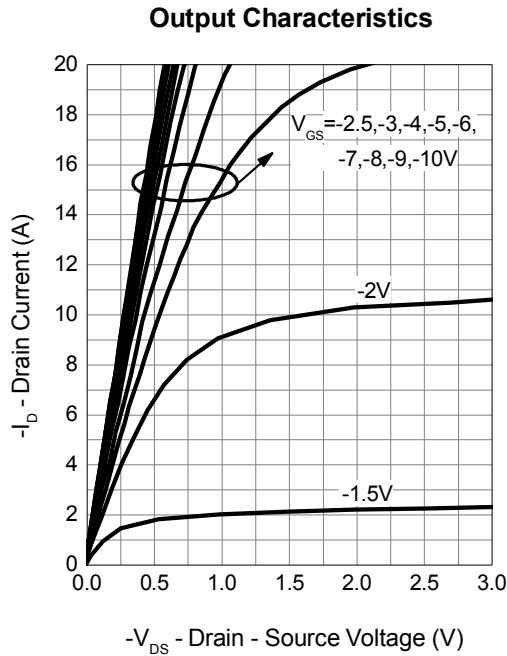




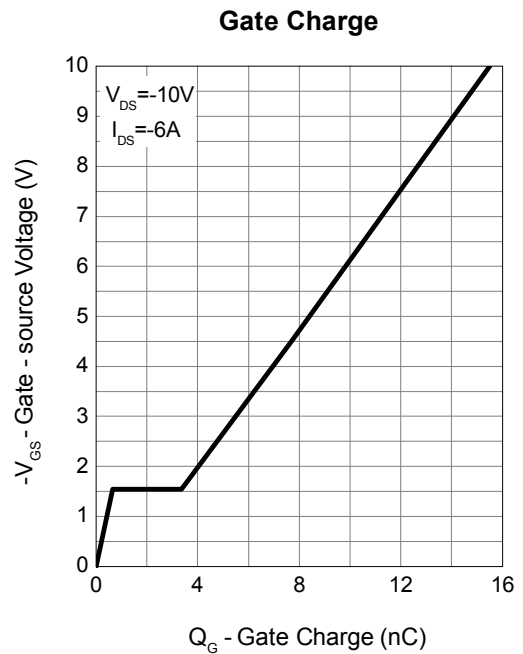
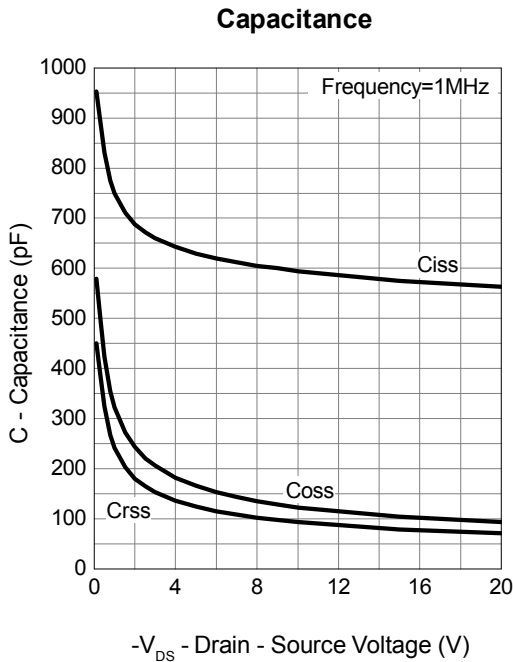
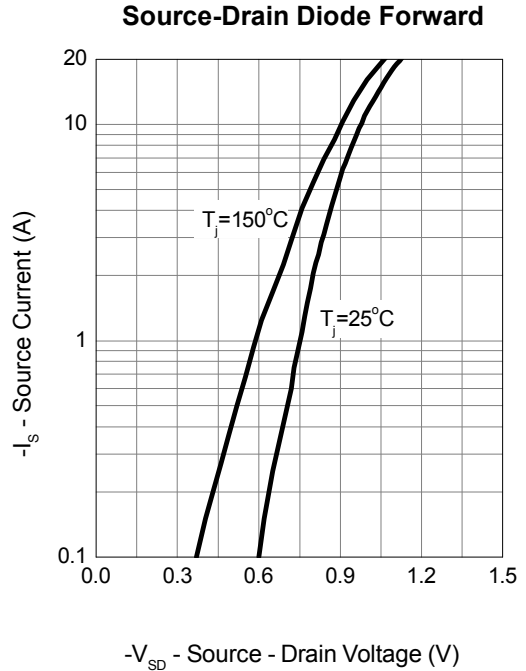
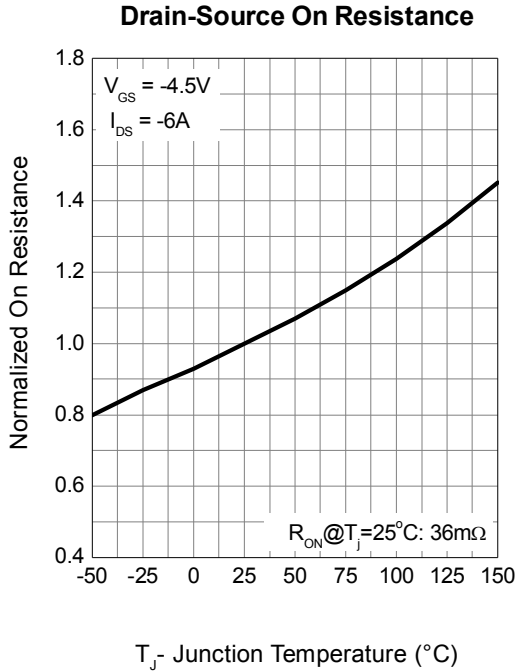
## P Channel Typical Operating Characteristics



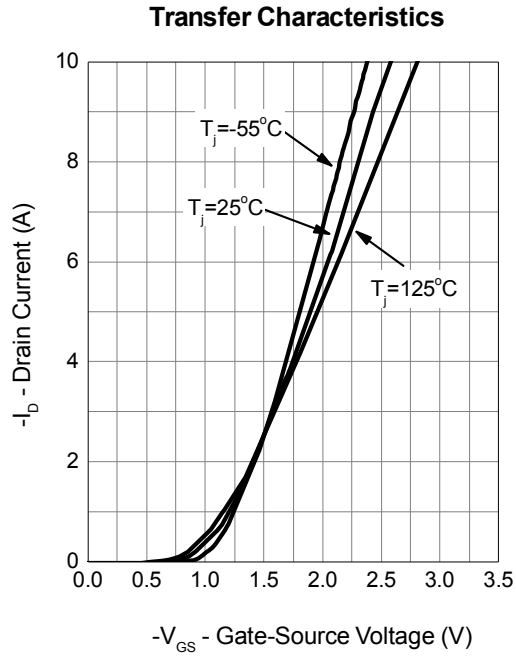
## P Channel Typical Operating Characteristics (Cont.)



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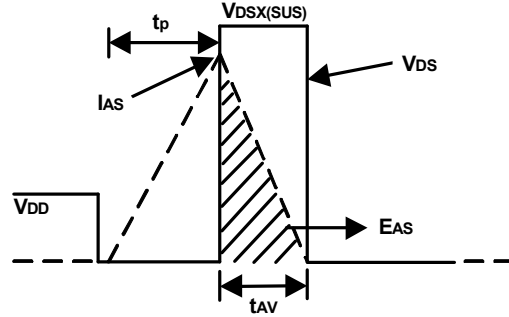
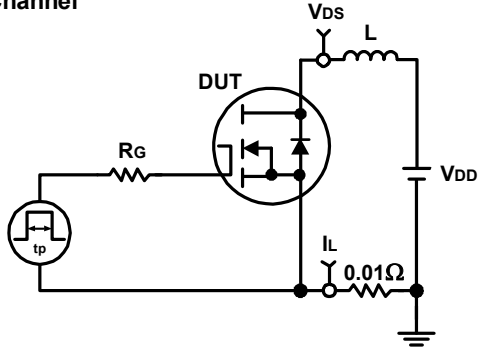


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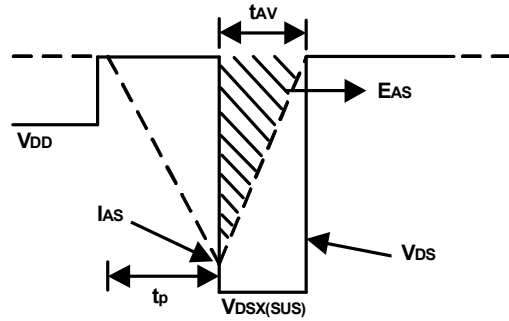
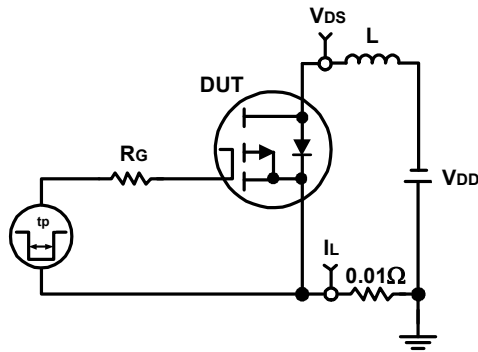


## Avalanche Test Circuit and Waveforms

N Channel

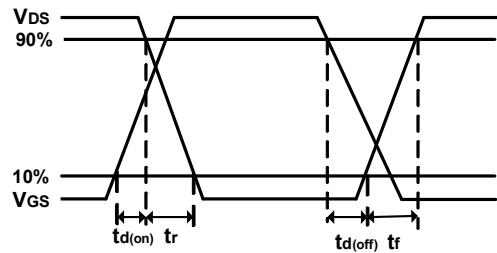
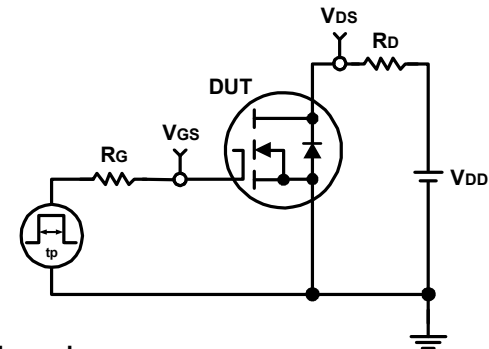


P Channel

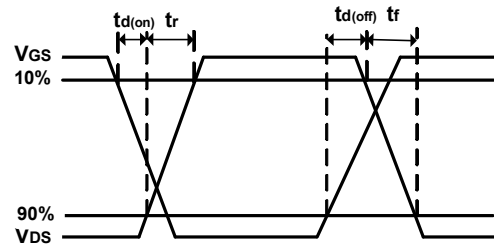
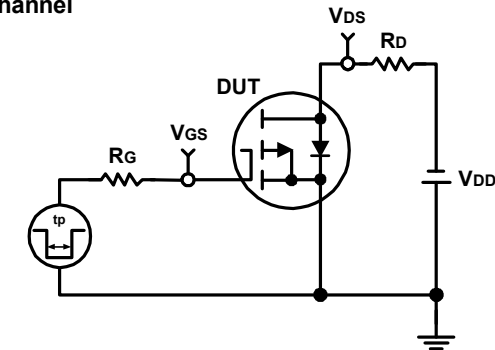


## Switching Time Test Circuit and Waveforms

N Channel



P Channel



## Disclaimer

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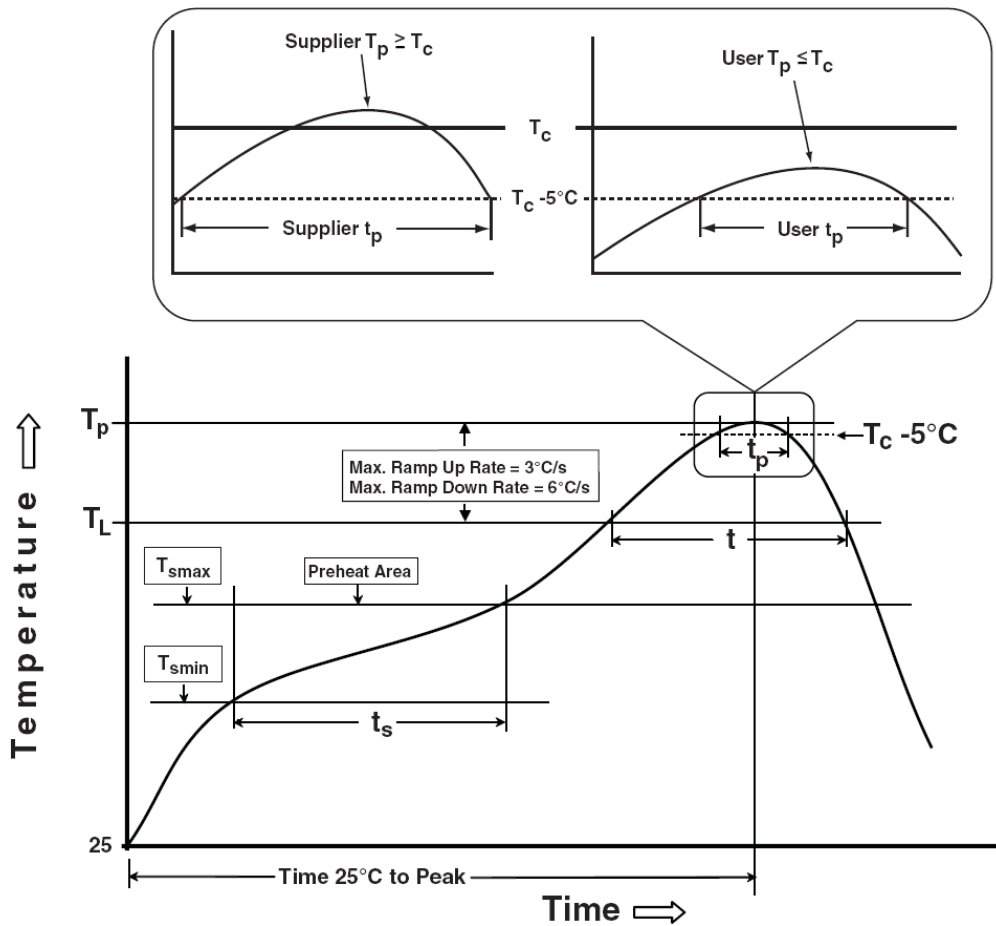
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In order to unify the quality and performance, Sinopower has been following JEDEC while defines assembly rule. Notwithstanding all the suppliers basically follow the rule for each product, different processes may cause slightly different results.

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Classification Profile



## Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{smin}$ )	100 °C	150 °C
Temperature max ( $T_{smax}$ )	150 °C	200 °C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217 °C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature ( $T_p$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_p$ )** within 5°C of the specified classification temperature ( $T_c$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ $T_{jmax}$
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ $T_{jmax}$
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

## Customer Service

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