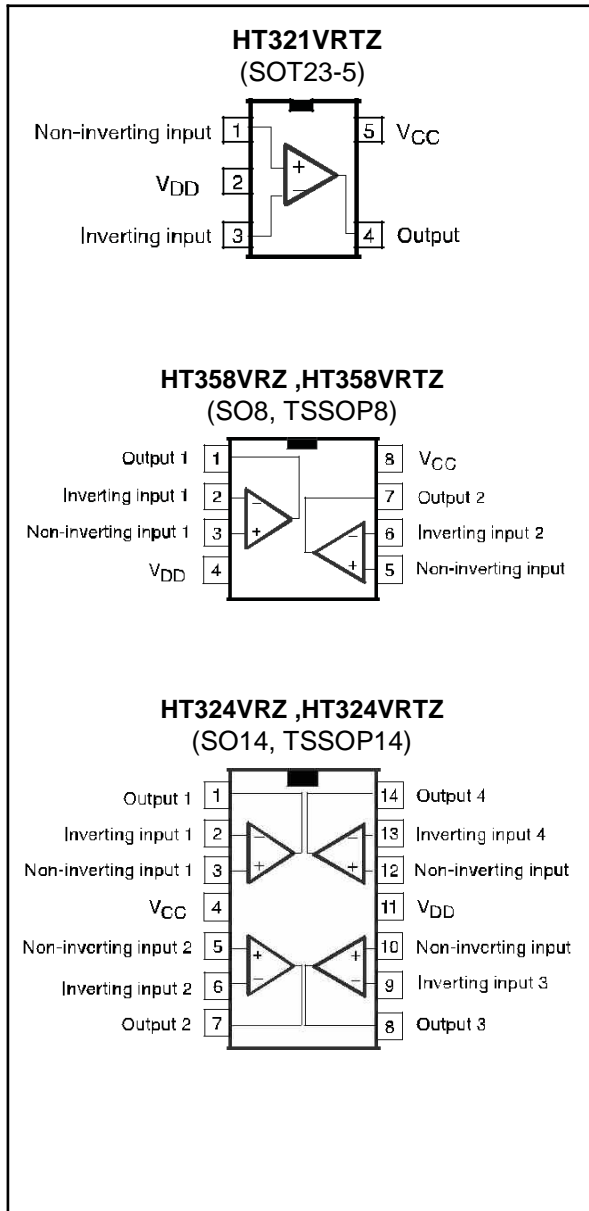


Low cost, low power, input/output rail-to-rail operational amplifiers



Features

- Operating range from $V_{CC} = 2.7$ to 6 V
- Rail-to-rail input and output
- Extended V_{icm} ($V_{DD} - 0.2$ V to $V_{CC} + 0.2$ V)
- Low supply current ($145 \mu\text{A}$)
- Gain bandwidth product (1 MHz)
- ESD tolerance (2 kV)

Related products

- See HT321V, HT358V, HT324V for newer technological version
- See TSV851, TSV852, TSV854 for enhanced performances

Applications

- Battery powered electronic equipment
- Personal medical care (glucose meters)
- Laptops

Description

The HT321V/358/324 family (single, dual, and quad) answers the need for low cost, general-purpose operational amplifiers. They operate with voltages as low as 2.7 V and feature both input and output rail-to-rail, $145 \mu\text{A}$ consumption current, and 1 MHz gain bandwidth product (GBP).

With such a low consumption and a sufficient GBP for many applications, these op amps are well suited for any kind of battery supplied and portable equipment application.

The HT321V device is housed in the space-saving 5 -pin SOT23-5 package, which simplifies board design. The SOT23-5 has two pinning configurations to answer all application requirements.

1 Absolute maximum ratings and operating conditions

Table 1: Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V _{CC}	Supply voltage ⁽¹⁾	7	V	
V _{id}	Differential input voltage ⁽²⁾	±1		
V _{in}	Input voltage	V _{DD} - 0.3 to V _{CC} + 0.3		
T _{oper}	Operating free air temperature range	-40 to 125	°C	
T _{stg}	Storage temperature	-65 to 150		
T _j	Maximum junction temperature	150		
R _{thja}	Thermal resistance junction-to-ambient ⁽³⁾	SOT23-5	250	°C/W
		SO8	125	
		TSSOP8	120	
		SO14	103	
		TSSOP14	100	
R _{thjc}	Thermal resistance junction-to-case ⁽³⁾	SOT23-5	81	
		SO8	40	
		TSSOP8	37	
		SO14	31	
		TSSOP14	32	
ESD	HBM: human body model ⁽⁴⁾	2	kV	
	MM: machine model ⁽⁵⁾	200	V	
	CDM: charged device model ⁽⁶⁾	1.5	kV	
	Lead temperature (soldering, 10 s)	250	°C	
	Output short-circuit duration	See ⁽⁷⁾		

Notes:

(1) All voltage values, except differential voltage are with respect to network terminal.

(2) The differential voltage is the non-inverting input terminal with respect to the inverting input terminal. If V_{id} > ±1 V, the maximum input current must not exceed ±1 mA. In this case (V_{id} > ±1 V), an input series resistor must be added to limit input current.

(3) Short-circuits can cause excessive heating. Destructive dissipation can result from simultaneous short-circuits on all amplifiers. All values are typical.

(4) Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.

(5) Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.

(6) Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins. No value specified for CDM on SOT23-5 package. The value is given for SO8 and TSSOP packages.

(7) Short-circuits from the output to V_{CC} can cause excessive heating. The maximum output current is approximately 48 mA, independent of the magnitude of V_{CC}. Destructive dissipation can result from simultaneous short-circuits on all amplifiers.

Table 2: Operating conditions

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	2.7 to 6	V
V _{icm}	Common mode input voltage range ⁽¹⁾	V _{DD} - 0.2 to V _{CC} + 0.2	
V _{icm}	Common mode input voltage range ⁽²⁾	V _{DD} to V _{CC}	
T _{oper}	Operating free air temperature range	-40 to 125	°C

Notes:

⁽¹⁾ At 25 °C, for $2.7 \leq V_{CC} \leq 6$ V, V_{icm} is extended to V_{DD} - 0.2 V, V_{CC} + 0.2 V.

⁽²⁾ In full temperature range, both rails can be reached when V_{CC} does not exceed 5.5 V.

2 Electrical characteristics

Table 3: Electrical characteristics at VCC = 2.7 V, VDD = 0 V, CL and RL connected to VCC/2, Tamb = 25 °C (unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V _{io}	Input offset voltage	V _{icm} = V _{out} = V _{CC} /2		0.1	3	mV
		T _{min} ≤ T _{amb} ≤ T _{max}			6	
ΔV _{io} /ΔT	Input offset voltage drift			2		μV/°C
I _{io}	Input offset current	V _{icm} = V _{out} = V _{CC} /2 ⁽¹⁾		1	9	nA
		T _{min} ≤ T _{amb} ≤ T _{max}			25	
I _{ib}	Input bias current	V _{icm} = V _{out} = V _{CC} /2 ⁽¹⁾		10	50	
		T _{min} ≤ T _{amb} ≤ T _{max}			85	
CMR	Common mode rejection ratio	0 ≤ V _{icm} ≤ V _{CC}	55	85		dB
SVR	Supply voltage rejection ratio	V _{icm} = V _{CC} /2	70	80		
A _{vd}	Large signal voltage gain	V _{out} = 0.5 V to 2.2 V, R _L = 10 kΩ	80	100		
		V _{out} = 0.5 V to 2.2 V, R _L = 2 kΩ	70	88		
V _{OH}	High level output voltage	V _{id} = 100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 10 kΩ	2.6	2.65		V
		V _{id} = 100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 2 kΩ	2.55	2.6		
V _{OL}	Low level output voltage	V _{id} = -100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 10 kΩ		15	90	mV
		V _{id} = -100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 2 kΩ		50	100	
I _o	Output current	Output source current, V _{id} = 100 mV, V _O = V _{DD}	5	46		mA
		Output sink current, V _{id} = -100 mV, V _O = V _{CC}	5	46		
I _{CC}	Supply current (per amplifier)	V _{out} = V _{CC} /2, A _{vCL} = 1, no load		145	200	μA
		T _{min} ≤ T _{amb} ≤ T _{max}			230	
GBP	Gain bandwidth product	R _L = 10 kΩ, C _L = 100 pF, f = 100 kHz		1		MHz
SR	Slew rate	R _L = 600 Ω, C _L = 100 pF, A _v = 1		0.35		V/μs
φ _m	Phase margin	R _L = 600 Ω, C _L = 100 pF		44		Degrees
e _n	Input voltage noise			40		nV/√Hz
THD	Total harmonic distortion			0.01		%

Notes:

⁽¹⁾Maximum values include unavoidable inaccuracies of the industrial tests.

Table 4: Electrical characteristics at VCC = 5 V, VDD = 0 V, CL and RL connected to VCC/2, Tamb = 25 °C (unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V _{io}	Input offset voltage	V _{icm} = V _{out} = V _{CC} /2		0.1	3	mV
		T _{min} ≤ T _{amb} ≤ T _{max}			6	
ΔV _{io} /ΔT	Input offset voltage drift			2		μV/°C
I _{io}	Input offset current	V _{icm} = V _{out} = V _{CC} /2 ⁽¹⁾		1	9	nA
		T _{min} ≤ T _{amb} ≤ T _{max}			25	
I _{ib}	Input bias current	V _{icm} = V _{out} = V _{CC} /2 ⁽¹⁾		16	63	nA
		T _{min} ≤ T _{amb} ≤ T _{max}			95	
CMR	Common mode rejection ratio	0 ≤ V _{icm} ≤ V _{CC}	65	95		dB
SVR	Supply voltage rejection ratio	V _{icm} = V _{CC} /2	70	90		
A _{vd}	Large signal voltage gain	V _{out} = 0.5 V to 4.5 V, R _L = 10 kΩ	85	97		
		V _{out} = 0.5 V to 4.5 V, R _L = 2 kΩ	77	93		
V _{OH}	High level output voltage	V _{id} = 100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 10 kΩ	4.85	4.95		V
		V _{id} = 100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 2 kΩ	4.8	4.91		
V _{OL}	Low level output voltage	V _{id} = -100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 10 kΩ		40	180	mV
		V _{id} = -100 mV, T _{min} ≤ T _{amb} ≤ T _{max} , R _L = 2 kΩ		80	200	
I _o	Output current	Output source current, V _{id} = 100 mV, V _O = V _{DD}	7	48		mA
		Output sink current, V _{id} = -100 mV, V _O = V _{CC}	7	48		
I _{CC}	Supply current (per amplifier)	V _{out} = V _{CC} /2, A _{VCL} = 1, no load		162	220	μA
		T _{min} ≤ T _{amb} ≤ T _{max}			250	
GBP	Gain bandwidth product	R _L = 10 kΩ, C _L = 100 pF, f = 100 kHz		1.3		MHz
SR	Slew rate	R _L = 600 Ω, C _L = 100 pF, A _V = 1		0.45		V/μs
φ _m	Phase margin	R _L = 600 Ω, C _L = 100 pF		48		Degrees
e _n	Input voltage noise			40		nV/√Hz
THD	Total harmonic distortion			0.01		%

Notes:

⁽¹⁾ Maximum values include unavoidable inaccuracies of the industrial tests.

Figure 1: Supply current/amplifier vs. supply voltage

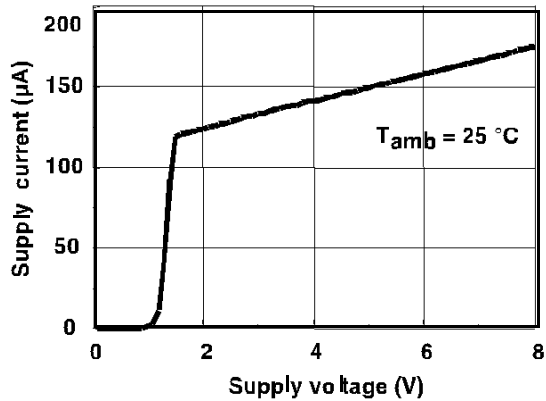
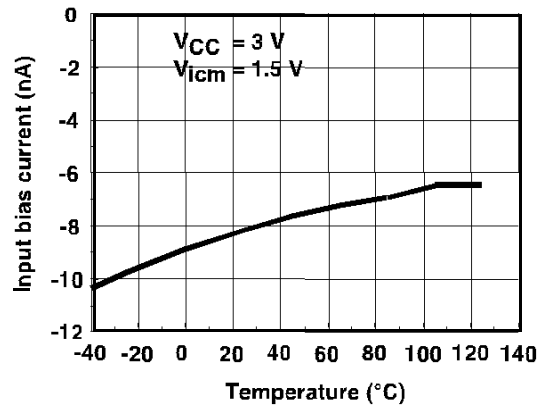
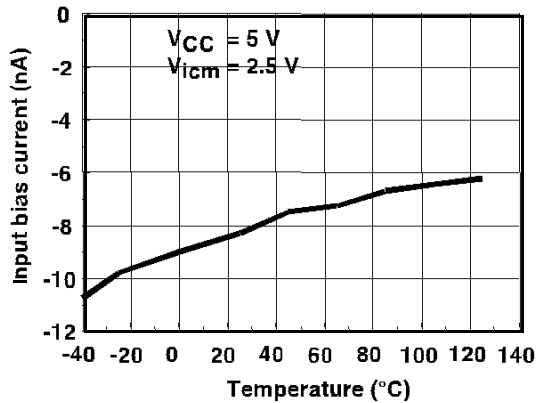
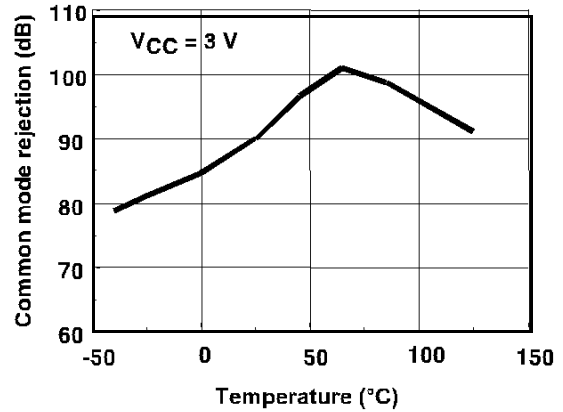
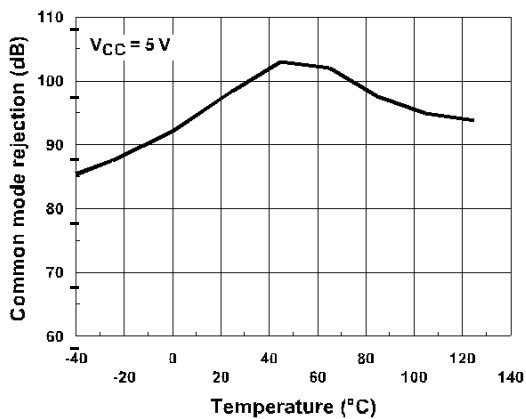
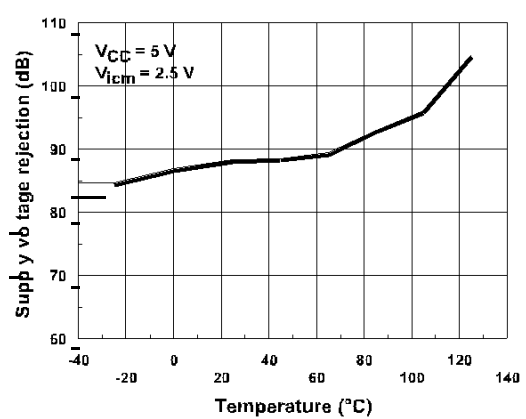

 Figure 2: Input bias current vs. temperature
 ($V_{CC} = 3\text{ V}$, $V_{icm} = 1.5\text{ V}$)

 Figure 3: Input bias current vs. temperature
 ($V_{CC} = 5\text{ V}$, $V_{icm} = 2.5\text{ V}$)

 Figure 4: Common mode rejection vs. temperature
 ($V_{CC} = 3\text{ V}$)

 Figure 5: Common mode rejection vs. temperature
 ($V_{CC} = 5\text{ V}$)

 Figure 6: Supply voltage rejection vs. temperature
 ($V_{CC} = 5\text{ V}$, $V_{icm} = 2.5\text{ V}$)


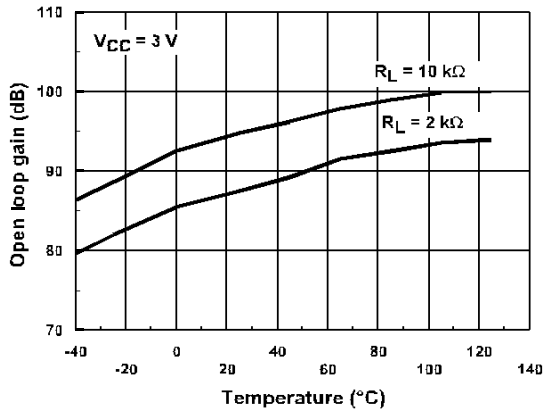
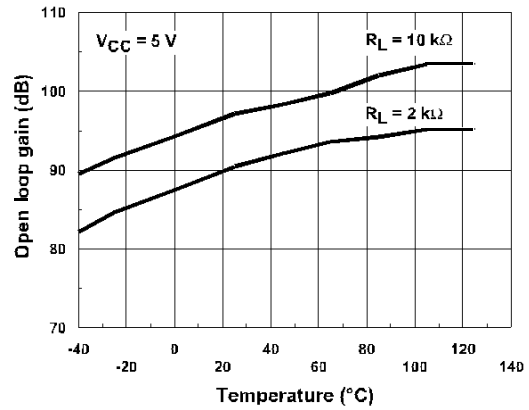
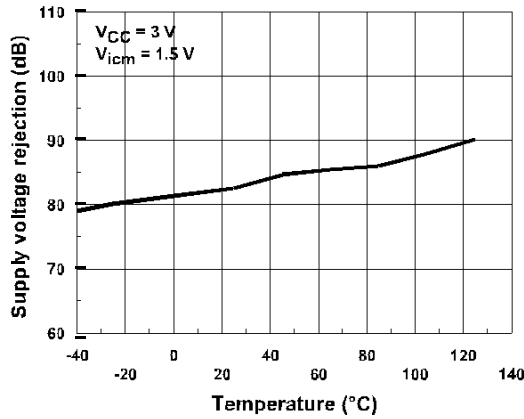
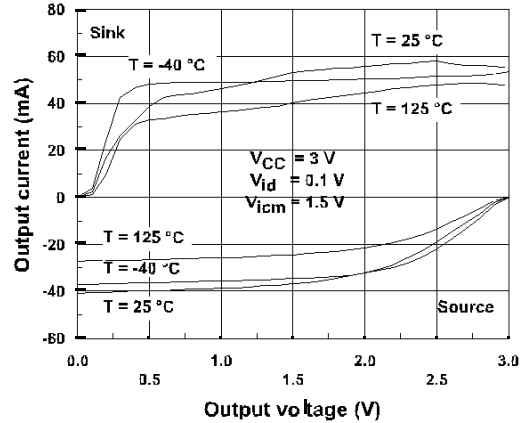
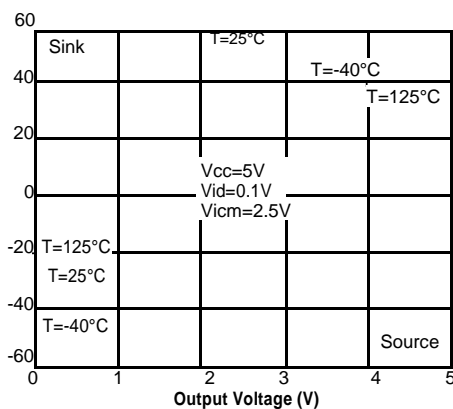
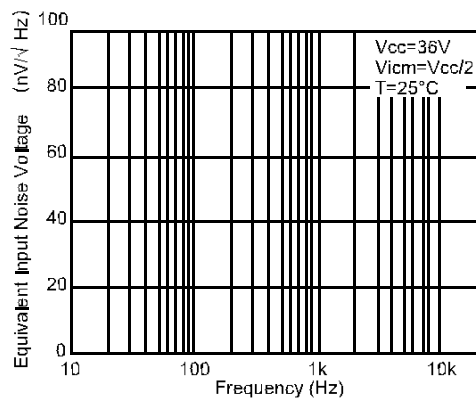
Figure 7: Open loop gain vs. temperature
 ($V_{CC} = 3\text{ V}$, $R_L = 10/2\text{ kW}$)

 Figure 8: Open loop gain vs. temperature
 ($V_{CC} = 5\text{ V}$, $R_L = 10/2\text{ kW}$)

 Figure 9: Supply voltage rejection vs. temperature
 ($V_{CC} = 3\text{ V}$, $V_{icm} = 1.5\text{ V}$)

 Figure 10: Output current vs. output voltage
 ($V_{CC} = 3\text{ V}$, $V_{id} = 0.1\text{ V}$, $V_{icm} = 1.5\text{ V}$)

 Figure 11: Output current vs. output voltage
 ($V_{CC} = 5\text{ V}$, $V_{id} = 0.1\text{ V}$, $V_{icm} = 2.5\text{ V}$)


Figure 12: Noise versus frequency



3.1 SOT23-5 package information

Figure 13: SOT23-5 package outline

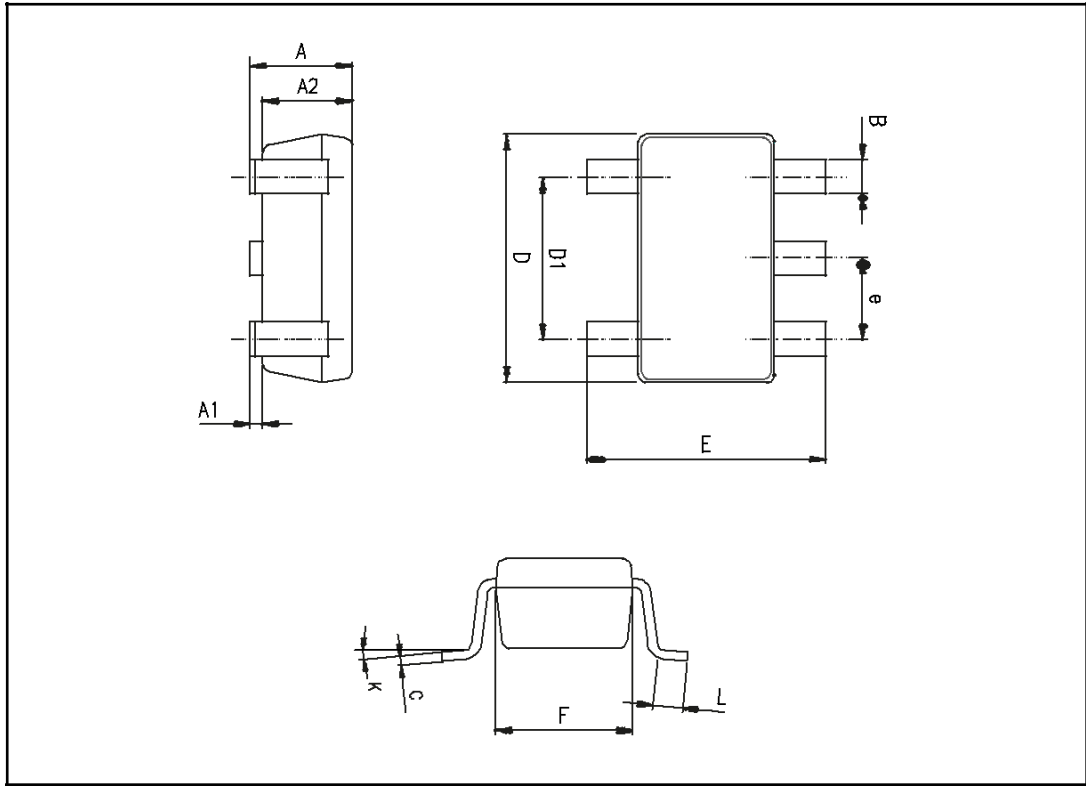


Table 5: SOT23-5 mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.90	1.20	1.45	0.035	0.047	0.057
A1			0.15			0.006
A2	0.90	1.05	1.30	0.035	0.041	0.051
B	0.35	0.40	0.50	0.014	0.016	0.020
C	0.09	0.15	0.20	0.004	0.006	0.008
D	2.80	2.90	3.00	0.110	0.114	0.118
D1		1.90			0.075	
e		0.95			0.037	
E	2.60	2.80	3.00	0.102	0.110	0.118
F	1.50	1.60	1.75	0.059	0.063	0.069
L	0.10	0.35	0.60	0.004	0.014	0.024
K	0 degrees		10 degrees	0 degrees		10 degrees

3.2 SO8 package information

Figure 14: SO8 package outline

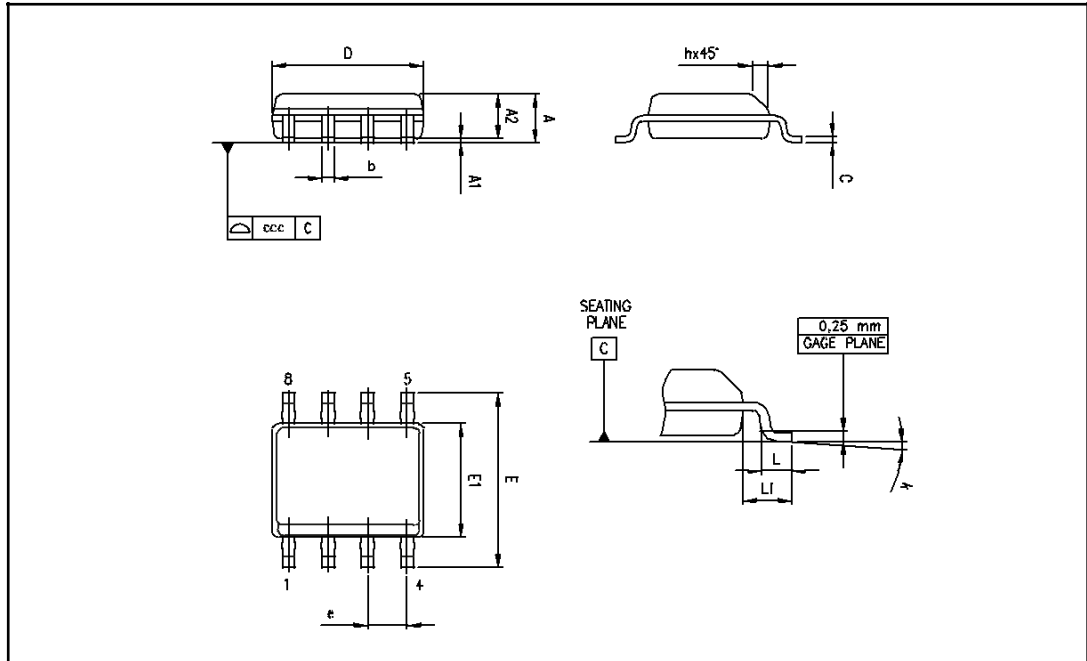


Table 6: SO8 mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.75			0.069
A1	0.10		0.25	0.004		0.010
A2	1.25			0.049		
b	0.28		0.48	0.011		0.019
c	0.17		0.23	0.007		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e		1.27			0.050	
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
L1		1.04			0.040	
k	1°		8°	1°		8°
ccc			0.10			0.004

3.3 TSSOP8 package information

Figure 15: TSSOP8 package outline

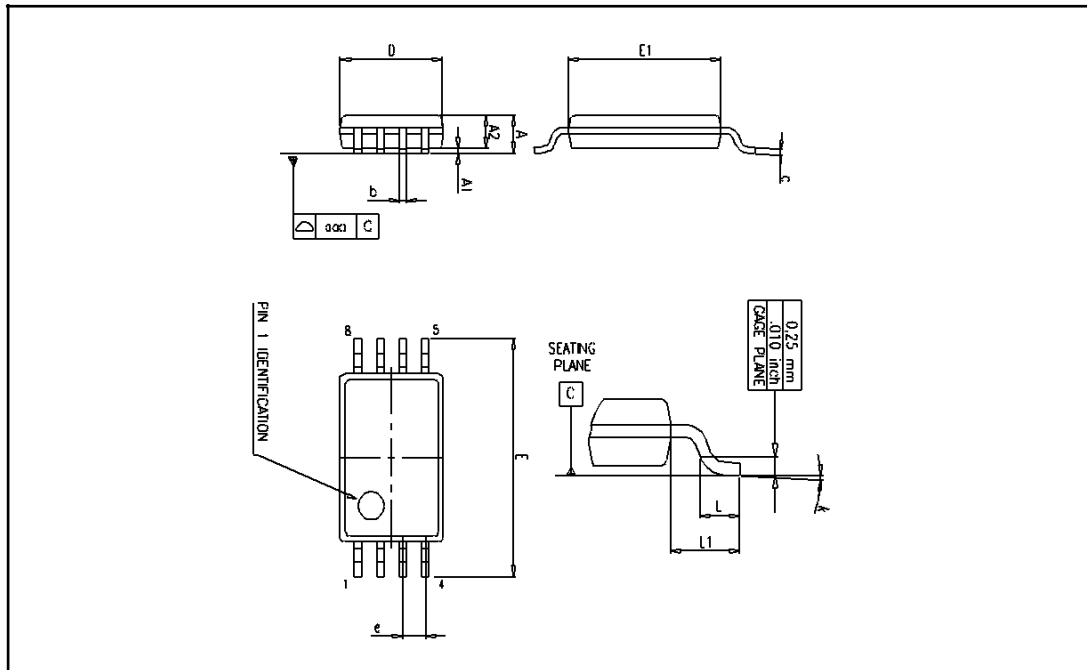


Table 7: TSSOP8 mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.2			0.047
A1	0.05		0.15	0.002		0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.008
D	2.90	3.00	3.10	0.114	0.118	0.122
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.177
e		0.65			0.0256	
k	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030
L1		1			0.039	
aaa		0.1			0.004	

3.4 SO14 package information

Figure 16: SO14 package outline

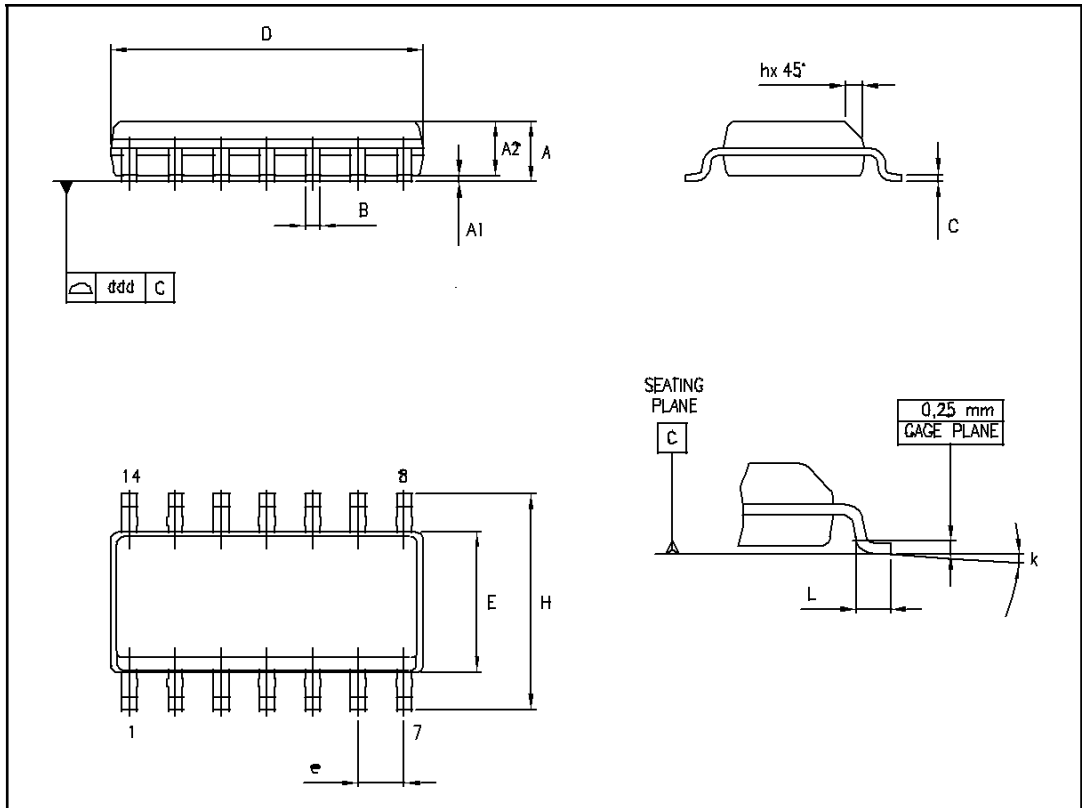


Table 8: SO14 mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.35		1.75	0.05		0.068
A1	0.10		0.25	0.004		0.009
A2	1.10		1.65	0.04		0.06
B	0.33		0.51	0.01		0.02
C	0.19		0.25	0.007		0.009
D	8.55		8.75	0.33		0.34
E	3.80		4.0	0.15		0.15
e		1.27			0.05	
H	5.80		6.20	0.22		0.24
h	0.25		0.50	0.009		0.02
L	0.40		1.27	0.015		0.05
k	8° (max)					
ddd			0.10			0.004

3.5 TSSOP14 package information

Figure 17: TSSOP14 package outline

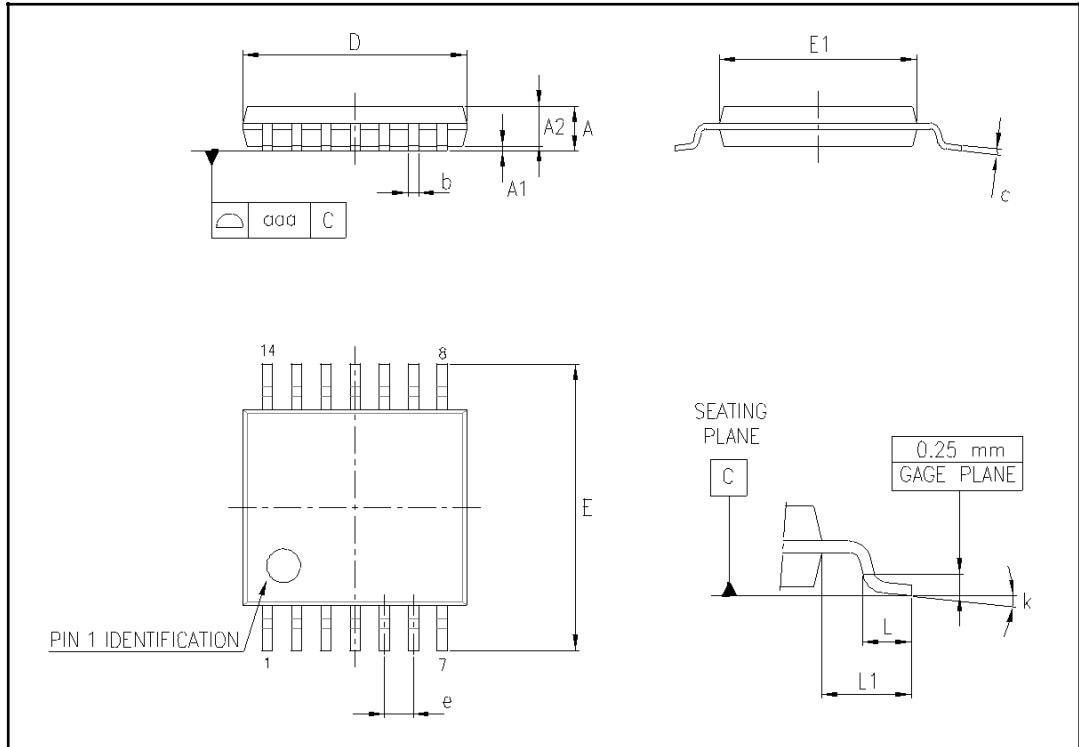


Table 9: TSSOP14 mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.20			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.0089
D	4.90	5.00	5.10	0.193	0.197	0.201
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.176
e		0.65			0.0256	
L	0.45	0.60	0.75	0.018	0.024	0.030
L1		1.00			0.039	
k	0°		8°	0°		8°
aaa			0.10			0.004