

Photocouplers Photorelay

# **TLP3122A**

#### 1. Applications

- · Measuring Instruments
- · Security Systems
- ATE (Automatic Test Equipment)
- Factory Automation (FA)
- Battery Management System (BMS) (Non-Automotive)
- · Programmable Logic Controllers (PLCs)
- · Mechanical relay replacements

#### 2. General

The Toshiba TLP3122A consists of a gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in a 4-pin SO6 package. This photorelay has higher output current rating than phototransistor-type photocoupler; hence, it is suitable for use as On/Off control for high current.

#### 3. Features

(1) Halogen-free

For details, see "Devices in Halogen-Free Resin Packages" at the end of this datasheet.

- (2) Operating temperature range: 110°C(max)
- (3) Normally opened (1-Form-A)
- (4) OFF-state output terminal voltage: 60 V (min)
- (5) Trigger LED current: 3 mA (max)
- (6) ON-state current: 1.4A (max)
- (7) ON-state resistance:  $0.25 \Omega$  (max)
- (8) Isolation voltage: 3750 Vrms (min)
- (9) Safety standards

UL-approved: UL1577, File No.E67349

cUL-approved: CSA Component Acceptance Service No.5A File No.E67349

UL-approved: UL508, File No.E499232 (Note 1)

VDE-approved: EN60747-5-5 (Note 2)

Note 1: Please refer Absolute Maximum Ratings (UL-approved UL508) for UL508 products.

Note 2: When a VDE approved type is needed, please designate the Option (V4).

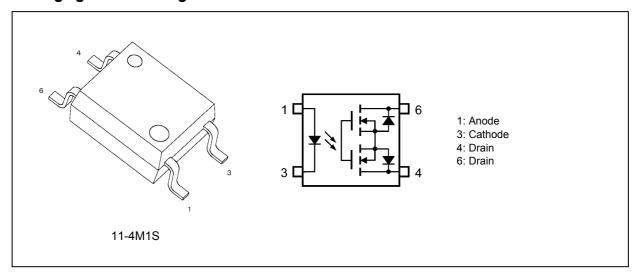
Table 3.1 Mechanical Parameters

Characteristics	TLP3122A	Unit
Creepage distances	5.0 (min)	mm
Clearance distances	5.0 (min)	
Internal isolation thickness	0.2 (min)	

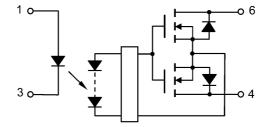
Start of commercial production



# 4. Packaging and Pin Assignment



# 5. Internal Circuit





# 6. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

	Characteristics		Symbol	Note	Rating	Unit
LED	Input forward current		I <sub>F</sub>		50	mA
	Input forward current derating	(T <sub>a</sub> ≥ 25 °C)	$\Delta I_F / \Delta T_a$		-0.5	mA/°C
	Input forward current (pulsed) (	100 μs pulse, 100 pps)	I <sub>FP</sub>		1	Α
	Input reverse voltage		V <sub>R</sub>		6	V
	Input power dissipation		P <sub>D</sub>		50	mW
	Input power dissipation derating	(T <sub>a</sub> ≥ 25 °C)	$\Delta P_D/\Delta T_a$		-0.5	mW/°C
	Junction temperature		Tj		125	°C
Detector	OFF-state output terminal voltage		V <sub>OFF</sub>		60	V
	ON-state current		I <sub>ON</sub>		1.4	Α
	ON-state current derating	(T <sub>a</sub> ≥ 25 °C)	$\Delta I_{ON}/\Delta T_a$		-14.0	mA/°C
	ON-state current (pulsed) (t	= 100 ms, duty = 1/10)	I <sub>ONP</sub>		4.2	Α
	Output power dissipation		Po		400	mW
	Output power dissipation derating	(T <sub>a</sub> ≥ 25 °C)	$\Delta P_{O}/\Delta T_{a}$		-4.0	mW/°C
	Junction temperature		Tj		125	°C
Common	Storage temperature		T <sub>stg</sub>		-55 to 125	°C
	Operating temperature		T <sub>opr</sub>	_	-40 to 110	°C
	Lead soldering temperature	(10 s)	T <sub>sol</sub>		260	°C
	Isolation voltage (A	AC, 60 s, R.H. ≤ 60 %)	BV <sub>S</sub>	(Note 1)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

## 7. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Supply voltage	$V_{DD}$		1	_	48	V
Input forward current	I <sub>F</sub>		5	7.5	25	mA
ON-state current	I <sub>ON</sub>		_	_	1.4	Α
Operating temperature	T <sub>opr</sub>		-20		100	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this datasheet should also be considered.



# 8. Absolute Maximum Ratings (UL-approved: UL508) (Note) (Unless otherwise specified, $T_a = 25 \, ^{\circ}\text{C}$ )

	Characteristics		Symbol	Note	Rating	Unit
LED	Input forward current		I <sub>F</sub>		50	mA
	Input forward current derating	(T <sub>a</sub> ≥ 25 °C)	$\Delta I_F/\Delta T_a$		-0.5	mA/°C
	Input forward current (pulsed) (1	00 μs pulse, 100 pps)	I <sub>FP</sub>		1	Α
	Input reverse voltage		V <sub>R</sub>		6	V
	Input power dissipation		$P_{D}$		50	mW
	Input power dissipation derating	$(T_a \ge 25  ^{\circ}C)$	$\Delta P_D/\Delta T_a$		-0.5	mW/°C
	Junction temperature		Tj		105	°C
Detector	OFF-state output terminal voltage		$V_{OFF}$		60	V
	ON-state current		I <sub>ON</sub>		1.4	Α
	ON-state current derating	$(T_a \ge 25  ^{\circ}C)$	$\Delta I_{ON}/\Delta T_a$		-14.0	mA/°C
	ON-state current (pulsed) (t =	= 100 ms, duty = 1/10)	I <sub>ONP</sub>		4.2	Α
	Output power dissipation		Po		400	mW
	Output power dissipation derating	(T <sub>a</sub> ≥ 25 °C)	$\Delta P_{O}/\Delta T_{a}$		-4.0	mW/°C
	Junction temperature		Tj		105	°C
Common	Storage temperature		T <sub>stg</sub>		-55 to 125	°C
	Case temperature		T <sub>c</sub>		105	°C
	Operating temperature		T <sub>opr</sub>		-40 to 85	°C
	Lead soldering temperature	(10 s)	T <sub>sol</sub>		260	°C
	Isolation voltage (A	AC, 60 s, R.H. ≤ 60 %)	BV <sub>S</sub>	(Note 1)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

#### 9. Recommended Operating Conditions (UL-approved: UL508) (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Supply voltage	$V_{DD}$			_	48	V
Input forward current	I <sub>F</sub>	(Note 1)	5	7.5	32.5	mA
ON-state current	I <sub>ON</sub>	(Note 1)	_	_	0.9	Α
Operating temperature	T <sub>opr</sub>		-20	_	85	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.

Note 1: The above recommended operating conditions are at  $T_a = 60 \,^{\circ}\text{C}$ . However, within the derating range of the characteristic curves of " $I_F - T_a$ ", " $I_{ON} - T_a$ ", it can be used up to 85 °C.



# 10. Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V <sub>F</sub>		I <sub>F</sub> = 10 mA	1.1	1.27	1.4	V
	Input reverse current	I <sub>R</sub>		V <sub>R</sub> = 5 V			10	μА
	Input capacitance	Ct		V = 0 V, f = 1 MHz		30		pF
Detector	OFF-state current	I <sub>OFF</sub>		V <sub>OFF</sub> = 60 V	_	0.002	1	μА
	Output capacitance	C <sub>OFF</sub>		V = 0 V, f = 1 MHz	_	100		pF

# 11. Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I <sub>FT</sub>		I <sub>ON</sub> = 1.4 A	_	1	3	mA
Return LED current	I <sub>FC</sub>		I <sub>OFF</sub> = 100 μA	0.1	_	_	mA
ON-state resistance	R <sub>ON</sub>		I <sub>ON</sub> = 1.4 A, I <sub>F</sub> = 5 mA, t < 1 s	_	0.13	0.25	Ω

# 12. Isolation Characteristics (Unless otherwise specified, T<sub>a</sub> = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	Cs	(Note 1)	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8		pF
Isolation resistance	R <sub>S</sub>	(Note 1)	$V_S$ = 500 V, R.H. $\leq$ 60 %	5 × 10 <sup>10</sup>	1014		Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	3750	_	_	Vrms

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

# 13. Switching Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Turn-on time	t <sub>ON</sub>		See Fig. 13.1	_	2	3	ms
Turn-off time	t <sub>OFF</sub>		$R_L = 200 \Omega, V_{DD} = 20 V, I_F = 5 mA$	_	0.1	1	

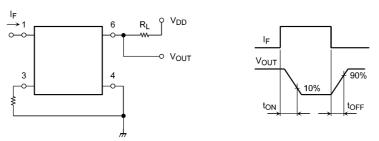


Fig. 13.1 Switching Time Test Circuit and Waveform



#### 14. Characteristics Curves and Circuit Connections

## 14.1. Characteristics Curves (Note)

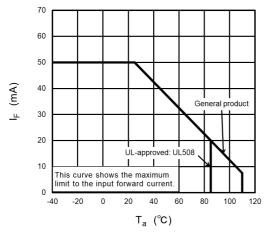
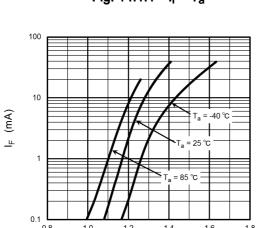


Fig. 14.1.1 I<sub>F</sub> - T<sub>a</sub>



 $V_F$  (V) Fig. 14.1.3 I<sub>F</sub> - V<sub>F</sub>

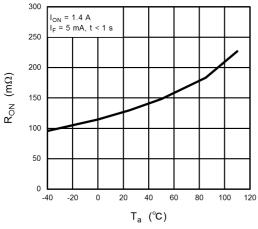


Fig. 14.1.5 R<sub>ON</sub> - T<sub>a</sub>

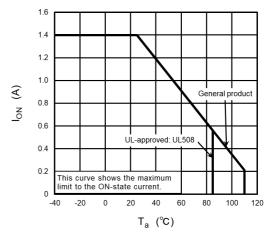


Fig. 14.1.2 I<sub>ON</sub> - T<sub>a</sub>

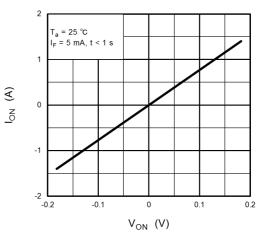


Fig. 14.1.4 I<sub>ON</sub> - V<sub>ON</sub>

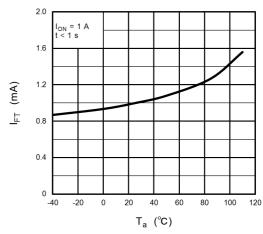


Fig. 14.1.6 I<sub>FT</sub> - T<sub>a</sub>



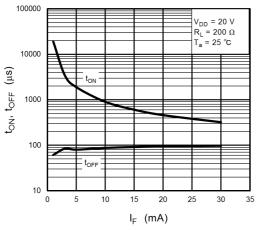


Fig. 14.1.7 toN, toFF - IF

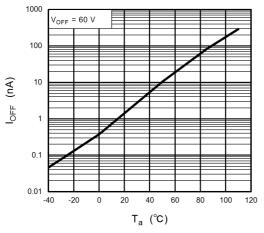


Fig. 14.1.9 I<sub>OFF</sub> - T<sub>a</sub>

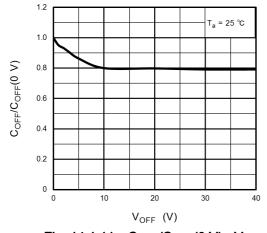


Fig. 14.1.11 C<sub>OFF</sub>/C<sub>OFF</sub>(0 V) - V<sub>OFF</sub>

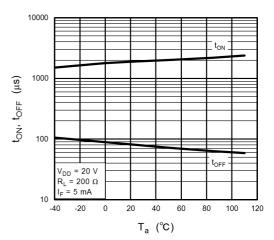


Fig. 14.1.8 t<sub>ON</sub>, t<sub>OFF</sub> - T<sub>a</sub>

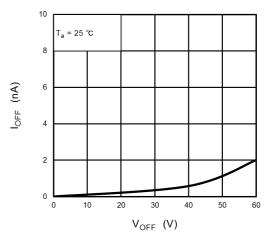


Fig. 14.1.10 | IOFF - VOFF

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



### 15. Soldering and Storage

### 15.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

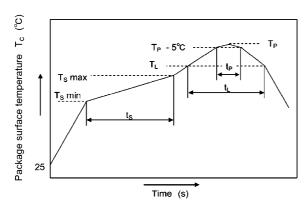
· When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	s
Ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )			3	°C/s
Liquidus temperature	TL	2	17	°C
Time above T <sub>L</sub>	t∟	60	150	s
Peak temperature	T <sub>P</sub>		260	°C
Time during which $T_c$ is between ( $T_P - 5$ ) and $T_P$	t <sub>P</sub>		30	s
Ramp-down rate (T <sub>P</sub> to T <sub>L</sub> )			6	°C/s

An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

· When using soldering flow

Preheat the device at a temperature of 150  $^{\circ}\text{C}$  (package surface temperature) for 60 to 120 seconds.

Mounting condition of 260  $^{\circ}$ C within 10 seconds is recommended.

Flow soldering must be performed once.

· When using soldering Iron

Complete soldering within 10 seconds for lead temperature not exceeding 260  $^{\circ}$ C or within 3 seconds not exceeding 350  $^{\circ}$ C

Heating by soldering iron must be done only once per lead.

#### 15.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- · When restoring devices after removal from their packing, use anti-static containers.
- · Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.



## 16. Embossed-Tape Packing (TP) Specification for Mini-Flat Photorelays

#### 16.1. Applicable Package

Package Name	Product Type
4-pin SO6	Photorelay

#### 16.2. Product Naming Conventions

Type of package used for shipment is denoted by a symbol suffix after a part number. The method of classification is as below.

Example) TLP3122A(TPL,E

Part number: TLP3122A

Tape type: TPL

[[G]]/RoHS COMPATIBLE: E (Note 1)

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

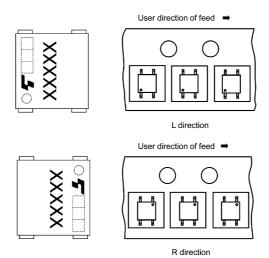
RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 16.3. Tape Dimensions Specification

Tape Type	Division	Packing Amount (A unit per reel)
TPL	L direction	3000
TPR	R direction	3000

#### 16.3.1. Orientation of Device in Relation to Direction of Feed

Device orientation in the carrier cavities as shown in the following figure.



**Device Orientation** 

#### 16.3.2. Empty Cavities

Characteristics	Criterion	Remarks
Occurrences of 2 or more successive empty cavities	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty cavity	6 devices (max) per reel	Not including leader and trailer



### 16.3.3. Tape Leader and Trailer

The start of the tape has 50 or more empty cavities. The end of the tape has 50 or more empty cavities and two empty turns only for a cover tape.

## 16.3.4. Tape Dimensions

Tape material: Plastic (for protection against static electricity)

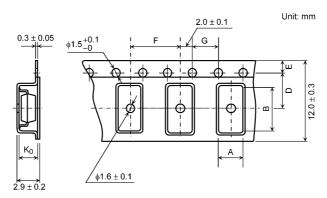


Table Tape Dimensions (unit: mm, tolerance: ±0.1)

Symbol	Dimension	Remark
А	4.0	_
В	7.6	_
D	5.5	Center line of embossed cavity and sprocket hole
E	1.75	Distance between tape edge and sprocket hole center
F	8.0	Cumulative error +0.1/-0.3 per 10 empty cavities holes
G	4.0	Cumulative error +0.1/-0.3 per 10 sprocket holes
K <sub>0</sub>	2.6	Internal space



## 16.3.5. Reel Specification

Material: Plastic (for protection against static electricity)

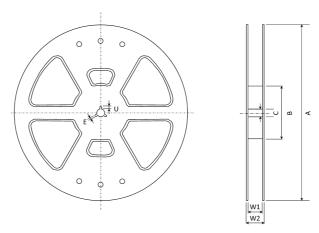
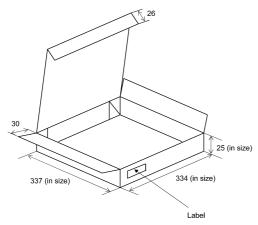


Table Reel Dimensions (unit: mm)

Symbol	Dimension	
Α	$\phi 330 \pm 2.0$	
В	ф80 ± 1.0	
С	$\varphi 13 \pm 0.5$	
E	$2.0\pm0.5$	
U	4.0 ± 0.5	
W1	W1 13.5 ± 0.5	
W2	17.5 ± 1.0	

# 16.4. Packing (Note)



1 reel/carton (unit: mm)

Note: Taping reel diameter: \$\phi 330 mm

#### 16.5. Label Format

- (1) Carton: The label provides the part number, quantity, lot number, the Toshiba logo, etc.
- (2) Reel: The label provides the part number, the taping name, quantity, lot number, etc.



#### 16.6. Ordering Information

When placing an order, please specify the part number, tape type and quantity as shown in the following example.

Example) TLP3122A(TPL,E 3000 pcs

Part number: TLP3122A

Tape type: TPL

[[G]]/RoHS COMPATIBLE: E (Note 1)

Quantity (must be a multiple of 3000): 3000 pcs

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

#### 17. Devices in Halogen-Free Resin Packages

· This product is Halogen-Free

Toshiba Electronic Devices & Storage Corporation ("Toshiba") defines a "Halogen-Free resin semiconductor product" as a semiconductor product in which:

- (1) the encapsulating resins do not contain any of the following elements: bromine (Br), chlorine (Cl) and antimony (Sb), respectively, in an amount exceeding 0.09 weight percent, and do not contain chlorine and bromine in an aggregate amount exceeding 0.15 weight percent of the encapsulating resins, and/or
- (2) the resin portion(s) in printed circuit boards do not contain any of the following elements: bromine, chlorine and antimony, respectively, in an amount exceeding 0.09 weight percent, and do not contain chlorine and bromine in an aggregate amount exceeding 0.15 weight percent of the each resin portion(s) in printed circuit boards.

For avoidance of doubt, "Halogen-Free resin semiconductor product" does not mean, and Toshiba does not make any warranty of any kind, that said semiconductor product is entirely free of antimony or of any of the following elements of the halogen family: bromine, chlorine, iodine (I), fluorine (F) and astatine (At).

In addition, a Halogen-Free resin semiconductor product may contain antimony and/or any of the elements of the halogen family as mentioned in the above paragraph in one or more portion(s) of the semiconductor product other than the encapsulating resins and the resin portion(s) in printed circuit boards.

The information provided herein is accurate as of the date that it was provided, to the best of the knowledge and belief of the Toshiba Electronic Devices & Storage Corporation ("Toshiba"), Toshiba bases such knowledge and belief on information provided by third parties, and Toshiba makes no representation or warranty as to the accuracy of such third party information. Toshiba has taken and will continue to take, reasonable steps to provide accurate information to its customers, but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals.



# 18. Ordering Information (Example of Item Name)

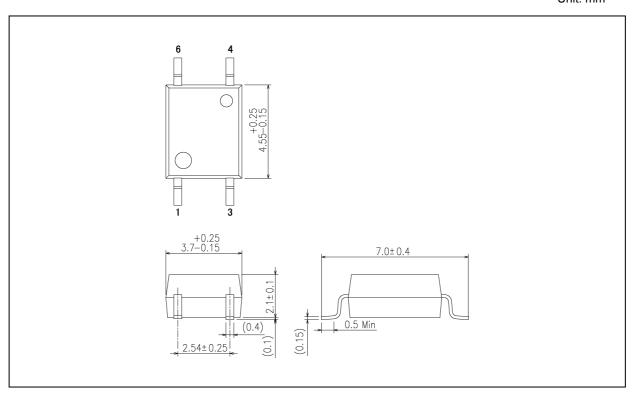
Item Name	Packaging (Note 1)	Packing (MOQ)
TLP3122A(E	SMD	Magazine (125 pcs)
TLP3122A(TPL,E	SMD	Tape and reel (3000 pcs)
TLP3122A(TPR,E	SMD	Tape and reel (3000 pcs)
TLP3122A(V4,E	SMD	Magazine (125 pcs)
TLP3122A(V4TPL,E	SMD	Tape and reel (3000 pcs)
TLP3122A(V4TPR,E	SMD	Tape and reel (3000 pcs)

Note 1: SMD: Surface Mount Device



# **Package Dimensions**

Unit: mm



Weight: 0.1 g (typ.)

	Package Name(s)
TOSHIBA: 11-4M1S	



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