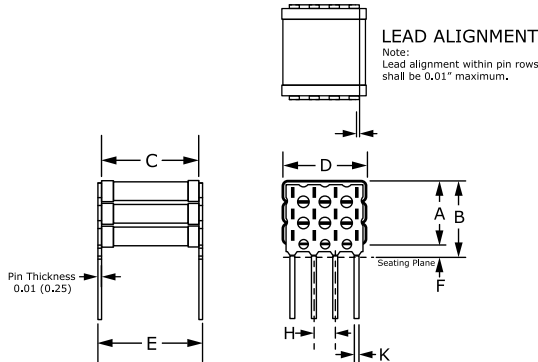


KPS-MCC Indust COG HT200C, Ceramic, 1 uF, 10%, 200 VDC, COG



Dimensions	
D	10.16mm +/-0.635mm
L	6.35mm MIN
H	2.54mm NOM
F	1.397mm +/-0.25mm
A	8.13mm MAX
B	9.908mm MAX
C	10.16mm +/-0.635mm
E	11.18mm MAX
K	0.5mm NOM

Packaging Specifications	
Packaging:	Waffle, Box
Packaging Quantity:	50

General Information	
Series:	KPS-MCC Indust COG HT200C
Style:	Leaded Stacked Chip
Description:	Low ESR, Stacked Ceramic Chips
Features:	200C, Low ESR, High Thermal Stability, Bulk Capacitance
RoHS:	Yes
SCIP Number:	297427bb-2a48-4853-b594-641304a2cc24
Termination:	Silver
Lead:	Straight Leads
AEC-Q200:	No
Notes:	Number of chips in this stack: 3.

Specifications	
Capacitance:	1 uF
Capacitance Tolerance:	10%
Voltage DC:	200 VDC
Dielectric Withstanding Voltage:	500 VDC
Temperature Range:	-55/+200°C
Temperature Coefficient:	COG
Dissipation Factor:	0.1% 1 kHz 25C
Aging Rate:	0% Loss/Decade Hour
Insulation Resistance:	1 GOhms

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