# Quad TTL-to-MECL Translator With TTL Strobe Input

#### Description

The MC10H124 is a quad translator for interfacing data and control signals between a saturated logic section and the MECL section of digital systems. The 10H part is a functional/pinout duplication of the standard MECL  $10K^{TM}$  family part, with 100% improvement in propagation delay, and no increase in power-supply current.

#### **Features**

- Propagation Delay, 1.5 ns Typical
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K Compatible
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

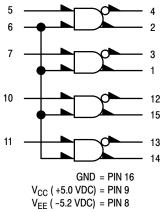


Figure 1. Logic Diagram



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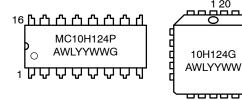




PDIP-16 P SUFFIX CASE 648-08

PLLC-20 FN SUFFIX CASE 775-02

#### **MARKING DIAGRAMS\***



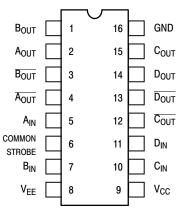
A = Assembly Location
WL, L = Wafer Lot
YY, Y = Year
WW, W = Work Week
G or = Pb-Free Package

#### **ORDERING INFORMATION**

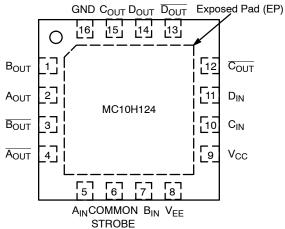
Device	Package	Shipping <sup>†</sup>
MC10H124FNG	PLCC-20 (Pb-Free)	46 Units/Tube
MC10H124FNR2G	PLCC-20 (Pb-Free)	500/Tape & Reel
MC10H124PG	PDIP-16 (Pb-Free)	25 Units/Tube

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional marking information, refer to Application Note <u>AND8002/D</u>.



Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see Table 1.



Pin assignment for QFN16 Package.

Figure 2. Pin Assignment

# Table 1. DIP CONVERSION TABLE 16-Pin DIL to 20-Pin PLCC

16 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
20 PIN PLCC	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20

**Table 2. MAXIMUM RATINGS** 

Symbol	Characteristic	Rating	Unit
V <sub>EE</sub>	Power Supply (V <sub>CC</sub> = 5.0 V)	-8.0 to 0	Vdc
V <sub>CC</sub>	Power Supply (V <sub>EE</sub> = -5.2 V)	0 to +7.0	Vdc
VI	Input Voltage (V <sub>CC</sub> = 5.0 V) TTL	0 to V <sub>CC</sub>	Vdc
l <sub>out</sub>	Output Current Continuous Surge	50 100	mA
T <sub>A</sub>	Operating Temperature Range	0 to +75	°C
T <sub>stg</sub>	Storage Temperature Range Plastic Ceramic	-55 to +150 -55 to +165	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Table 3. ELECTRICAL CHARACTERISTICS ( $V_{EE}$  = -5.2 V ±5%,  $V_{CC}$  = 5.0 V ± 5.0%)

	, ==				•			
		0° 25°		7	75°			
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
Ι <sub>Ε</sub>	Negative Power Supply Drain Current	-	72	-	66	_	72	mA
I <sub>CCH</sub>	Positive Power Supply Drain Current	- -	16 25	- -	16 25	- -	18 25	mA
I <sub>R</sub>	Reverse Current Pin 6 Pin 7	- -	200 50	- -	200 50	- -	200 50	μΑ
l <sub>F</sub>	Forward Current Pin 6 Pin 7	- -	-12.8 -3.2	- -	-12.8 -3.2	- -	-12.8 -3.2	mA
V <sub>(BR)in</sub>	Input Breakdown Voltage	5.5	-	5.5	-	5.5	-	Vdc
VI	Input Clamp Voltage	-	-1.5	-	-1.5	-	-1.5	Vdc
V <sub>OH</sub>	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V <sub>OL</sub>	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V <sub>IH</sub>	High Input Voltage	2.0	-	2.0	-	2.0	-	Vdc
V <sub>IL</sub>	Low Input Voltage	-	0.8	-	0.8	-	0.8	Vdc

<sup>1.</sup> Each MECL 10H™ series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50 Ω resistor to −2.0 V.

**Table 4. AC CHARACTERISTICS** 

		<b>0</b> °		25°		75°		
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
t <sub>pd</sub>	Propagation Delay	0.55	2.5	0.55	2.65	0.85	3.1	ns
t <sub>r</sub>	Rise Time	0.5	1.5	0.5	1.6	0.5	1.7	ns
t <sub>f</sub>	Fall Time	0.5	1.5	0.5	1.6	0.5	1.7	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

### **APPLICATIONS INFORMATION**

The MC10H124 has TTL-compatible inputs and MECL complementary open-emitter outputs that allow use as an inverting/non-inverting translator or as a differential line driver. When the common strobe input is at the low-logic level, it forces all true outputs to a MECL low-logic state and all inverting outputs to a MECL high-logic state.

An advantage of this device is that TTL-level information can be transmitted differentially, via balanced twisted pair lines, to MECL equipment, where the signal can be received by the MC10H115 or MC10H116 differential line receivers. The power supply requirements are ground, +5.0 V, and -5.2 V.

#### **Resource Reference of Application Notes**

AN1405/D - ECL Clock Distribution Techniques

AN1406/D - Designing with PECL (ECL at +5.0 V)

AN1503/D - ECLinPS™ I/O SPICE Modeling Kit

AN1504/D - Metastability and the ECLinPS Family

AN1568/D - Interfacing Between LVDS and ECL

AN1672/D - The ECL Translator Guide

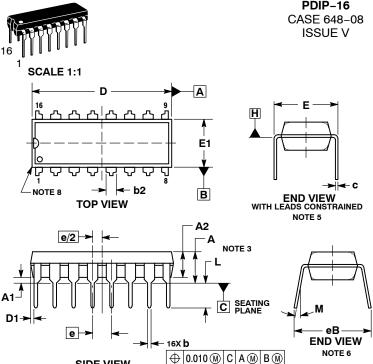
AND8001/D - Odd Number Counters Design

AND8002/D - Marking and Date Codes

AND8020/D - Termination of ECL Logic Devices

AND8066/D - Interfacing with ECLinPS

AND8090/D - AC Characteristics of ECL Devices



PDIP-16

**DATE 22 APR 2015** 

#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

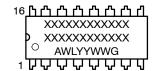
- DIMENSIONING AND TOLERANGING FER ASME 114-3M, 1994
  CONTROLLING DIMENSION: INCHES.

  DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.

  DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH
  OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE
  NOT TO EXCEED 0.10 INCH.
- DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR
- DIMENSION eB IS MEASURED AT THE LEAD TIPS WITH THE
- DIMENSION 8B IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
  DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE LEADS, WHERE THE LEADS EXIT THE BODY.
  PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).

	INC	HES	MILLIM	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α		0.210		5.33	
A1	0.015		0.38		
A2	0.115	0.195	2.92	4.95	
b	0.014	0.022	0.35	0.56	
b2	0.060	TYP	1.52 TYP		
С	0.008	0.014	0.20	0.36	
D	0.735	0.775	18.67	19.69	
D1	0.005		0.13		
E	0.300	0.325	7.62	8.26	
E1	0.240	0.280	6.10	7.11	
е	0.100 BSC		2.54	BSC	
eВ		0.430		10.92	
L	0.115	0.150	2.92	3.81	
М		10°		10°	

#### **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code

= Assembly Location

WL = Wafer Lot YY = Year

WW = Work Week

G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1	:	STYLE 2	:
PIN 1.	CATHODE	PIN 1.	COMMON DRAIN
2.	CATHODE	2.	COMMON DRAIN
3.	CATHODE	3.	COMMON DRAIN
4.	CATHODE	4.	COMMON DRAIN
5.	CATHODE	5.	COMMON DRAIN
6.	CATHODE	6.	COMMON DRAIN
7.	CATHODE	7.	COMMON DRAIN
8.	CATHODE	8.	COMMON DRAIN
9.	ANODE	9.	GATE
10.	ANODE	10.	SOURCE
11.	ANODE	11.	GATE
12.	ANODE	12.	SOURCE
13.	ANODE	13.	GATE
14.	ANODE	14.	SOURCE
15.	ANODE	15.	GATE
16.	ANODE	16.	SOURCE

**SIDE VIEW** 

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# **MECHANICAL CASE OUTLINE**

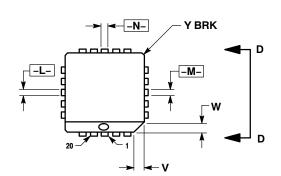


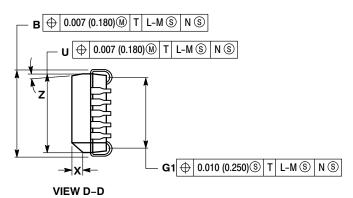


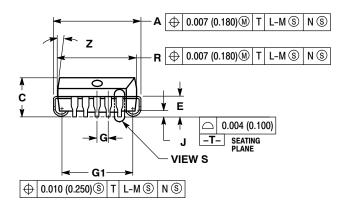
### 20 LEAD PLCC CASE 775-02 ISSUE G

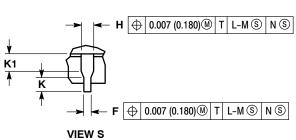
**DATE 06 APR 2021** 











#### NOTES

- DIMENSIONS AND TOLERANCING PER ANSI Y14.5M,
- 2. DIMENSIONS IN INCHES.
- DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD

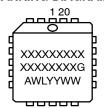
- PARTING LINE.
  DIMENSION GT, TRUE POSITION TO BE MEASURED AT DATUM -T -, SEATING PLANE.
  DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH.
  ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.
  DIMENSIONS IN THE PACKAGE TOP MAY BE SMALLER
  THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300).
  DIMENSIONS R AND U ARE DETERMINED AT THE
  OUTERMOST EXTREMES OF THE PLASTIC BODY
  EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE
  BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.
- DIMENSION H DOES NOT INCLUDE DAMBAR
  PROTRUSION OR INTRUSION. THE DAMBAR
  PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

l .	INC	пЕЭ	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.385	0.395	9.78	10.03	
В	0.385	0.395	9.78	10.03	
С	0.165	0.180	4.20	4.57	
E	0.090	0.110	2.29	2.79	
F	0.013	0.021	0.33	0.53	
G	0.050	BSC	1.27	BSC	
Н	0.026	0.032	0.66	0.81	
J	0.020		0.51		
K	0.025		0.64		
R	0.350	0.356	8.89	9.04	
U	0.350	0.356	8.89	9.04	
V	0.042	0.048	1.07	1.21	
W	0.042	0.048	1.07	1.21	
X	0.042	0.056	1.07	1.42	
Υ		0.020		0.50	
Z	2°	10 °	2 °	10 °	
G1	0.310	0.330	7.88	8.38	
K1	0.040		1.02		

INCHES

MILLIMETERS

#### **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code = Assembly Location

WL = Wafer Lot = Year YY WW = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " = ", may or may not be present.

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