



MICROPOWER, ULTRA-SENSITIVE HALL EFFECT SWITCH

#### Description

AH1801 is a Micropower, Ultra-sensitive Hall Effect Switch, which is with two Hall effect plates and a output driver, mainly designed for battery–operation, hand-held equipment (such as Cellular and Cordless Phone, PDA). The total operation power is down to  $24\mu$ W in the 3V supply.

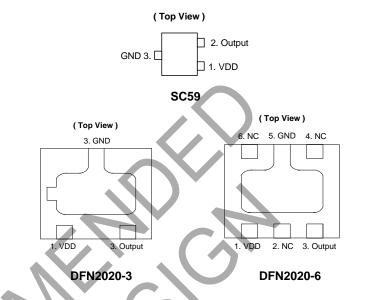
Either north or south pole of sufficient strength will turn the output off. The output will be turned on under no magnetic field.

While the magnetic flux density (**B**) is larger than operate point (**Bop**), the output will be turned off, the output is held until **B** is lower than release point (**Brp**), then turned on.

#### **Features**

- Micropower Operation
- Operation with North or South Pole
- 2.5V to 5.5V Battery Operation
- Inverted Output-on without Magnet Present
- Chopper Stabilized
  - Superior Temperature Stability
  - Extremely Low Switch-Point Drift
  - Insensitive to Physical Stress
- Good RF Noise Immunity
- -40°C to +85°C Operating Temperature
- Low Profile 3 Pin SC59 (Commonly Known as SOT23 in Asia)
- and DFN2020-3, DFN2020-6 Packages • ESD (HBM) > 5KV for DFN2020-3 and DFN2020-6
- ESD (HBM) > 5KV for DFN2020-3 and DFN2020-6 > 6KV for SC59
- SC59, DFN2020-3 and DFN2020-6: Available in "Green" Molding Compound (No Br, Sb)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

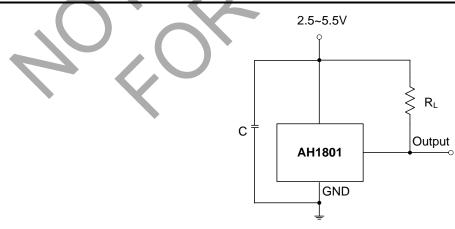
#### **Pin Assignments**



### Applications

- Cellular Phone
- PDA
- Cordless Phone
- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

### **Typical Applications Circuit**



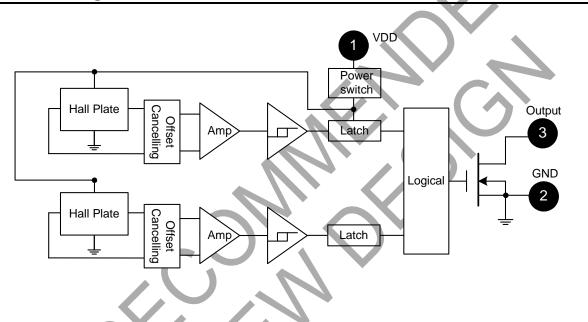
Notes: 4. C is for power stabilization and to strengthen the noise immunity, the recommended capacitance is 10nF to 100nF.  $R_L$  is the pull-up resistor, the recommended resistance is 10K $\Omega$  to 100K $\Omega$ .



### **Pin Descriptions**

Pin Name	P/I/O	Description	
VDD	P/I	Power Supply Input	
GND	P/I	Ground	
Output	0	Output Pin	
NC		No Connected	

### Functional Block Diagram



### Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit	
V <sub>DD</sub>	Supply Voltage		7	V
В	Magnetic Flux Density		Unlimited	b
Τs	Storage Temperature Range		-65 to +150	°C
		SC59		mW
PD	Package Power Dissipation	DFN2020-3	230	
	DFN2020-6			
ТJ	Maximum Junction Temperature		+150	°C

# Recommended Operating Conditions (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter Conditions		Rating	Unit
V <sub>DD</sub>	Supply Voltage	Operating	2.5 to 5.5	V
TA	Operating Temperature Range	Operating	-40 to +85	°C

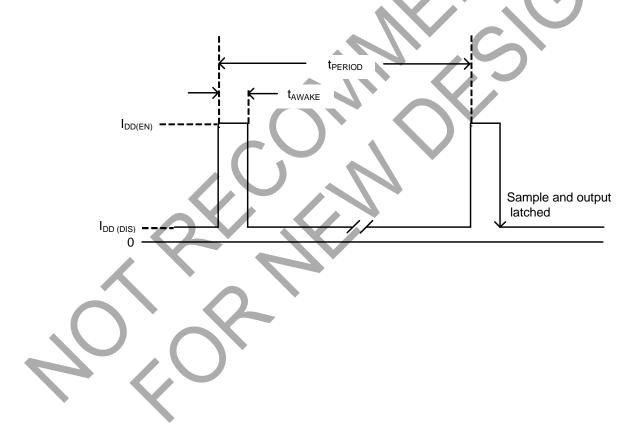


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# **Electrical Characteristics** ( $@T_A = +25^{\circ}C$ , $V_{DD} = 3V$ , unless otherwise specified.)

Symbol	Parameter Conditions		Min	Тур	Max	Unit	
Vout	Output On Voltage	I <sub>OUT</sub> = 1mA	-	0.1	0.3	V	
IOFF	Output Leakage Current	$V_{OUT} = 5.5V$ , Output off	-	<0.1	1	μA	
		Chip enable, $T_A = +25^{\circ}C$ , $V_{DD} = 3V$	-	3	6	mA	
I <sub>DD(EN)</sub>		Chip enable, $T_A = -40$ to $+85^{\circ}C$ , V <sub>DD</sub> = 2.5V to 5.5V	-	3	9	mA	
		Chip disable, $T_A = +25^{\circ}C$ , $V_{DD} = 3V$	-	5	10	μA	
I <sub>DD(DIS)</sub>	IDD(DIS) Supply Current   IDD(AVG) I	Chip disable, $T_A = -40$ to $+85^{\circ}$ C, V <sub>DD</sub> = 2.5V to 5.5V	-	5	18	μA	
		Average supply current, T <sub>A</sub> = +25°C, V <sub>DD</sub> = 3V	-	8	16	μΑ	
IDD(AVG)		Average supply current, T <sub>A</sub> = -40 to +85°C, $V_{DD}$ = 2.5V to 5.5V	-	8	27	μA	
t <sub>AWAKE</sub>	Awake Time	(Note 5)	-	75	150	μs	
<b>t</b> PERIOD	Period	(Note 5)	-	75	150	ms	
D.C.	Duty Cycle		-	0.1	-	%	

Notes: 5. When power is initially on, the operating V<sub>DD</sub> (2.5V to 5.5V) must be applied to be guaranteed for the output sampling. The output state is valid after the second operating phase (typical 150ms).





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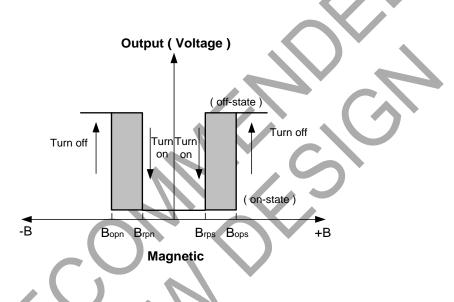
# **Magnetic Characteristics** (@T<sub>A</sub> = +25°C, $V_{DD}$ = 3V, unless otherwise specified.)

(1mT=10 Gauss)

Symbol	Characteristic	Min	Тур	Max	Unit
Bops(south pole to brand side)	Operate Reint	-	40	60	
Bopn(north pole to brand side)	Operate Point	-60	-40	-	
Brps(south pole to brand side)	Release Point	10	30	-	Gauss
Brpn(north pole to brand side)	Release Folin	-	-30	-10	Cauco
Bhy( Bopx-Brpx )	Hysteresis	-	10	-	

Notes: 6. Typical data is at  $T_A$ = +25°C,  $V_{DD}$ =3V, and for design information only.

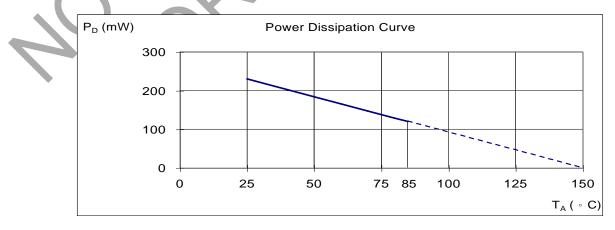
7. Operate point and release point will vary with supply voltage and operating temperature.



# **Performance Characteristics**

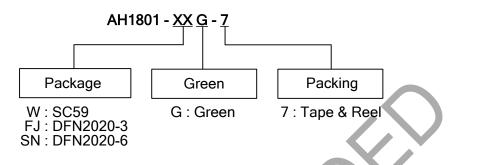
(1) SC59 / DFN2020-3 /DFN2020-6

Т <sub>А</sub> (°С)	25	50	60	70	80	85	90	100	110	120	130	140	150
P <sub>D</sub> (mW)	230	184	166	147	129	120	110	92	74	55	37	18	0
		-						•				•	





### Ordering Information

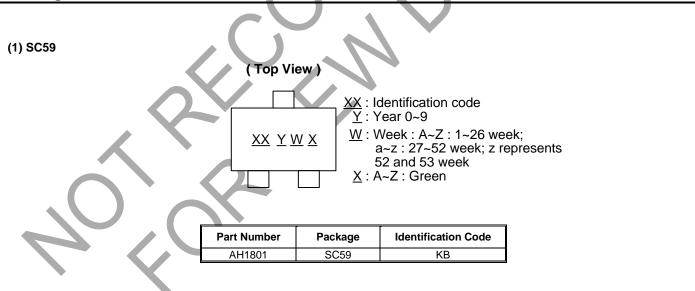


Dent Number	Status	Destant Orde	Packaging	7" Tape a	and Reel
Part Number	(Note 9)	Package Code	(Note 8)	Quantity	Part Number Suffix
AH1801-WG-7	NRND	W	SC59	3000/Tape & Reel	-7
AH1801-FJG-7	NRND	FJ	DFN2020-3	3000/Tape & Reel	-7
AH1801-SNG-7	NRND	SN	DFN2020-6	3000/Tape & Reel	-7

Notes: 8. Pad layout as shown on Diodes Incorporated's suggested pad layout document, which can be found on our website at http://www.diodes.com/package-outlines.html.

9. NRND = Not Recommended for New Design

### **Marking Information**

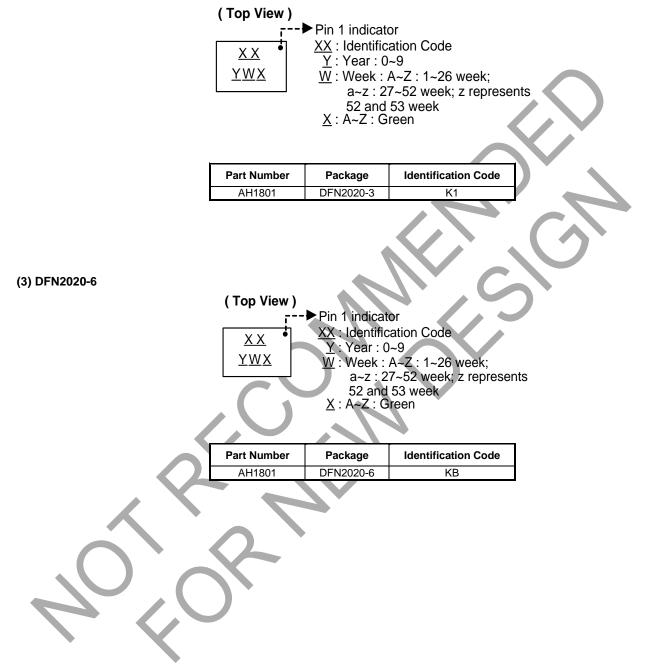




AH1801

### Marking Information (Cont.)

#### (2) DFN2020-3

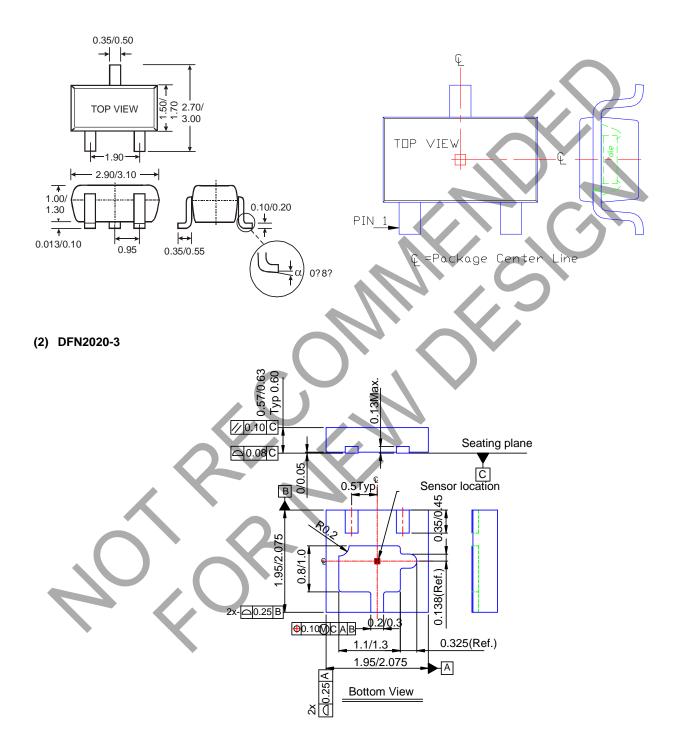




# Package Outline Dimensions (All dimensions in mm.)

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### (1) SC59 (Commonly Known as SOT23 in Asia)

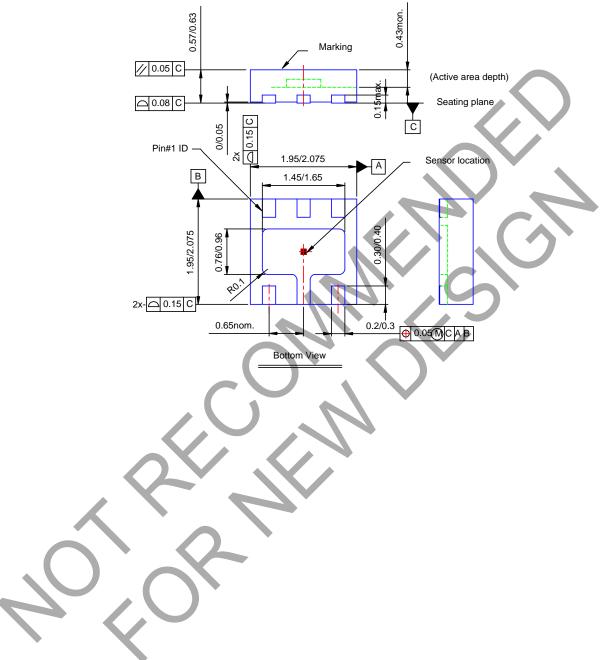




### Package Outline Dimensions (All dimensions in mm.) (Cont.)

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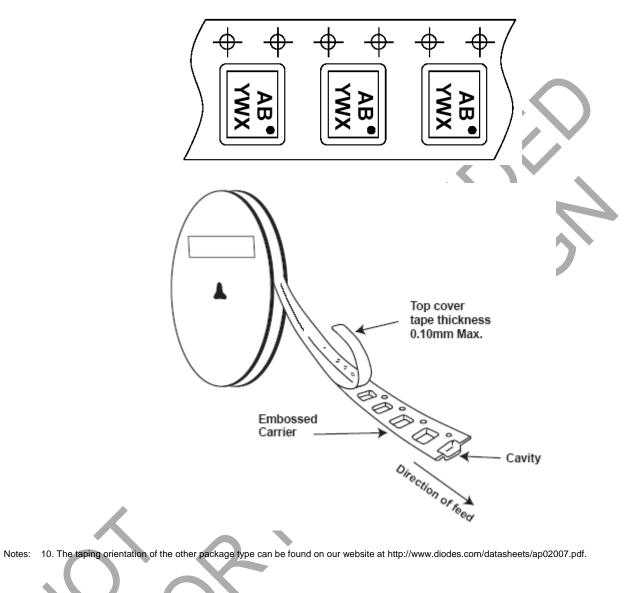
#### (3) DFN2020-6





# **Taping Orientation**

#### (1) DFN2020-3 and DFN2020-6





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