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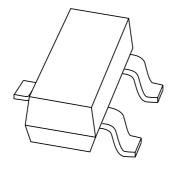
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Kind regards,

Team Nexperia

# DISCRETE SEMICONDUCTORS

# DATA SHEET



# **BSV52**NPN switching transistor

Product data sheet Supersedes data of 1999 Apr 15 2004 Jan 14



# **NPN** switching transistor

**BSV52** 

#### **FEATURES**

• Low current (max. 100 mA)

• Low voltage (max. 12 V).

#### **APPLICATIONS**

• High speed saturated switching applications, especially in portable equipment.

#### **DESCRIPTION**

NPN switching transistor in a SOT23 plastic package.

#### **MARKING**

TYPE NUMBER	MARKING CODE(1)
BSV52	B2*

#### Note

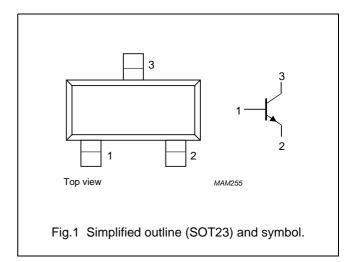
1. \* = p: Made in Hong Kong.

\* = t : Made in Malaysia.

\* = W: Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE NUMBER		PACKAGE				
TIPE NUMBER	NAME	DESCRIPTION	VERSION			
BSV52	_	plastic surface mounted package; 3 leads	SOT23			

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	20	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	12	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	200	mA
I <sub>BM</sub>	peak base current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		<b>-65</b>	+150	°C

# NPN switching transistor

BSV52

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

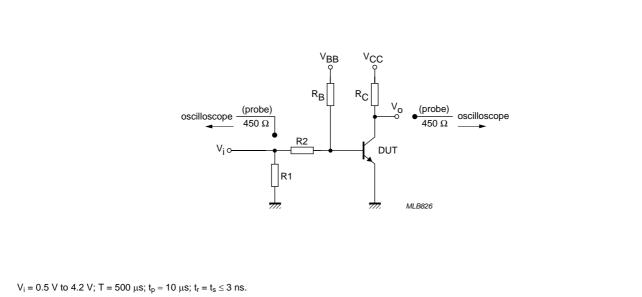
#### **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 20 V	_	_	400	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 20 V; T <sub>j</sub> = 125 °C	_	_	30	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 4 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V				
		I <sub>C</sub> = 1 mA	25	_	_	
		I <sub>C</sub> = 10 mA	40	_	120	
		I <sub>C</sub> = 50 mA	25	_	_	
V <sub>CEsat</sub>	collector-emitter saturation	$I_C = 10 \text{ mA}; I_B = 300 \mu\text{A}$	_	_	300	mV
voltage	voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	_	_	250	mV
		$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	_	_	400	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	700	_	850	mV
		$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	_	_	1.2	V
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 5$ V; $f = 1$ MHz	_	_	4	pF
C <sub>e</sub>	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 1 \text{ V}$ ; $f = 1 \text{ MHz}$	_	_	4.5	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	400	500	_	MHz
Switching t	imes (between 10% and 90% leve	els); (see Fig.2)				
t <sub>on</sub>	turn-on time	I <sub>Con</sub> = 10 mA; I <sub>Bon</sub> = 3 mA;	_	-	10	ns
t <sub>d</sub>	delay time	$I_{Boff} = -1.5 \text{ mA}$	_	-	4	ns
t <sub>r</sub>	rise time	1	_	-	6	ns
t <sub>off</sub>	turn-off time		_	_	20	ns
t <sub>s</sub>	storage time	1	_	-	10	ns
t <sub>f</sub>	fall time	1	_	-	10	ns

# NPN switching transistor

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R1 = 56  $\Omega$ ; R2 = 1 k $\Omega$ ; R<sub>B</sub> = 1 k $\Omega$ ; R<sub>C</sub> = 270  $\Omega$ .

 $V_{BB}$  = 0.2 V;  $V_{CC}$  = 2.7 V.

Oscilloscope: input impedance  $Z_i$  = 50  $\Omega$ .

Fig.2 Test circuit for switching times.

2004 Jan 14

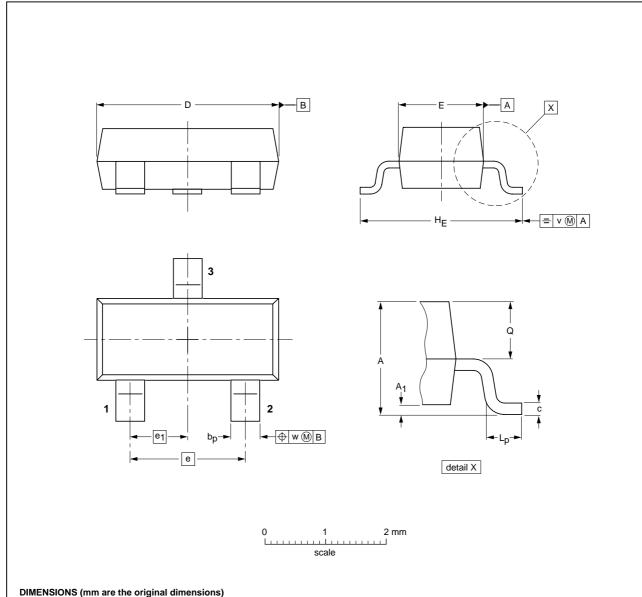
# NPN switching transistor

BSV52

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; 3 leads

SOT23



DIMENS	ЮИЗ (П	ım are tı	ne origir	nai dime	nsions)	

ι	JNIT	Α	A <sub>1</sub> max.	bp	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	v	w
	mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				<del>-04-11-04-</del> 06-03-16

### NPN switching transistor

BSV52

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

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## **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

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