

LOW-VOLTAGE 12-BIT 1:2 MUX / DEMUX BUS SWITCH WITH INTERNAL PULL DOWN RESISTORS

74CBTLV16292

FEATURES:

- 5Ω A/B bi-directional switch
- Isolation Under Power-Off Conditions
- · Make-before-break feature
- · Over-voltage tolerant
- Internal 500 Ω pull-down resistor to GND
- Latch-up performance exceeds 100mA
- Vcc = 2.3V 3.6V, normal range
- ESD >2000V per MIL-STD-883, Method 3015; >200V using machine model (C = 200pF, R = 0)
- · Available in TSSOP package

APPLICATIONS:

- 3.3V High Speed Bus Switching and Bus Isolation
- · Resource sharing

DESCRIPTION:

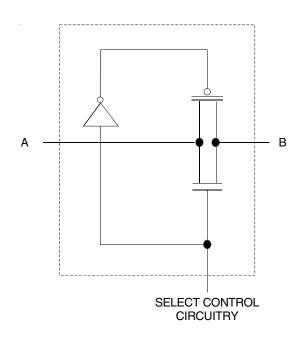
The CBTLV16292 is a single 12-bit multiplexing / demultiplexing bus switch, which provides high speed switching. This device has very low ON resistance, resulting in under 250ps propagation delay throught the switch. The demultiplexer side has a 500Ω resistor (R pulldown) termination to GND to eliminate floating nodes.

When the select (S) input is low, the A port is connected to the B1 port, and the R pulldown is connected to the B2 port. Similarly, when the S input is high, A port is connected to B2 port and the R pulldown is connected to B1 port.

FUNCTIONAL BLOCK DIAGRAM

RPULLDOWN SW RPULLDOWN SW RPULLDOWN 12A RPULLDOWN 30 12B1

SIMPLIFIED SCHEMATIC, EACH SWITCH



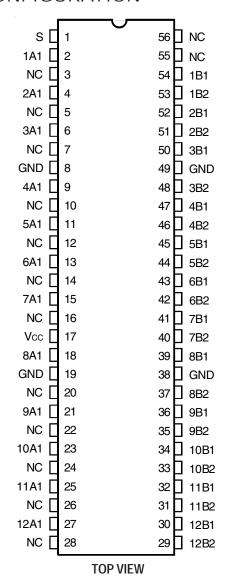
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INDUSTRIAL TEMPERATURE RANGE

JUNE 2019



PIN CONFIGURATION



Package Type	Package Code	Order Code
TSSOP	PAG56	PAG

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max.	Unit
Vcc	Supply Voltage Range	-0.5 to 4.6	V
Vı	Input Voltage Range	-0.5 to 4.6	V
	Continuous Channel Current	128	mA
lık	Input Clamp Current, VI/O < 0	-50	mA
Tstg	Storage Temperature Range	-65 to +150	°C

NOTE:

Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause
permanent damage to the device. This is a stress rating only and functional operation
of the device at these or any other conditions above those indicated in the operational
sections of this specification is not implied. Exposure to absolute maximum rating
conditions for extended periods may affect reliability.

PIN DESCRIPTION

Pin Names	Description
S	Select Input
хАх	Port A Inputs or Outputs
хВх	Port B Inputs or Outputs

FUNCTION TABLE(1)

Input	
S	Operation
L	A Port = B1 Port
	RPULLDOWN = B2 Port
Н	A Port = B2 Port
	RPULLDOWN = B1 Port

NOTE:

1. H = HIGH Voltage Level L = LOW Voltage Level

OPERATING CHARACTERISTICS⁽¹⁾

Symbol	Parameter	Test Conditions	Min.	Max.	Unit
Vcc	Supply Voltage		2.3	3.6	V
ViH	High-Level Control Input Voltage	Vcc = 2.3V to 2.7V	1.7	_	V
		Vcc = 2.7V to 3.6V	2	_	
VIL	Low-Level Control Input Voltage	Vcc = 2.3V to 2.7V	_	0.7	V
		Vcc = 2.7V to 3.6V	_	0.8	
TA	Operating Free-Air Temperature		-40	+85	°C

NOTE:

1. All unused control inputs of the device must be held at Vcc or GND to ensure proper device operation.



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DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: $TA = -40^{\circ}C$ to $+85^{\circ}C$

Symbol	Parameter	Test Conditions		Min.	Тур. ⁽¹⁾	Max.	Unit
Vik	Control Inputs, Data I/O	VCC = 3V, II = -18mA		_	_	-1.2	V
lı	Control Inputs	Vcc = 3.6V, Vi = Vcc or GNE)	_	_	±1	μA
loff		Vcc = 0V, Vi or Vo = 0V or 3	.6V	_	_	10	μΑ
Icc		Vcc = 3.6V, lo = 0, VI = Vcc	or GND	_	_	10	μA
Δ lcc ⁽²⁾	Control Inputs	Vcc = 3.6V, one input at 3V, o	ther inputs at Vcc or GND	_	_	300	μΑ
Сі	Control Inputs	VI = 3.3V or 0		_	3.5	_	рF
CIO(OFF)	A port or B port	Vo = 3.3V or 0		_	22.5	_	pF
	Max. at Vcc = 2.3V	VI = 0	Io = 64mA	_	5	8	
	Typ. at Vcc = 2.5V		Io = 24mA	_	5	8	
Ron ⁽³⁾		Vı = 1.7V	Io = 15mA	_	11	40	Ω
		VI = 0	Io = 64mA	_	3	7	
	Vcc = 3V		Io = 24mA	_	3	7	
		VI = 2.4V	Io = 15mA	_	7	15	

NOTES:

- 1. Typical values are at 3.3V, +25°C ambient.
- 2. The increase in supply current is attributable to each input that is at the specified voltage level rather than Vcc or GND.
- 3. This is measured by the voltage drop between the A and B terminals at the indicated current through the switch.

SWITCHING CHARACTERISTICS

		Vcc = 2.5V ± 0.2V		Vcc = 3.3V ± 0.3V		
Symbol	Parameter	Min.	Max.	Min.	Max.	Unit
t _{PD} ⁽¹⁾	Propagation Delay	_	0.15	_	0.25	ns
	A to B or B to A					
t _{PD} ⁽²⁾	Propagation Delay Propagation Delay	2.5	7.1	2.5	6.7	ns
	S to A					
ten	Output Enable Time	1	5.6	1	5	ns
	S to B					
tois	Output Disable Time	1	5	1	4.5	ns
	S to B					
tmB/B ^(3,4)	Make-Before-Break Time	0	2	0	2	ns

NOTES:

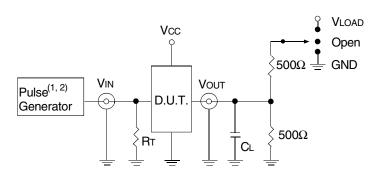
- 1. The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance when driven by an ideal voltage source (zero output impedance).
- 2. The condition to measure this propagation delay is by observing the change of voltage on the A port introduced by static fields equal to 3V or 0V for 3.3V±0.3V or Vcc or 0 for 2.5V±0.2V on B₁ and B₂ ports to get the required transition.
- 3. The make-before-break time is the duration between the make and break, during transition from one selected port to another.
- 4. This parameter is guaranteed by design but not production tested.



TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	Vcc ⁽¹⁾ = 3.3V±0.3V	Vcc ⁽²⁾ =2.5V±0.2V	
VLOAD	6	2 x Vcc	V
VIH	3	Vcc	V
VT	1.5	Vcc / 2	V
VLZ	300	150	mV
VHZ	300	150	mV
CL	50	30	pF



Test Circuits for All Outputs

DEFINITIONS:

CL = Load capacitance: includes jig and probe capacitance.

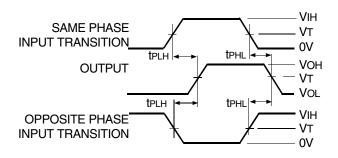
 RT = Termination resistance: should be equal to ZOUT of the Pulse Generator.

NOTES

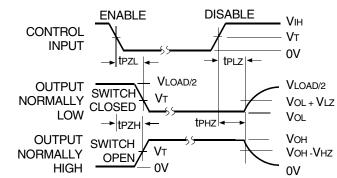
- 1. Pulse Generator for All Pulses: Rate \leq 10MHz; tr \leq 2.5ns; tr \leq 2.5ns.
- 2. Pulse Generator for All Pulses: Rate \leq 10MHz; tF \leq 2ns; tR \leq 2ns.

SWITCH POSITION

Test	Switch
tplz/tpzl	Vload
tphz/tpzh	GND
tpp	Open



Propagation Delay



NOTES:

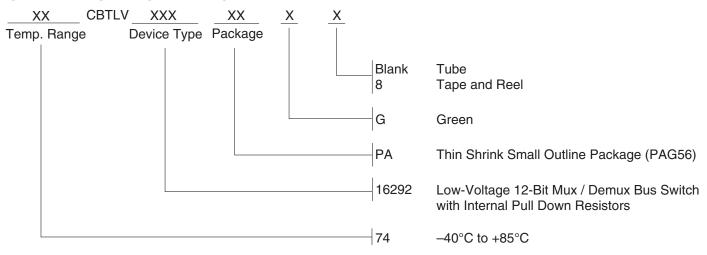
- 1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.
- Disable Low waveform applies to outputs that are LOW, except when disabled by the output control S.

Enable and Disable Times



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ORDERING INFORMATION



Orderable Part Information

Speed (ns)	Orderable Part ID	Pkg. Code	Pkg. Type	Temp. Grade
	74CBTLV16292PAG	PAG56	TSSOP	I
	74CBTLV16292PAG8	PAG56	TSSOP	Ī

Datasheet Document History

12/04/2014	Pg. 5	Updated the ordering information by removing the "IDT" notation and non RoHS part and by
		adding Tape and Reel information.
06/01/2019	Pg. 2,5	Added table under pin configuration diagram with detailed package information and orderable part information table.
		Undated the ordering information diagram in clearer detail

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

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