### NCE P-Channel Enhancement Mode Power MOSFET

#### Description

The NCE4801 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

#### **General Features**

•  $V_{DS} = -30V, I_{D} = -5A$ 

 $R_{DS(ON)}$  < 80m $\Omega$  @  $V_{GS}$ =-2.5V

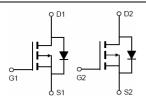
 $R_{DS(ON)}$  < 57m $\Omega$  @  $V_{GS}$ =-4.5V

 $R_{DS(ON)}$  < 48m $\Omega$  @  $V_{GS}$ =-10V

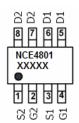
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

#### **Application**

- PWM applications
- Load switch
- Power management



#### Schematic diagram



#### **Marking and Pin Assignment**



SOP-8 top view

#### **Package Marking And Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE4801	NCE4801	SOP-8	Ø330mm	12mm	4000 units

#### Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Drain Current-Continuous	I <sub>D</sub>	-5	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-28	Α
Maximum Power Dissipation	P <sub>D</sub>	2	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	62.5	°C/W
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#### Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-30		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V	-	-	-1	μA



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# NCE4801

Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	·	•				
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-0.7	-1	-1.3	V
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	-	40	48	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	-	45	57	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3A		60	80	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-5A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ - 15\/\/ -0\/	-	880	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V, F=1.0MHz	-	105	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F = 1.0IVII 12	-	65	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	7	-	nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =-15V,I <sub>D</sub> =-5A	-	3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =6 $\Omega$	-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	$Q_g$		-	8.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-15V, $I_{D}$ =-5A, $V_{GS}$ =-4.5V	-	1.8	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-5A	-	-	-1.2	V

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production



## Typical Electrical and Thermal Characteristics

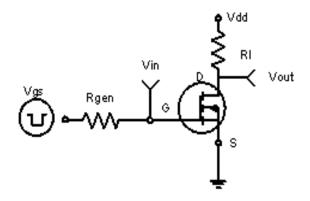
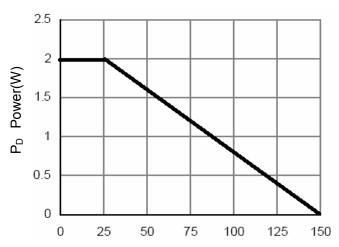
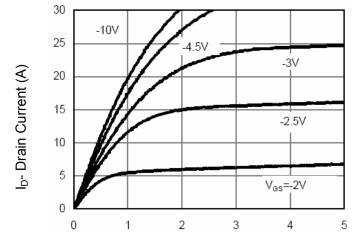


Figure 1:Switching Test Circuit



 $T_J$ -Junction Temperature( $^{\circ}$ C) Figure 3 Power Dissipation



Vds Drain-Source Voltage (V) Figure 5 Output Characteristics

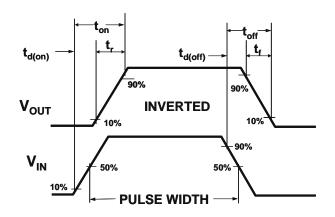
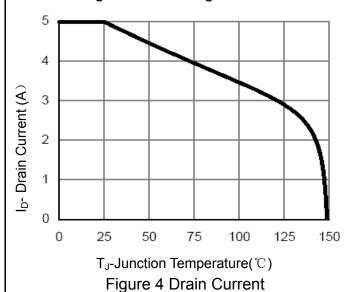


Figure 2:Switching Waveforms



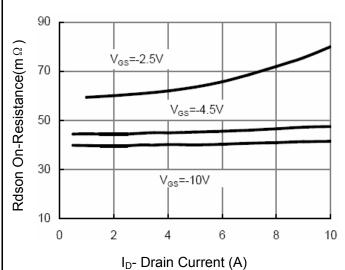
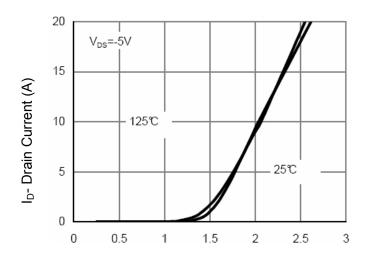


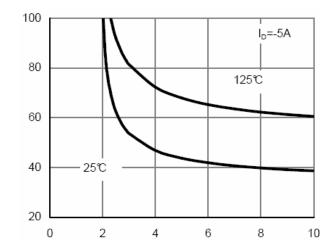
Figure 6 Drain-Source On-Resistance



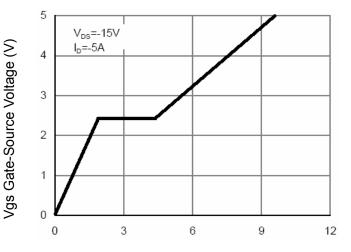
Rdson On-Resistance(m 2)



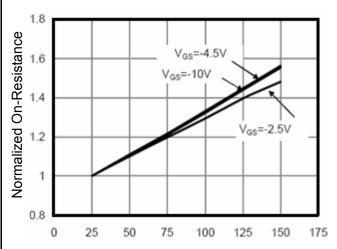
Vgs Gate-Source Voltage (V) Figure 7 Transfer Characteristics



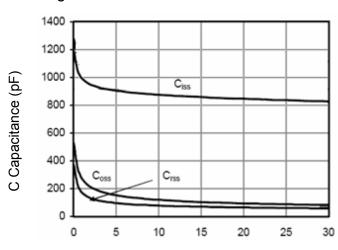
Vgs Gate-Source Voltage (V) Figure 9 Rdson vs Vgs



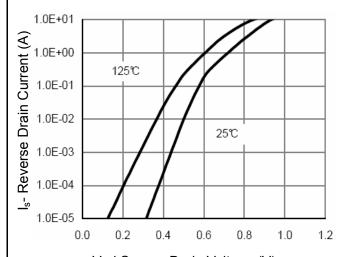
Qg Gate Charge (nC) Figure 11 Gate Charge



 $T_J$ -Junction Temperature( ${}^{\circ}$ C) Figure 8 Drain-Source On-Resistance

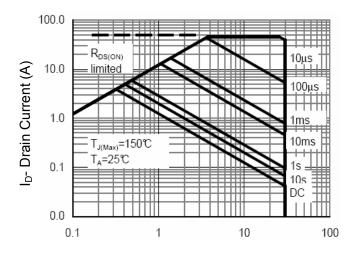


Vds Drain-Source Voltage (V)
Figure10 Capacitance vs Vds



Vsd Source-Drain Voltage (V)
Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)
Figure 13 Safe Operation Area

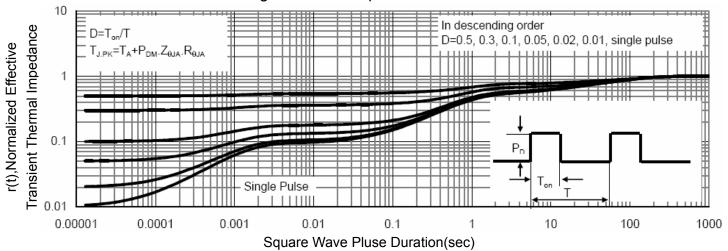
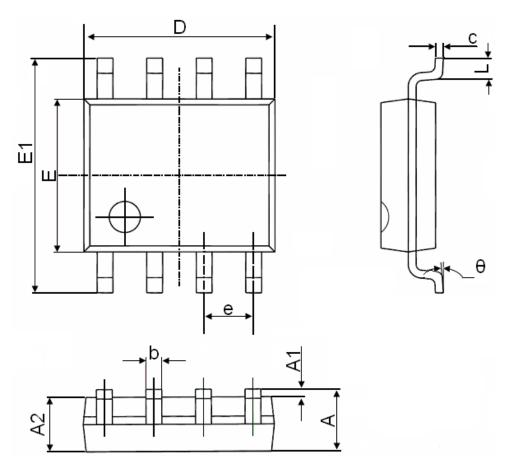


Figure 14 Normalized Maximum Transient Thermal Impedance



# **SOP-8 Package Information**



Comple ed	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
Е	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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