TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TCK2291xG

2A Load Switch IC with True Reverse Current Blocking

The TCK2291xG series is Load Switch ICs for power management with True Reverse Current Blocking and Thermal Shutdown function featuring low switch on resistance, ultra low quiescent current, high output current and wide input operating voltage range of 1.1 to 5.5 V. Switch ON resistance is only 31 m Ω at 5.0 V, -0.15 A load conditions and output current is available on 2.0 A. And these feature a slew rate control driver and output auto-discharge function.

These devices are available in 0.4 mm pitch ultra small package WCSP6E (0.8 mm x 1.2 mm, t: 0.55 mm). Thus these devices are ideal for portable applications that require high-density board assembly such as cellular phone.

Feature

- True Reverse Current Blocking
- Thermal Shutdown function

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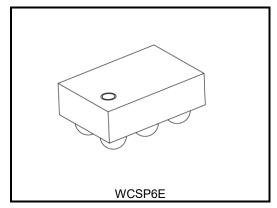
- Output auto-discharge (Option)
- Under voltage lockout
- Low ON resistance :

$$\begin{split} R_{ON} &= 31 \ m\Omega \ (typ.) \ at \ V_{IN} = 5.0 \ V, \ I_{OUT} = -0.15 \ A \\ R_{ON} &= 40 \ m\Omega \ (typ.) \ at \ V_{IN} = 3.3 \ V, \ I_{OUT} = -0.15 \ A \end{split}$$

 R_{ON} = 70 m Ω (typ.) at V_{IN} = 1.8 V, I_{OUT} = -0.15 A

 R_{ON} = 141 m Ω (typ.) at V_{IN} = 1.2 V, I_{OUT} = -0.15 A

- Low Quiescent Current: $I_Q = 11 \ \mu A$ (typ.) at $I_{OUT} = 0 \ mA$
- Low standby current: $I_{Q(OFF)} = 0.6 \ \mu A$ (typ.) at OFF state
- Inrush current reduction circuitt
- Pull down connection between Control and GND(Option)
- Ultra small package : WCSP6E (0.8 mm x 1.2 mm, t: 0.55 mm)



Weight: 1 mg (typ.)



Function Table

Part number	Function							
	True Reverse current blocking	Output auto- discharge	Under voltage lock out	Thermal shutdown	Control pin polarity	Control pin pull down connection	Device Marking	
TCK22910G	Built in	N/A	Built in	Built in	Active Low	N/A	4S	
TCK22911G	Built in	Built in	Built in	Built in	Active Low	N/A	3S	
TCK22912G	Built in	N/A	Built in	Built in	Active High	Built in	2S	
TCK22913G	Built in	Built in	Built in	Built in	Active High	Built in	1S	

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol		Rating	Unit			
Input voltage	VIN		-0.3 to 6.0	V			
Control voltage	V _{CT}		-0.3 to 6.0				
Output voltage	V _{OUT} -0.3 to 6.0		V				
	1	DC	2.0	A			
Output current	Ιουτ	Pulse	3.0 (Note1)	A			
Power dissipation	PD		800 (Note 2)				
Operating temperature range	Topr		-40 to 85	°C			
Junction temeperature	Tj	150		°C			
Storage temperature	T _{stg}		-55 to 150	°C			

Note : Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: 100 µs pulse, 2% duty cycle

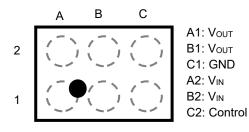
Note2: Rating at mounting on a board

Glass epoxy board dimension : 40mm x 40mm (both sides of board), t=1.6mm Metal pattern ratio : a surface approximately 50%, the reverse side approximately 50% Through hole : diameter 0.5mm x 28)

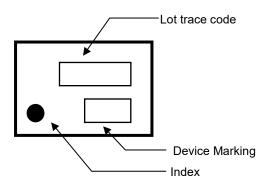
Operating conditions

Characteristics	Symbol	Condition	Min	Max	Unit	
Input voltage	Vin	—	1.1	5.5	V	
Output voltage	Vout	—	_	VIN	V	
Output current	Ιουτ	$1.8 V \le V_{IN}$	_	2.0	А	
Control Lligh lovel input veltage	Maria	1.2V < V _{IN} ≤ 5.5 V	1.0	-	V	
Control High-level input voltage	Vih	1.1V ≤VIN ≤1.2 V	0.9	_	v	
Control Low-level input voltage	VIL	—	_	0.4	V	

Pin Assignment(Top view)

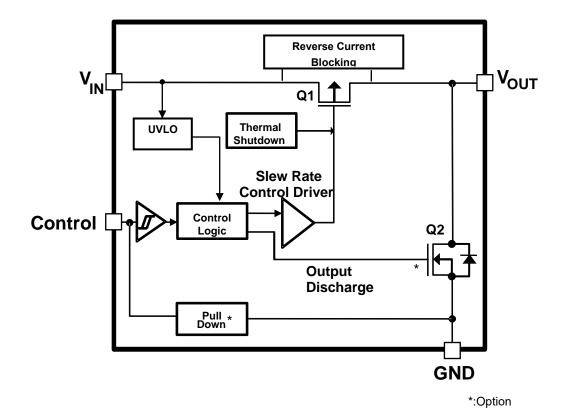


Top marking





Block Diagram



Operation logic table

		TCK22910G	TCK22911G	TCK22912G	TCK22913G
	Output Q₁	OFF	OFF	ON	ON
Control	Discharge Q ₂	_	ON		OFF
"High"	Reverse current blocking	Active	Active	Active	Active
	Output Q ₁	ON	ON	OFF	OFF
Control	Discharge Q ₂	_	OFF	_	ON
"Low"	Reverse current blocking	Active	Active	Active	Active

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Observations	Quarter	Task		7	Ta = 25°C		Ta = -40	Linit	
Characteristics	Symbol	lest	Condition	Min	Тур.	Max	Min	Max	Unit
Quiescent ourrent (QNI state)			VIN = 1.1 V	_	9	_	_	—	μA
Quiescent current (ON state)	IQ	IOUT = 0 mA	VIN = 5.5 V	_	11	_	_	20	μA
Quiescent current (OFF state)	IQ(OFF)	V _{IN} = 5.5 V, V _O (Note 3)	UT = OPEN,	_	0.6	_	_	2.5	μΑ
Switch leakage current(OFF state)	ISD(OFF)	$\begin{array}{l} V_{OUT} = GND,\\ current through\\ from V_{IN} to\\ V_{OUT}.\\ (Note 4) \end{array}$	VIN = VCT = 5.5 V	_	20	_	_	2000	nA
Reverse blocking current	I _{RB}	Vout = 5.0 V, VIN = 0 V, RCB	active	_	0.01	_	_	2	μΑ
Reverse blocking voltage threshold	Vrb	Vout – Vin		_	35	—	_	_	mV
Reverse blocking release voltage threshold	V _{RBR}	Vout – Vin		_	-15	_	_	_	mV
Under Voltage Lock Out (UVLO) rising threshold	Vuvl_ri	_		_	0.82	_	_	1.1	V
Under Voltage Lock Out (UVLO) falling threshold	VUVL_FA	_		_	0.77	_	_	_	V
			V _{IN} = 5.0 V	_	31	—	_	85	
			VIN = 3.3 V	_	40	_	_	95	
On resistance	R _{ON}	I _{OUT} = -0.15 A	V _{IN} = 1.8 V	_	70	—	_	140	mΩ
			V _{IN} = 1.2 V	_	141	—	_	_	
			V _{IN} = 1.1 V	_	179	—	_	_	
Output discharge on resistance	R _{SD}	— (Note 5)		_	100	_	_	_	Ω

Note 3 : Except I_{SD(OFF)} OFF-state switch current

- Note 4 : Only applies to the TCK22910G and TCK22912G
- Note 5 : Only applies to the TCK22911G and TCK22913G

AC Characteristics (Ta = 25°C)

V_{IN} = 5.0 V, TCK22910G

Characteristics	Symbol	Test Condition(Figure 2)	Min	Тур.	Max	Unit
VOUT rise time	tr	$V_{\text{IN}}\text{=}~5.0~\text{V}$, RL = 500 Ω , CL=0.1 $\mu\text{F},$	_	1.4	_	ms
VOUT fall time	tf	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	120	_	μS
Turn on delay	ton	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	800	_	μS
Turn off delay	tOFF	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	5		μS

V_{IN} = 5.0 V, TCK22911G

Characteristics	Symbol	Test Condition(Figure 2)	Min	Тур.	Max	Unit
V _{OUT} rise time	tr	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	1.4	_	ms
V _{OUT} fall time	tf	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	60	_	μS
Turn on delay	ton	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	800	—	μS
Turn off delay	tOFF	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	5	—	μS

V_{IN} = 5.0 V, TCK22912G

Characteristics	Symbol	Test Condition(Figure 1)	Min	Тур.	Max	Unit
VOUT rise time	tr	$V_{\text{IN}}\text{=}~5.0~\text{V}$, RL = 500 Ω , CL=0.1 $\mu\text{F},$	_	1.4	_	ms
VOUT fall time	tf	$V_{\text{IN}}\text{=}~5.0~\text{V}$, RL = 500 Ω , CL=0.1 $\mu\text{F},$	_	120	_	μS
Turn on delay	ton	$V_{\text{IN}}\text{=}~5.0~\text{V}$, RL = 500 Ω , CL=0.1 $\mu\text{F},$	_	800	_	μS
Turn off delay	tOFF	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	10	_	μS

V_{IN} = 5.0 V, TCK22913G

Characteristics	Symbol	Test Condition(Figure 1)	Min	Тур.	Max	Unit
VOUT rise time	tr	$V_{\text{IN}}\text{=}~5.0~\text{V}$, RL = 500 Ω , CL=0.1 $\mu\text{F},$	_	1.4	_	ms
VOUT fall time	tf	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	60	_	μS
Turn on delay	tON	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	800	_	μS
Turn off delay	tOFF	$V_{\text{IN}}\text{=}~5.0~\text{V}$, $\text{R}_{\text{L}}\text{=}~500~\Omega$, $\text{C}_{\text{L}}\text{=}0.1~\mu\text{F},$	_	10	_	μS

AC Waveform

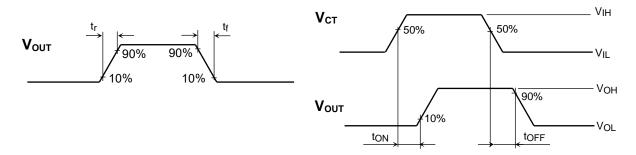


Figure 1 t_r, t_f, t_{ON}, t_{OFF} Waveforms(Active High)

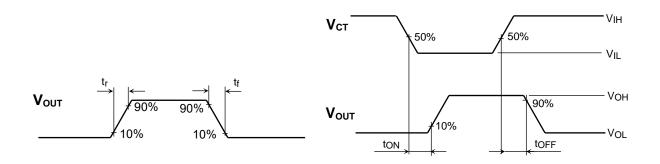
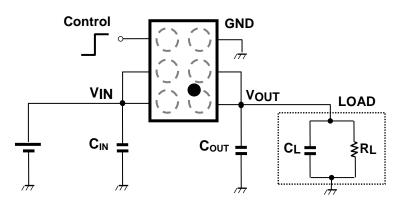


Figure 2 t_r, t_f, t_{ON}, t_{OFF} Waveforms(Active Low)

Application Note

1. Application circuit example (top view)

The figure below shows the example of configuration for TCK2291xG.



1) Input and Output capacitor

An input capacitor (CIN) and an output capacitor (COUT) are necessary for the stable operation of TCK2291xG. And they are effective to reduce voltage overshoot or undershoot due to sharp changes in output current and also for improved stability of the power supply. When used, place CIN and C_{OUT} more than 1.0µF as close to VIN pin and VOUT pin to improve stability of the power supply.

2) Control pin

The Control pin for TCK2291xG controls state of the switch, operated by the control voltage. Control pin is equipped with Schmitt trigger. Also, pull down resistance equivalent to a few M Ω is connected between Control and GND. Thus the load switch IC is in OFF state even when Control pin is OPEN. (except TCK22910G and TCK22911G). A control pins for TCK22910G and TCK22911G is Active low. Products that Control pin is an open connection, please use be sure to fix the potential of the Control pin to High or Low.

2. Thermal shutdown function

Each device has a built-in thermal shutdown circuit. If the junction temperature goes beyond 170°C (Typ.), thermal shutdown circuit operates and turns off power switch. When the junction temperature decreases lower than 150°C, the power switch is turned on due to hysteresis. This operation is repeated as long as the junction temperature continues increasing.

3. True reverse current blocking

Each device has built-in true reverse current blocking circuit (TRCB) to block reverse current from VOUT to VIN regardless of output MOSFET ON/OFF condition. (Full-Time Reverse Current Protection)

4. Under-voltage Lockout

Each device has a built-in under-voltage lockout circuit to turn off switch if VIN drops below UVLO. This circuit has hysteresis and UVLO is released when VIN exceeds threshold.

5. Instructions and directions for use

Each device has a built-in several functions, but these do not assure the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommends inserting failsafe system into the design.

6. Power Dissipation

Power dissipation is measured on the board condition shown below.

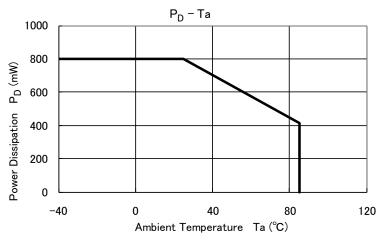
[The Board Condition]

Board material: Glass epoxy (FR4)

Board dimension: 40mm x 40mm (both sides of board), t=1.6mm

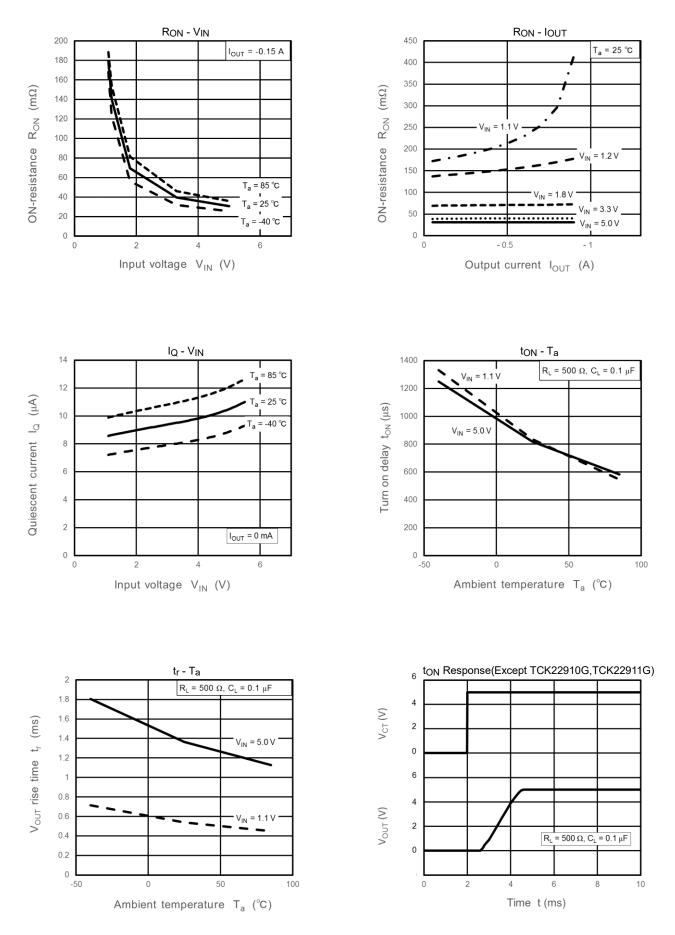
Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

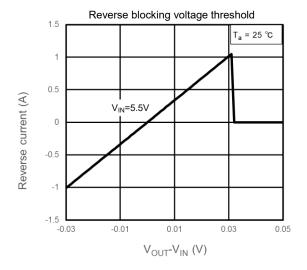
Through hole: diameter 0.5mm x 28



Please allow sufficient margin when designing a board pattern to fit the expected power dissipation. Also take into consideration the ambient temperature, input voltage, output current, etc. and applying the appropriate derating for allowable power dissipation during operation.

TCK2291xG Representative Typical Characteristics

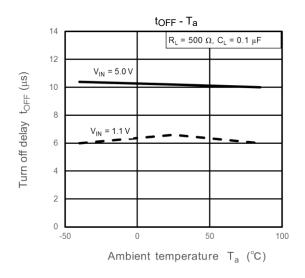




toff-Ta Representative Typical Characteristics

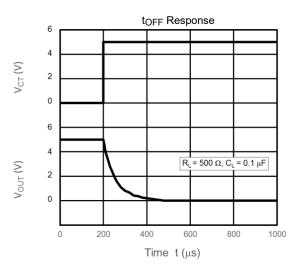
TCK22912G, TCK22913G

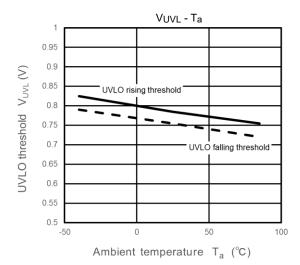
TOSHIBA



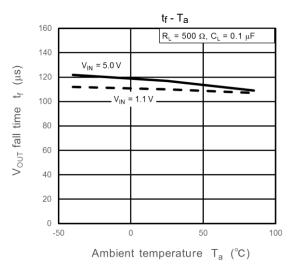
toff Response Representative Typical Characteristics

TCK22910G

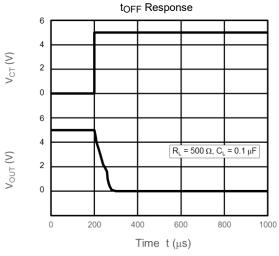




tf-Ta Representative Typical Characteristics TCK22910G, TCK22912G

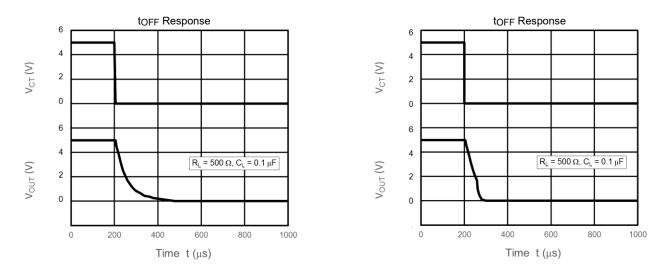






TCK22912

TCK22913G

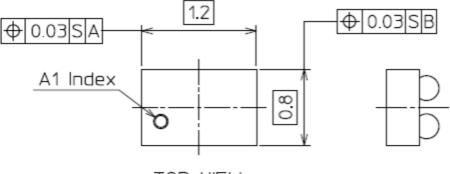


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

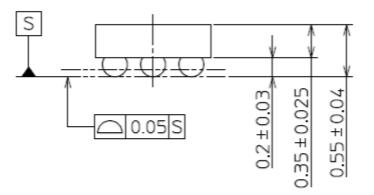


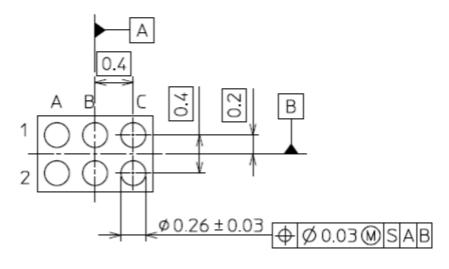
Package dimension

Unit: mm







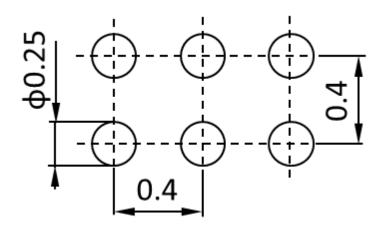


BOTTOM VIEW

Weight: 1 mg (typ.)

Land pattern dimensions (for reference only)

Unit: mm



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