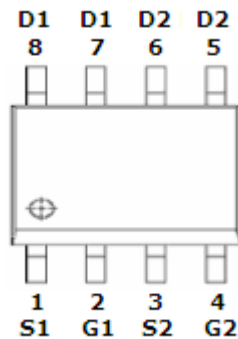


DESCRIPTION

The STC4614 is the N & P-Channel enhancement mode power field effect transistor using high cell density DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. This device is particularly suited for low voltage application such as notebook computer power management and other battery powered circuits, where high-side switching, low in-line power loss and resistance to transient are needed.

**PIN CONFIGURATION
SOP-8**

PART MARKING

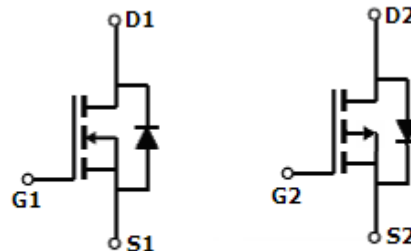

Y : Year A : Product code

FEATURE
N-Channel

- 40V/10.0A, $R_{DS(ON)} = 25m\Omega$ (Typ.)
@ $V_{GS} = 10V$
- 40V/6.0A, $R_{DS(ON)} = 32m\Omega$
@ $V_{GS} = 4.5V$

P-Channel

- -40V/-10.0A, $R_{DS(ON)} = 37m\Omega$ (Typ.)
@ $V_{GS} = -10V$
- -40V/-5.0A, $R_{DS(ON)} = 43m\Omega$
@ $V_{GS} = -4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOP-8 package


ORDERING INFORMATION

Part Number	Package	Part Marking
STC4614S8RG	SOP-8	STC4614
STC4614S8TG	SOP-8	STC4614

※ Process Code : A ~ Z ; a ~ z

※ STC4614S8RG S8 : SOP-8 ; R : Tape Reel ; G : Pb - Free

※ STC4614S8TG S8 : SOP-8 ; T : Tube ; G : Pb - Free



STC4614 

N&P Pair Enhancement Mode MOSFET

10.0A / -10.0A

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C Unless otherwise noted)

Parameter	Symbol	Typical		Unit
		N	P	
Drain-Source Voltage	V _{DSS}	40	-40	V
Gate-Source Voltage	V _{GSS}	±20	±20	V
Continuous Drain Current (T _J =150°C)	I _D	T _A =25°C 10.0	-10.0	A
		T _A =70°C 7.0	-6.0	
Pulsed Drain Current	I _{DM}	20	-20	A
Continuous Source Current (Diode Conduction)	I _S	2.5	-2.5	A
Power Dissipation	P _D	T _A =25°C 2.5	2.5	W
		T _A =70°C 1.8	1.8	
Operation Junction Temperature	T _J	150		°C
Storage Temperature Range	T _{STG}	-55/150		°C
Thermal Resistance-Junction to Ambient	R _{θJA}	T _□ 10Sec 62.5	62.5	°C/W
		Sready State 110	110	



STC4614  Lead-free

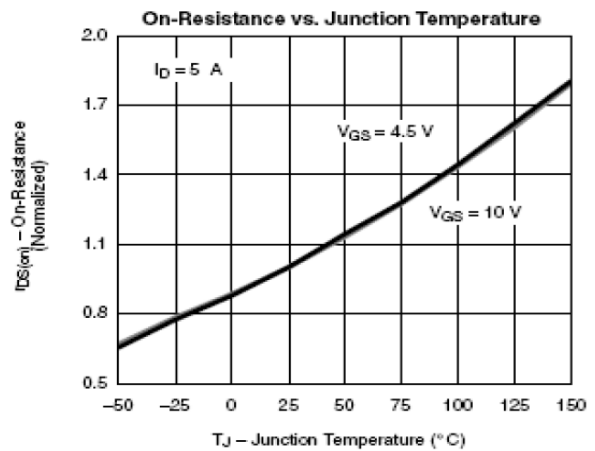
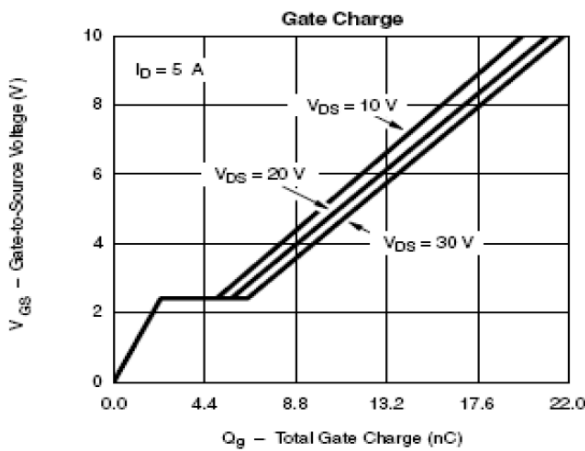
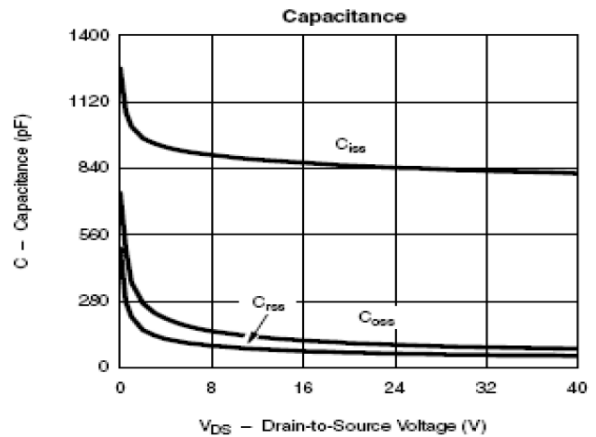
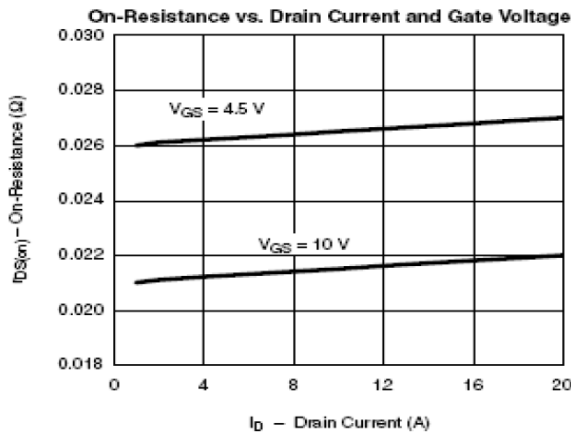
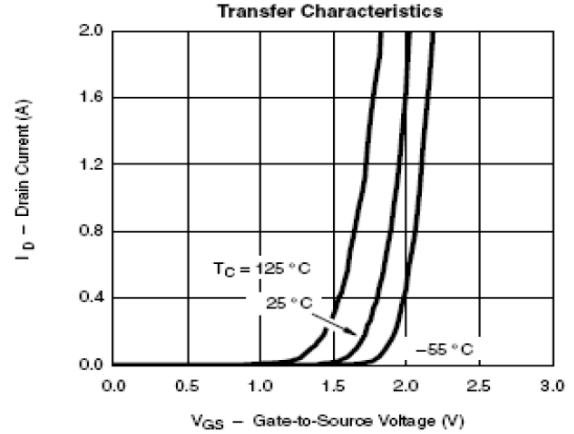
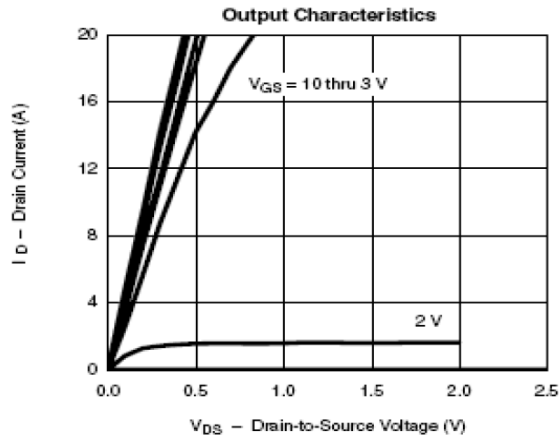
N&P Pair Enhancement Mode MOSFET

10.0A / -10.0A

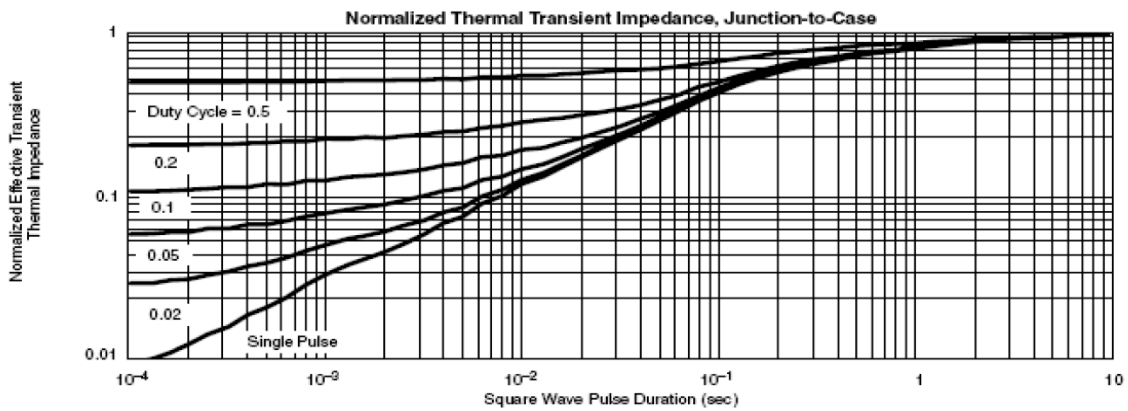
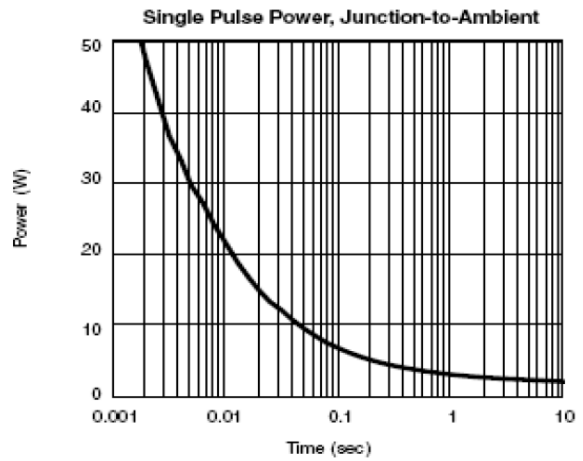
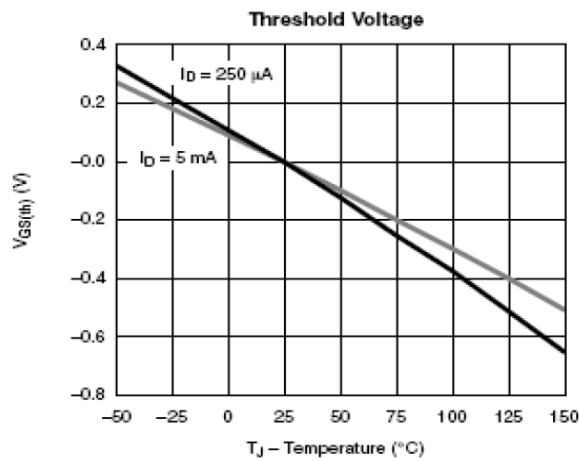
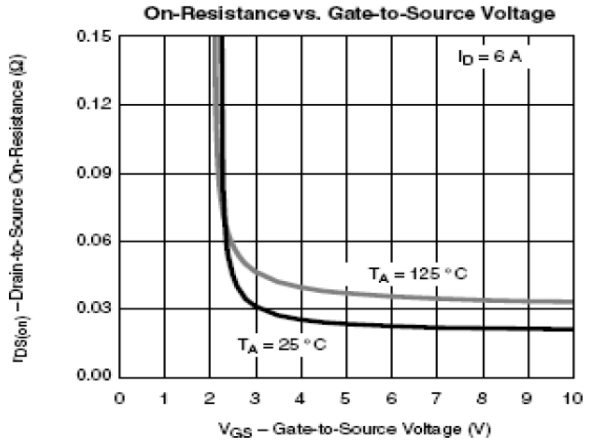
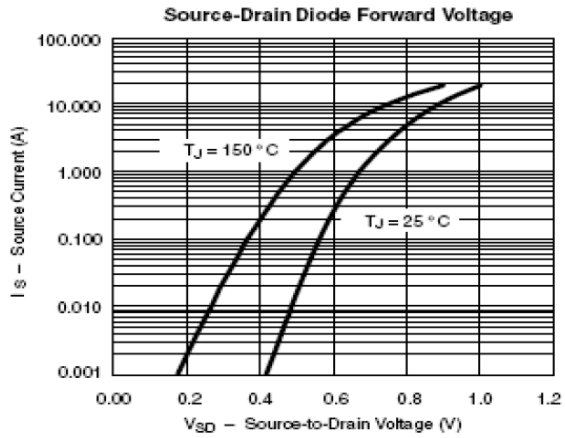
ELECTRICAL CHARACTERISTICS (Ta = 25°C Unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=10mA$ $V_{GS}=0V, I_D=-10mA$	N P	40 -40		V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250 \mu A$ $V_{DS}=V_{GS}, I_D=-250 \mu A$	N P	1.0 -1.0	3.0 -3.0	V
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$ $V_{DS}=0V, V_{GS}=\pm 20V$	N P		± 100 ± 100	nA
Zero Gate Voltage Drain Current	I_{DSS} $T_J=55^\circ C$	$V_{DS}=32V, V_{GS}=0V$	N		1	uA
		$V_{DS}=-32V, V_{GS}=0V$	P		-1	
		$V_{DS}=32V, V_{GS}=0V$	N		5	
		$V_{DS}=-32V, V_{GS}=0V$	P		-5	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5V, V_{GS}=10V$ $V_{DS} \leq -5V, V_{GS}=-10V$	N P	20 -20		A
Drain-source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10.0A$	N		0.025	Ω
		$V_{GS}=-10V, I_D=-10.0A$	P		0.035	
		$V_{GS}=4.5V, I_D=6.0A$	N		0.030	
		$V_{GS}=-4.5V, I_D=-5.0A$	P		0.043	
Forward Tran Conductance	g_{fs}	$V_{DS}=5V, I_D=6.9A$	N		22	S
		$V_{DS}=-15V, I_D=-5.9A$	P		13	
Diode Forward Voltage	V_{SD}	$I_S=1.0A, V_{GS}=0V$ $I_S=-1.7A, V_{GS}=0V$	N P		1.2 -1.2	V
Dynamic						
Total Gate Charge	Q_g	N-Channel $V_{DS}=20V, V_{GS}=10V$	N P		8.3 13.6	nC
Gate-Source Charge	Q_{gs}	$I_D \equiv 6.0A$	N		1.3	
			P		1.8	
Gate-Drain Charge	Q_{gd}	P-Channel $V_{DS}=-20V, V_{GS}=-10V$ $I_D \equiv -5.0A$	N		2.3	
			P		4.0	
Turn-On Time	$t_{d(on)}$ t_r	N-Channel $V_{DS}=20V, R_L=3.3 \Omega$ $I_D=1A, R_{GEN}=3 \Omega$	N		4.6	nS
			P		7.7	
			N		3.1	
			P		6.7	
Turn-Off Time	$t_{d(off)}$ t_f	P-Channel $V_{DS}=-20V, R_L=4.0 \Omega$ $I_D=-1A, R_{GEN}=-3 \Omega$	N		15.6	
			P		26.2	
			N		3.0	
			P		11.2	

TYPICAL CHARACTERISTICS (N MOS)



TYPICAL CHARACTERISTICS (N MOS)

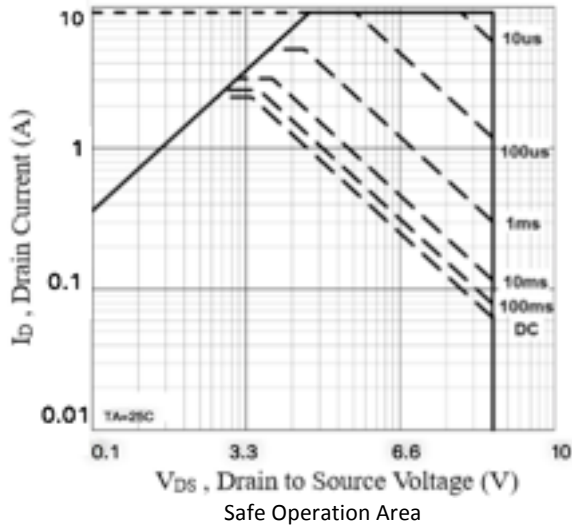




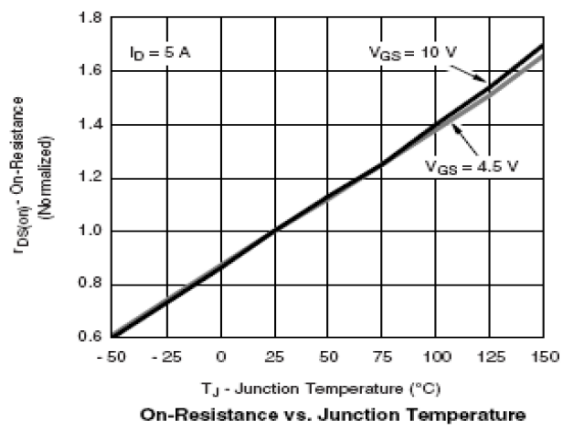
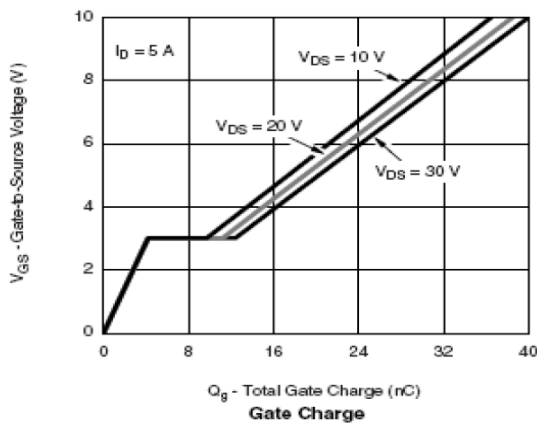
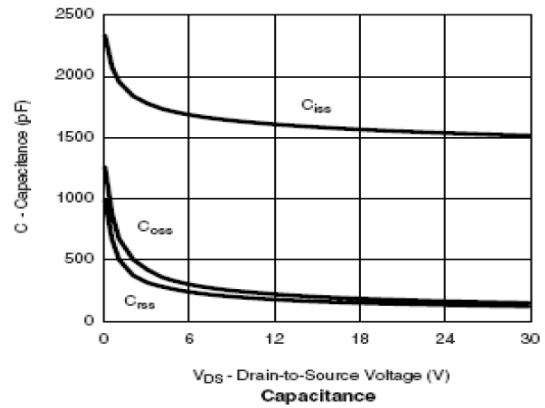
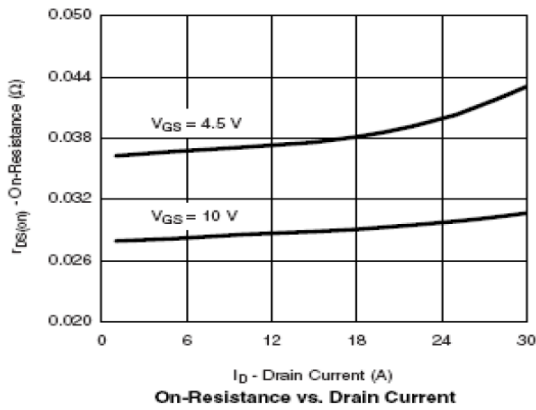
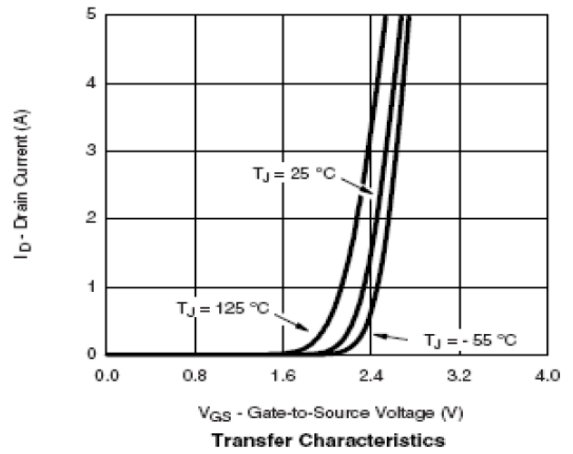
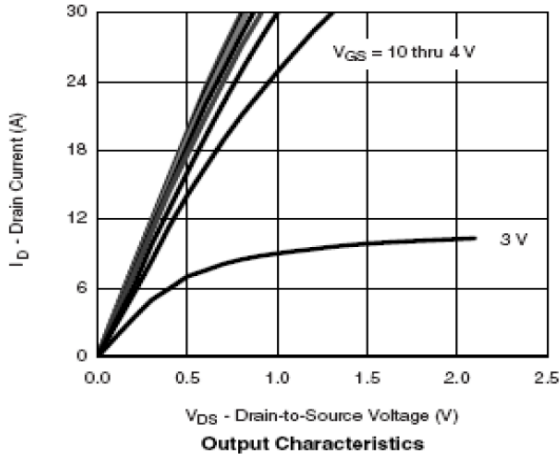
STC4614  Lead-free

N&P Pair Enhancement Mode MOSFET

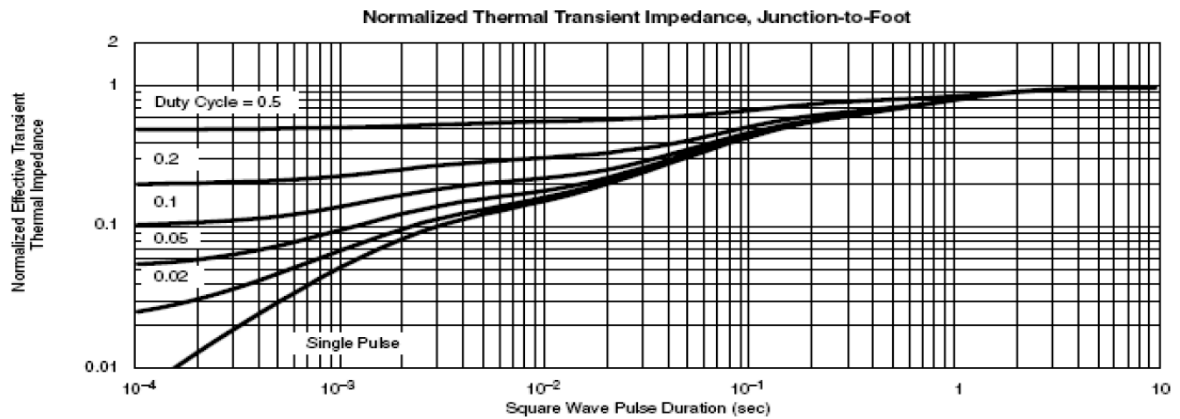
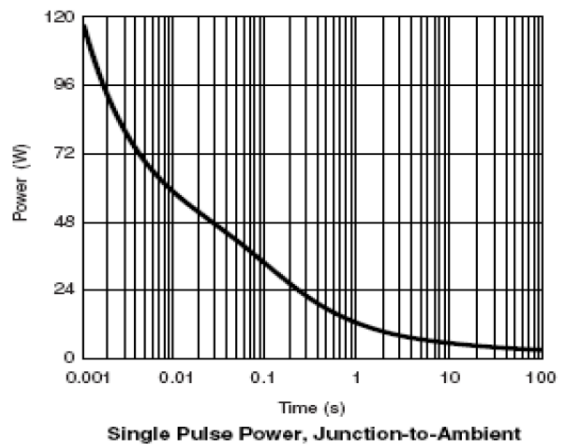
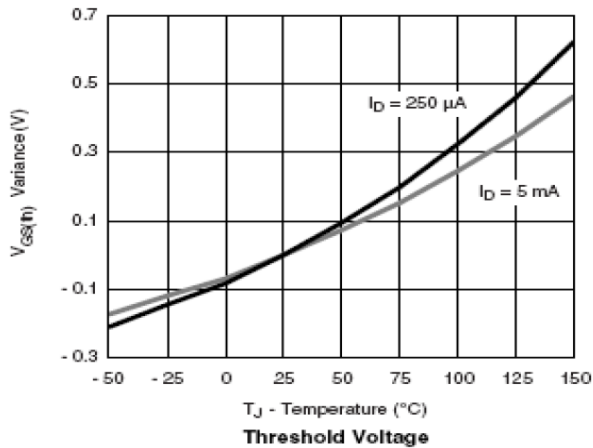
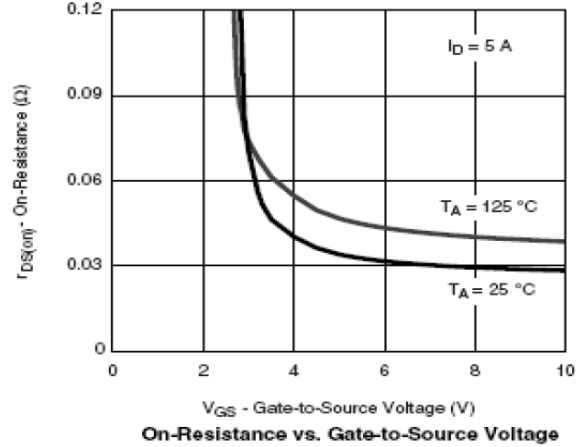
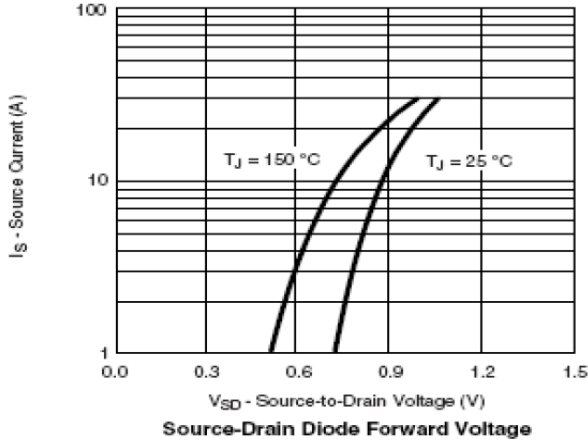
10.0A / -10.0A



YPICAL CHARACTERISTICS (P MOS)



TYPICAL CHARACTERISTICS (P MOS)

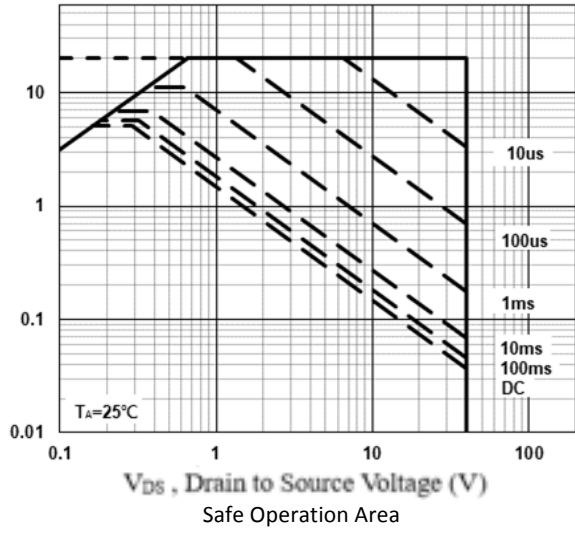


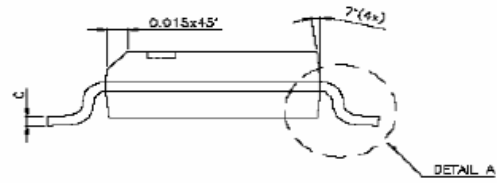
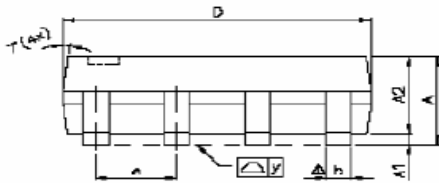
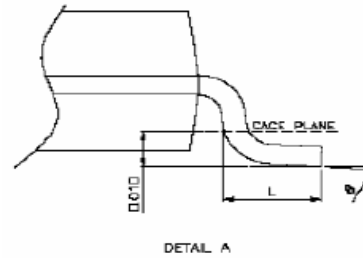
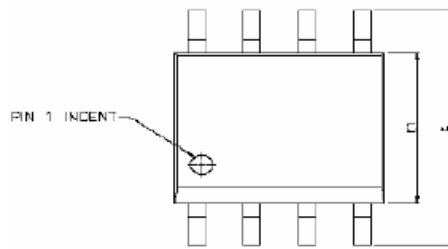


STC4614  Lead-free

N&P Pair Enhancement Mode MOSFET

10.0A / -10.0A



SOP-8 PACKAGE OUTLINE


SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
Δ y	—	—	0.076	—	—	0.003
\varnothing	0°	—	8°	0°	—	8°