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# TD041SCANFD DFN package isolated CANFD Transceiver

#### **Features**

- Ultra-small, ultra-thin, chip scale DFN package
- Compatible with the "ISO 11898-2" standard
- I/O power supply range supports 3.3V and 5V microprocessors
- High isolation to 3750Vrms
- Bus-Pin ESD protection up to 15kV(HBM)
- Baud rate up to 5Mbps
- -58V to +58V Bus fault protection
- >25kV/µs CMTI
- TXD dominant time-out function
- Low communication delay
- The bus supports maximum 110 nodes
- Meet AEC-Q100 standards
- EN62368 approval

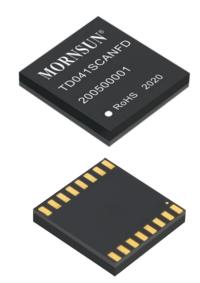
#### **Applications**

- Industrial automation, control, sensors and drive systems
- · Building and greenhouse environmental control(HVAC) automation
- Security system
- Transport
- Medical treatment
- Telecommunication
- Can Bus standard such as CAN open, Device Net, NMEA2000, ARNIC825, ISO11783, CAN Kingdom, CAN aerospace

#### Functional Description

TD041SCANFD is a isolated CAN bus transceiver, which is compliant with ISO11898-2 standard. The TD041SCANFD provide differential transmitting and receiving capability between the CANFD protocol controller and the physical layer bus. It is capable of running at data rates of up to 5 Mbps. The device has the function of series line, over-voltage, ground loss protection(-58V to +58V) and thermal shutdown so that it is especially suitable for working in harsh environment. The rated operating temperature range of TD041SCANFD is -40°C to 125°C.

#### Package



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#### **Pin Connection** GND<sub>2</sub> NC 16 GND<sub>1</sub> NC 14 GND<sub>1</sub> NC $V_{\text{DD2SENSE}}$ $V_{\text{DD2}}$ TD041SCANFD **RXD CANH** TXD **CANL** 10 NC $VDD_1$ GND<sub>1</sub> 8 $\mathsf{GND}_2$

Note: All GND1 pins are internally connected; All GND2 pins are internally connected.

# VDD1 GND1 RXD VDD2SENSE TXD Internal Block Diagram VDD2 Voltage Sense Signal transmission CANH

#### **Function Table**

Letter	Description
Н	High-Level
L	Low-Level
I	Indeterminate
X	Unrelated
Z	High Impedance
NC	No Connection

Table 1. Driver Function table

Power		Input	Output			
V <sub>DD1</sub>	Vdd2	TXD	Bus State	CANH	CANL	VDD2SENSE
On	On	L	Dominant H		L	L
On	On	Н	Recessive	Z	Z	L
On	On	left floating	Recessive	Z	Z	L
Off	On	X	Recessive	Z	Z	I
On	Off	L	I	I	I	Н

Table 2. Receiver Function table

Power		Inpu	ıt	Output		
V <sub>DD1</sub>	V <sub>DD2</sub>	VID = CANH - CANL	Bus State	RXD	VDD2SENSE	
On	On	≥0.9 V	Dominant	L	L	

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On	On	≤0.5 V	Recessive	Н	L
On	On	0.5  V < VID < 0.9  V	I	I	L
On	On	OPEN	Recessive	Н	L
Off	On	X	X	I	I
On	Off	X	X	Н	Н

#### Pin Descriptions

Pin Number	Pin Name	Pin Functions
1	NC	No Connect.
2	GND1	Ground(Logic side).
3	GND1	Ground(Logic side).
4	VDD2SENSE	VDD2 Voltage Sense.
5	RXD	Receiver Output Data(L:Dominant Bus State; H:Recessive Bus State).
6	TXD	Driver Input Data(L:Dominant Bus State; H:Recessive Bus State).
7	VDD1	Power Supply (Logic Side).
8	GND1	Ground(Logic side).
9	GND2	Ground (Bus Side).
10	NC	No Connect.
11	CANL	Low Level CAN Voltage Input/Output.
12	CANH	High Level CAN Voltage Input/Output.
13	VDD2	Power Supply (Bus Side).
14	NC	No Connect.
15	NC	No Connect.
16	GND2	Ground (Bus Side).

#### **Absolute Maximum Ratings**

General test conditions: Free-air, normal operating temperature range (unless otherwise specified).

	PARAMETERS	Min.	Max.	UNIT	
$V_{DD1}, V_{DD2}$	Power Supply	-0.5	6	V	
Vı	Input Voltage(TXD)	-0.5	V <sub>DD1</sub> + 0.5	V	
V <sub>O(RXD)</sub>	Output Voltage (RXD)	-0.5	V <sub>DD1</sub> + 0.5	V	
V <sub>O(SENSE)</sub>	Output Voltage (VDD2SENSE)	-0.5	V <sub>DD1</sub> + 0.5	V	
V <sub>CANH</sub> ,V <sub>CANL</sub>	Bus terminal voltage(CANH, CANL)	-58	58	V	
T <sub>A</sub>	Operating Temperature Range	-40	125	°C	
T <sub>stg</sub>	Storage Temperature Range	-50	150	°C	
Reflo	ow Soldering Temperature	Peak temp. ≤260°C, maximum duration ≤60s at 217°C. Please also refer toIPC/JEDEC J-STD-020D.3.			

Important: Exposure to absolute maximum rated conditions for an extended period may severely affect the device reliability, and stress levels exceeding the "Absolute Maximum Ratings" may result in permanent damage.

#### **Recommended Operating Conditions**

Recommended Operating Conditions				Nom.	Max.	Unit
V <sub>DD1</sub>	Power Supply(L	Power Supply(Logic Side)			5.5	
$V_{DD2}$	Power Supply(	Power Supply(Bus Side)			5.5	
V <sub>I</sub> or V <sub>IC</sub>	Voltage at any bus termin	Voltage at any bus terminal (common mode)			+12	V
V <sub>IH</sub>	High-level input v	High-level input voltage(TXD)				
V <sub>IL</sub>	Low-level input ve	Low-level input voltage(TXD)			0.8	
I <sub>D1</sub>	Input Static Current(Logic Side)	V <sub>DD1</sub> = 5.5V, V <sub>DD2</sub> = 5.25V,			3.5	
I <sub>D2</sub>	Input Static Current(Bus Side)	No Signal			13.5	mA

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Recommended Operating Conditions				Nom.	Max.	Unit
$P_D$	Power Dissipation	V <sub>DD1</sub> = 5.5V,V <sub>DD2</sub> = 5.25V, T <sub>A</sub> =105°C, R <sub>L</sub> = 60Ω; TXD Input Signal:f=500kHz; Duty=50%			200	
P <sub>D1</sub>	Power Dissipation(Logic Side)				25	mW
$P_{D2}$	Power Dissipation(Bus Side)				175	
	Signaling rate		40		5000	kbps

#### Electrical Characteristics

General test conditions and  $V_{DD1}$ = $V_{DD2}$ = 5V, Ta = 25 $^{\circ}$ C (unless otherwise specified).

PARAMETERS		CONDITIONS	Min.	Nom.	Max.	Unit
DRIVER			•		1	
VIH	Input High Voltage	TXD pin, see Figure 13	2			V
VIL	Input Low Voltage	TXD pin, see Figure 13			0.8	V
I <sub>IH</sub> , I <sub>IL</sub>	CMOS Input Current	TXD pin, see Figure 13			500	uA
VCANL, VCANH	Recessive Bus Voltage	V <sub>TXD</sub> = high, RL = ∞, see Figure 13	2.0		3.0	V
Vcanh	CANH Output Voltage	V <sub>TXD</sub> = low, see Figure 13	2.75		4.5	V
VCANL	CANL Output Voltage	V <sub>TXD</sub> = low, see Figure 13	0.5		2	V
V.	Differential Output Voltage	$V_{TXD}$ = low, RL = 45 $\Omega$ , see Figure 13	1.5		3	V
$V_{OD}$	Differential Output Voltage	V <sub>TXD</sub> = high, RL = ∞, see Figure 13	-500		+50	mV
RECEIVER						
V <sub>IT+</sub>	Positive-going input threshold voltage				900	mV
V <sub>IT-</sub>	Negative-going input threshold voltage		500			mV
VHYS	Hysteresis Voltage (V <sub>IT+</sub> – V <sub>IT-</sub> )	See Figure 12		150		mV
R <sub>IN</sub>	Differential Input Resistance		19	30	52	kΩ
$V_{OL}$	RXD Output Low Voltage	I <sub>OUT</sub> = 1.5 mA		0.2	0.4	V
$V_{OH}$	RXD Output High Voltage	I <sub>OUT</sub> = −1.5 mA	V <sub>DD1</sub> -0.4	V <sub>DD1</sub> -0.2		V
Сі	Input capacitance to ground (CANH or CANL)	TXD at 3 V, $V_1$ = 0.4 sin (4E6 $\pi$ t) + 2.5 V		26		pF
CID	Differential input capacitance	TXD at 3 V, V <sub>I</sub> = 0.4 sin (4E6πt)		5		pF
Other						
Vol	VDD2SENSE Output Low Voltage	I <sub>OSENSE</sub> = 1.5 mA		0.2	0.4	V
$V_{OH}$	VDD2SENSE Output High Voltage	I <sub>OSENSE</sub> = −1.5 mA	V <sub>DD1</sub> -0.4	V <sub>DD1</sub> -0.2		V
$V_{\text{TH}(\text{SENSE})}$	Bus Voltage Sense Threshold Voltage		2.0		2.5	V
	НВМ	CANH, CANL and GND			±15	KV
ESD	ПОІVІ	Other pins			±2	KV
	IEC/EN 61000-4-2 ( Contact ) Perf. Criteria B	CANH, CANL and GND			±4	KV
EFT	IEC61000-4-4: Perf. Criteria B	CANH, CANL and GND			±2	KV
Surge	IEC61000-4-5 : Perf. Criteria B	CANH, CANL and GND(Common Mode)			±2	KV
V <sub>I-O</sub>	Isolation Test	Leakage current <1mA.			3750	Vrms
R <sub>I-O</sub>	Insulation Resistance	At 500VDC	1000			ΜΩ
C <sub>I-O</sub>	Isolation Capacitor			3		pF
Cı	Input Capacitor			4		pF
CMTI	Common Mode Transient Immunity	TXD = $V_{DD1}$ or 0 V, $V_{CM}$ = 1 kV, transient magnitude = 800 V	25			kV/µs

Transmission Characteristics General test conditions and V<sub>DD1</sub>=V<sub>DD2</sub>= 5V, Ta = 25°C (unless otherwise specified).

PARAMETERS		PARAMETERS CONDITIONS		Nom.	Max.	Unit
t <sub>onTXD</sub>	Propagation Delay TXD On to Bus Active	$R_L = 60 \Omega$ , $C_L = 100 pF$ , see Figure 11			150	ns
t <sub>offTXD</sub>	Propagation Delay TXD Off to Bus	and Figure 13			200	ns

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	PARAMETERS	CONDITIONS	Min.	Nom.	Max.	Unit
t <sub>onRXD</sub>	Propagation Delay TXD On to Receiver Active				300	ns
toffRXD	Propagation Delay TXD Off to Receiver Inactive				250	ns
tse	Enable Time, VDD2 High to VDD2SENSE Low				100	us
t <sub>SD</sub>	Disable Time, VDD2 Low to VDD2SENSE High				100	us
t <sub>TXD_DTO</sub>	Dominant time-out time	C <sub>L</sub> = 100 pF	0.3	2	5	ms

#### **Physical Specifications**

PARAMETERS	Value	Unit
Weight	0.4(Typ.)	g

#### **Typical Performance Curves**

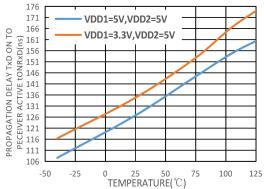


Figure 3. Propagation Delay from TxD On to receiver active vs.

Temperature

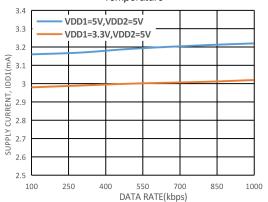


Figure 5. Supply Current(IDD1) vs. Data Rate

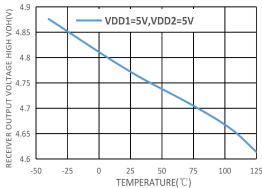


Figure 7. Receiver Output High Voltage vs. Temperature

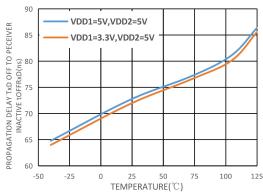


Figure 4. Propagation Delay from TxD Off to Receiver Inactive vs.
Temperature

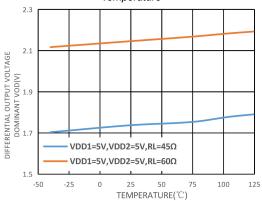


Figure 6. Drive Differential Output Voltage Dominant vs. Temperature

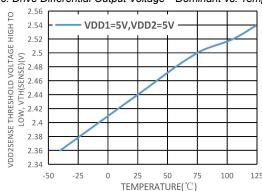
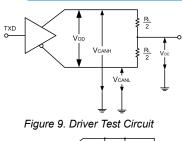
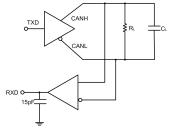


Figure8. VDD2 Voltage Sense Threshold Voltage High to Low vs.
Temperature



CANH O VID CANL O RXD

Figure 10. Receiver Test Circuit



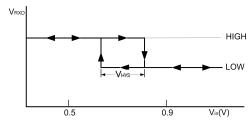


Figure 11. Switching Characteristics Test Circuit

Figure 12. Receiver Input Hysteresis

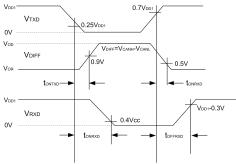


Figure 13. Drive and Receiver Propagation Drive

#### **Detailed Description**

TD041SCANFD is a isolated CAN bus transceiver with the ability of differential signal transmission between the bus and CAN protocol controller, which is compliant with ISO11898-2 standard. TD041SCANFD is an upgraded version of CAN. The main feature of the isolated CAN transceiver is to further enhance its data transmission performance that successfully achieves a data transfer rate of up to 5Mbit/s.

Short-circuit protection: TD041SCANFD has current-limiting protection to prevent the drive circuit from short-circuiting to positive and negative supply voltages. The power dissipation increases when a short circuit occurs. The short-circuit protection function protects the driver stage from damage.

Over-temperature protection: TD041SCANFD has over-temperature protection. When the over-temperature protection is triggered, the current in the driver stage will decrease. Because the drive tube is the primary energy consuming component, current reduction can reduce power consumption and reduce chip temperature. At the same time, the rest of the chip remains functional.

Dominant time-out function: TD041SCANFD has dominant time-out function to prevent if the pin TXD is forced to a permanent low level due to a hardware or software application failure, the built-in TXD dominant timeout timer circuit prevents the bus line from being driven to a permanent dominant state (blocking all network traffic). The timer is triggered by the negative edge on pin TXD. If the low level on pin TXD lasts longer than the internal timer value (tdom), the transmitter will be disabled and the drive bus will enter a recessive state. The timer is reset by the positive edge on pin TXD.

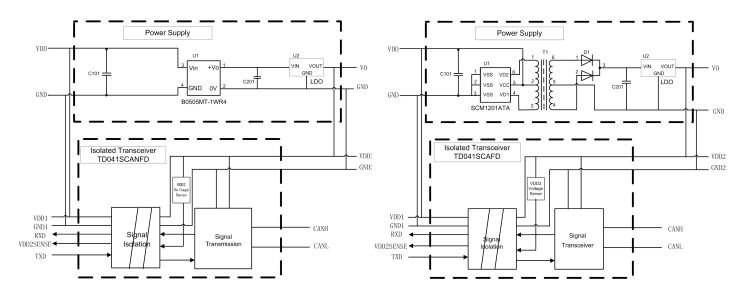


Figure 14. Drive and Receiver Propagation Drive

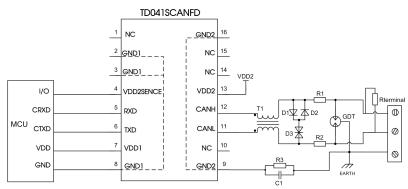


Figure 15. Port protection circuit for harsh environments

Recommended components and values:

Component	Recommended part, value	Component	Recommended part, value
R3	1ΜΩ	D1、D2	1N4007
C1	1nF, 2kV	D3	SMBJ30CA
T1	ACM2520-301-2P	R <sub>terminal</sub>	120Ω
GDT	B3D090L	R1、R2	2.7Ω/2W

When the module is used in applications with harsh environment, it can be susceptible to large energy like lightning strike, etc. in which case, it is essential to add an adequate protection circuit to the CAN signal ports to protect the system from failure and maintain a reliable bus communication. Figure 15 provides a recommended protection circuit design for high-energy lightning surges, with a degree of protection related to the selected protection device. Parameter description lists a set of recommended circuit parameters, which can be adjusted according to the actual application situation. Also, when using the shielded cable, the reliable single-point grounding of the shield must be achieved.

Note: The recommended components and values is a general guideline only and must be verified for the actual user's application. We recommended using PTC's for R1 and R2 and to use fast recovery diodes for D1 and D2.

#### **Using Suggests**

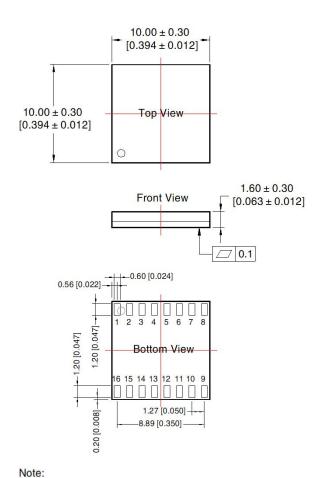
- ① Hot-swap is not supported.
- ② If the external input of TXD is insufficient, the pull-up resistor should be added according to the situation.
- 3 Refer to IPC 7093 for the welding process design of this product. For detailed operation guidance, please refer to Hot Air Gun Welding Operation Instruction for DFN Package Product or Welding Operation Instruction for DFN Package Product.

#### Ordering Information

Part number	Package	Number of pins	Product Marking	Tape & Reel
TD041SCANFD	DFN	16	TD041SCANFD	1.5k/REEL

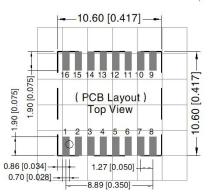
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## THIRD ANGLE PROJECTION



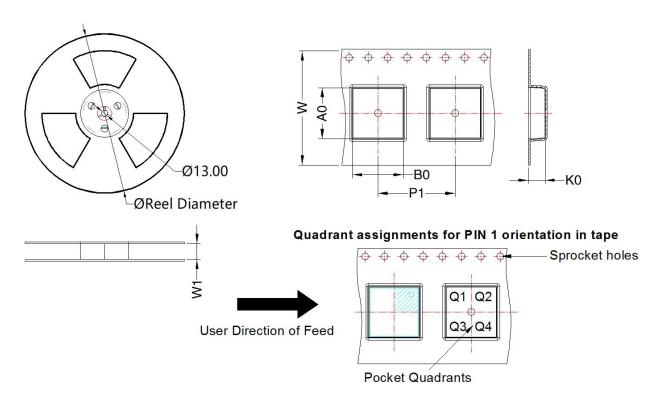


Note: Grid 2.54\*2.54mm

Pin-Out								
Pin	Function	Pin	Function					
1	NC	9	GND2					
2	GND1	10	NC					
3	GND1	11	CANL					
4	VDD2SENSE	12	CANH					
5	RXD	13	VDD2					
6	TXD	14	NC					
7	VDD1	15	NC					
8	GND1	16	GND2					

Unit: mm[inch]

Pin diameter tolerances:  $\pm 0.10[\pm 0.004]$ 



Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TD(H)041S485H											
TD(H)041SCANH	DFN 10x10	16	1500	330.0	24.4	10.44	10.44	2.0	16.0	24.0	Q2
TD(H)041SCANFD											

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