

### ■ Description

U6615S结合了一个专用电流模式PWM模式控制器和一个4A/630V MOSFEET。

U6615SHA高效、低待机功耗、低电磁兼容、低成本  
U6615S在输出功率为18W的离线反激式变换器上具有全面的保护功能，包括循环电流限制（OCP）、过温保护（OTP）、过电压钳位（OVP）和VCC上的欠压锁定（UVLO）。

U6615S采用SOP8包装。

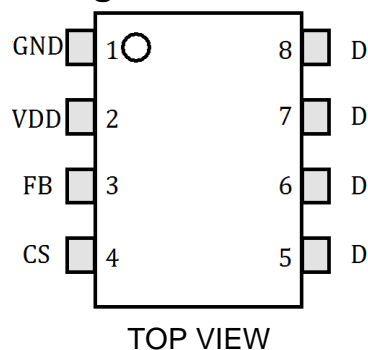
### ■ Features

- 数字频率洗牌技术改进提高EMI性能.
- 固定的 65kHz PWM 开关频率
- Leading-edge blanking on current sense.
- Internal synchronized slope compensation.
- Low stand-by power consumption (<100µA@VCC)
- Soft-start to reduce MOSFET Vds stress during power on
- Comprehensive protection function
  - Under voltage locked with hysteresis (UVLO) on VCC.
  - Over voltage protection (OVP) on VCC.
  - Cycle-by-Cycle current limitation.
  - Over load protection (OLP)
  - Over temperature protection (OTP)
  - Current limitation compensation to obtain the same output current in universal ac line input
- Low start-up current (<10µA@VCC=12V)

### ■ Applications

- Cell Phone Charger
- Digital Cameras Charger
- Battery charger

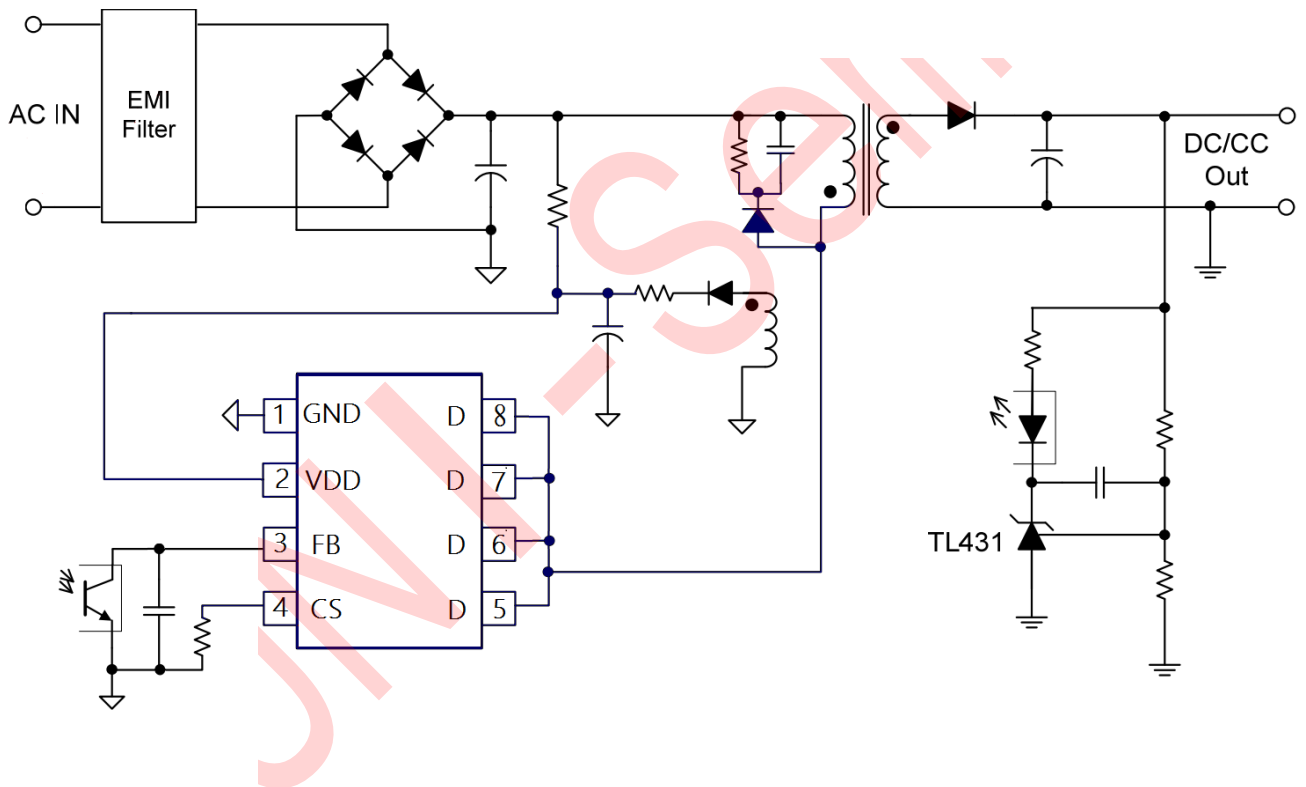
### ■ Package Information



## Pin Configuration

Pin Number	Pin Name	Function
1	GND	The ground of the IC.
2	VDD	IC power supply pin.
3	FB	Feedback pin. The loop regulation is achieved by connecting a photocoupler to this pin. PWM duty cycle is determined by this pin voltage and the current sense signal at Pin 4.
4	CS	Current Sense Input Pin.
5、6、7、8	D	The Power MOSFET Drain.

## Typical Application Circuit





■ **Electrical Characteristics** ( $T_A=25^{\circ}\text{C}$ ,  $V_{DD}=16\text{V}$ , if not otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
<b>Supply Voltage Section(VDD Pin)</b>						
$I_{VDD\_st}$	Start-up current into VDD pin			3	10	$\mu\text{A}$
$I_{VDD\_Op}$	Operation Current	$V_{FB}=2\text{V}$		1.5		$\text{mA}$
$V_{DD\_ON}$	VDD Under Voltage Lockout Exit		13	13.4	16.5	$\text{V}$
$V_{DD\_OFF}$	VDD Under Voltage Lockout Enter		6.8	7.8	8.2	$\text{V}$
$V_{DD\_OVP}$	VDD OVP Threshold		41		43	$\text{V}$
$V_{DD\_Clamp}$	VDD Zener Clamp Voltage	$I(V_{DD}) = 7 \text{ mA}$		44		$\text{V}$
<b>Feedback Input Section (FB Pin)</b>						
$V_{FB\_Open}$	FB Open Voltage			5.7		$\text{V}$
$I_{FB\_Short}$	FB Short Circuit Current	Short FB Pin to GND, Measure Current	90	150	300	$\mu\text{A}$
$Z_{FB\_IN}$	FB Input Impedance			30		$\text{Kohm}$
$V_{skip}$	FB Under Voltage GATE Clock is OFF			1.0		$\text{V}$
$V_{TH\_OLP}$	Power Limiting FB Threshold Voltage		2	2.5		$\text{V}$
$T_{D\_OLP}$	Power Limiting Debounce Time			60		$\text{ms}$

Current Sense Input Section (CS Pin)						
$T_{LEB}$	CS Input Leading Edge Blanking Time			400		ns
$V_{CS(max)}$	Current limiting threshold		0.51	0.56	0.61	V
$T_{D\_OC}$	Over Current Detection and Control Delay	GATE=1nF		70		ns
Oscillator Section						
$F_{OSC}$	Normal Oscillation Frequency		60	66.5	73	KHz
$\frac{\Delta F(shuffle)}{F_{OSC}}$	Frequency Shuffling Range		-4		4	%
$D_{MAX}$	Maximum Switching Duty Cycle			75		%
$F_{Burst}$	Burst Mode Base Frequency			25		KHz
On-Chip Thermal Shutdown						
$T_{SD}$	Thermal Shutdown	(Note 3)	---	165	--	°C
$T_{RC}$	Thermal Recovery	(Note 3)		140	--	°C
Power MOSFET Section (D Pin)						
$BV_{DSS}$	Power MOSFET Drain Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	630			V
$R_{dson}$	Static Drain-Source On Resistance	$V_{GS}=10V, I_D=2A$	2		3	$\Omega$

**Note:**

1. Stresses listed as the above "Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to maximum rating conditions for extended periods may remain possibility to affect device reliability.
2. The device is not guaranteed to function outside its operating conditions.
3. Guaranteed by the Design.

### ■ Peration Description

U6615S combines a dedicated current mode PWM mode controller with a 4A/630V MOSFEET. U6615S has high efficiency, low standby power consumption, low EMC and low cost.

- **System Start-Up Operation and IC Operation Current**

U6615S has very low start-up current that is less than 10uA. Therefore, a large resistor can be used in start-up circuit of switch power supply. This will minimize standby dissipation. The typical resistance of start-up resistor is 4M ohms.

The Operating current of U6615S is less than 1.5mA. Therefore, U6615S can have good efficiency.

- **Oscillator with Frequency Shuffling**

The frequency Shuffling is implemented in U6615S. The oscillation frequency is modulated with a random source so that the harmonic energy is spread out. The spread spectrum minimizes the conduction EMI and therefore reduces system design challenge.

- **Built-in Slope Compensation**

Slope compensation circuit adds voltage ramp onto the CS voltage according to PWM pulse width. This greatly improves the close loop stability at CCM and prevents the sub-harmonic oscillation and thus reduces the output ripple voltage. Slope compensation also help U6615S obtain the same output current in universal ac input voltage.

- **Current Sensing and Leading Edge Blanking (LEB)**

Cycle-by-Cycle current limitation is offered in U6615S. The switching current is detected by a resistor into the CS pin. An internal leading-edge

blanking circuit chops off the CS voltage spike at initial so that the external RC filtering on CS pin is no longer required. The current limiting comparator is disabled and thus cannot turn off the external MOSFET during the blanking period. PWM duty cycle is determined by the voltage in the CS pin and the FB pin.

- **Burst Mode Operation**

At zero load or light load condition, the main power dissipation in a switching mode power supply is from switching on the MOSFET, the core of transformer and the snubber circuit. The magnitude of power dissipation is proportional to the number of switching frequency within certain period. Less switching frequency can reduce the power dissipation. U6615S adjusts the switching frequency according to the loading condition. The PWM pulse width is kept greater than 1.2uS at any load condition. From light load to no load, the FB voltage drops. While the FB voltage is less than 1.1V, the gate pin output is disabled and kept low, while the FB voltage is higher than 1.2V, the gate output recovers to normal working mode. This is called 'burst mode'. To reduce audio noise, the switching frequency will be kept higher than 20KHz in burst mode.

- **Oscillator Operation**

The switching frequency is internally fixed at 65kHz. No external frequency setting components are required on PCB design.

- **On Chip Thermal Shutdown (OTP)**

When the IC temperature is over 165 °C , the IC shuts down. Only when the IC temperature drops to 140 °C, IC will restart.

- **Protection Controls**

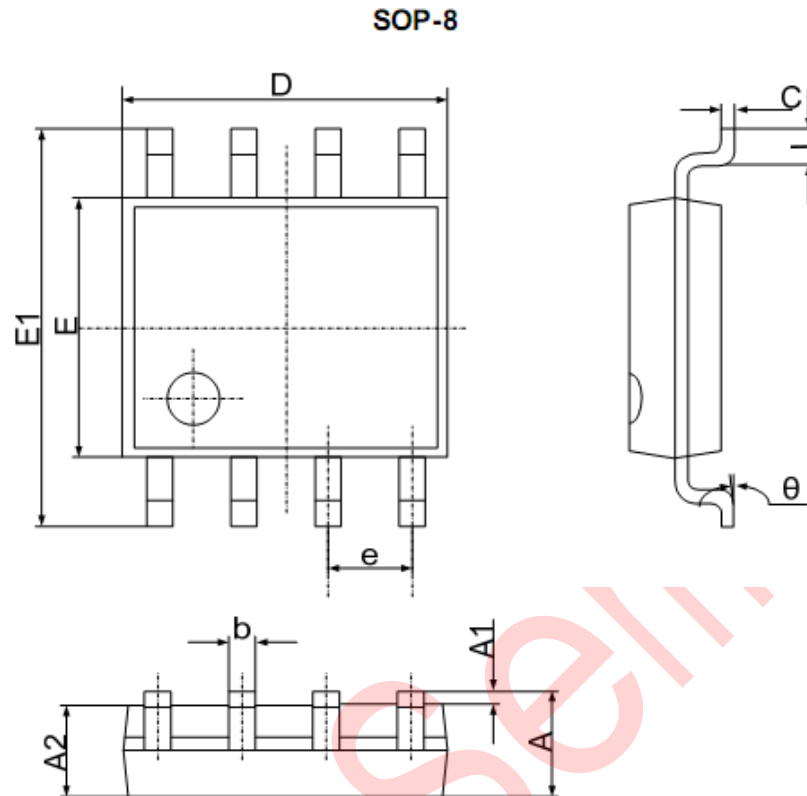
U6615S has comprehensive protection functions, including Cycle-by- Cycle current limitation (OCP), Over Load Protection (OLP) and over voltage clamp, Under Voltage Lockout on VCC (UVLO), Over Temperature Protection (OTP).

- **Current limitation compensation**

To obtain the same output current capability, the OLP threshold voltage is compensated for the different input AC voltage. This function makes the current of OLP is in consistency whatever the AC input is (110V or 220V).

UNI - Semi

## Package Dimensions



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°