

Features

- ◆ Ultra low capacitance: 0.1 pF typical (I/O to I/O)
- ◆ Ultra low leakage: nA level
- ◆ Low operating voltage: 3.3V
- ◆ Low clamping voltage
- ◆ Up to 4 data lines and one power line protects
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 20\text{kV}$
 - Contact discharge: $\pm 15\text{kV}$
 - IEC61000-4-4 (EFT) 40A (5/50ns)
 - IEC61000-4-5 (Lightning) : 6 A(8/20 μs)
- ◆ ROHS Compliant

Ordering Information

Part Number	Qty per Reel	Reel Size
TPESD3324PA	3000	7"

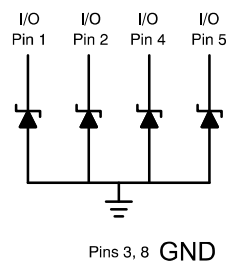
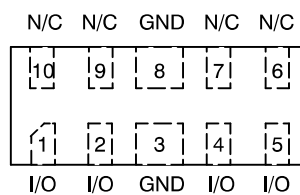
Mechanical Characteristics

- ◆ Package: DFN2510-10 (2.5×1.0×0.5mm)
- ◆ Ultra low leakage: nA level
- ◆ Case Material: "Green" Molding Compound.
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below

Applications

- ◆ High Definition Multimedia Interface (HDMI)
- ◆ Digital Visual Interface (DVI)
- ◆ Unified Display Interface (UDI)
- ◆ MDDI Ports
- ◆ PCI Express
- ◆ Serial ATA

Dimensions and Pin Configuration



Absolute Maximum Ratings (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Current (8/20μs)	I _{pp}	6	A
ESD per IEC 61000-4-2 (Air)	V _{ESD}	±20	kV
ESD per IEC 61000-4-2 (Contact)		±15	
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}			3.3	V	
Breakdown Voltage	V _{BR}	3.6	4	5.5	V	I _T = 1mA
Reverse Leakage Current	I _R			0.06	μA	V _{RWM} = 3.3V
Clamping Voltage	V _C			15	V	I _{PP} = 4A (8 x 20μs pulse) any I/O pin to ground
Junction Capacitance	C _J		0.3	0.4	pF	V _R = 0V, f = 1MHz, between I/O pins
Junction Capacitance	C _J			0.8	pF	V _R = 0V, f = 1MHz, any I/O pin to ground

Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)

Fig1. 8/20 μs Pulse Waveform

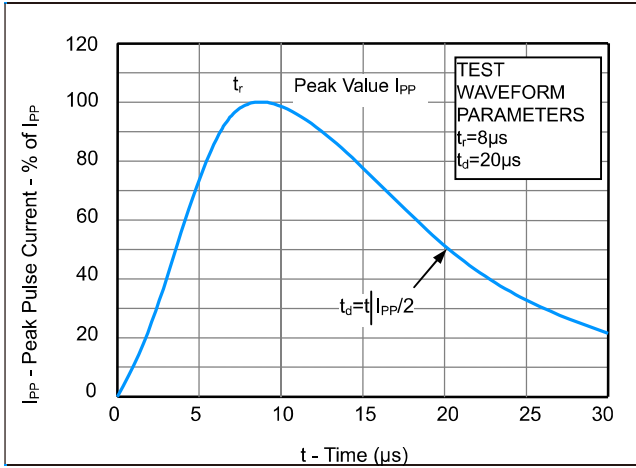


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

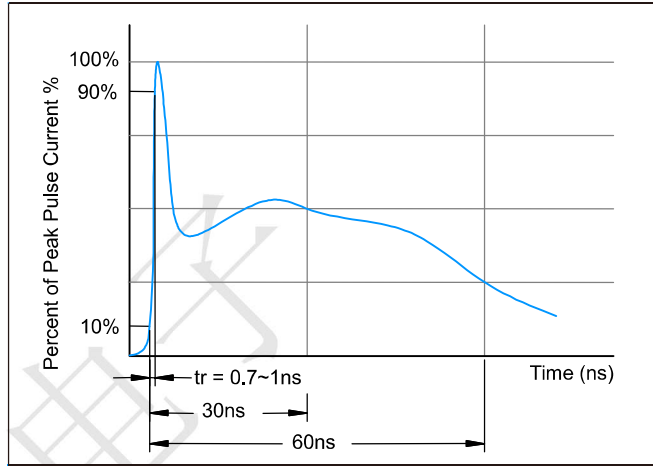
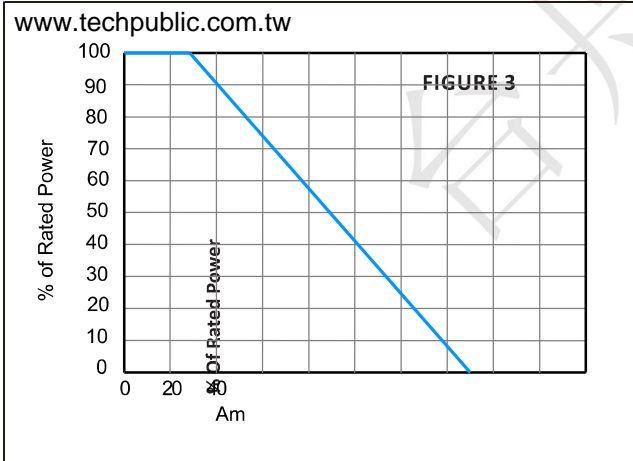
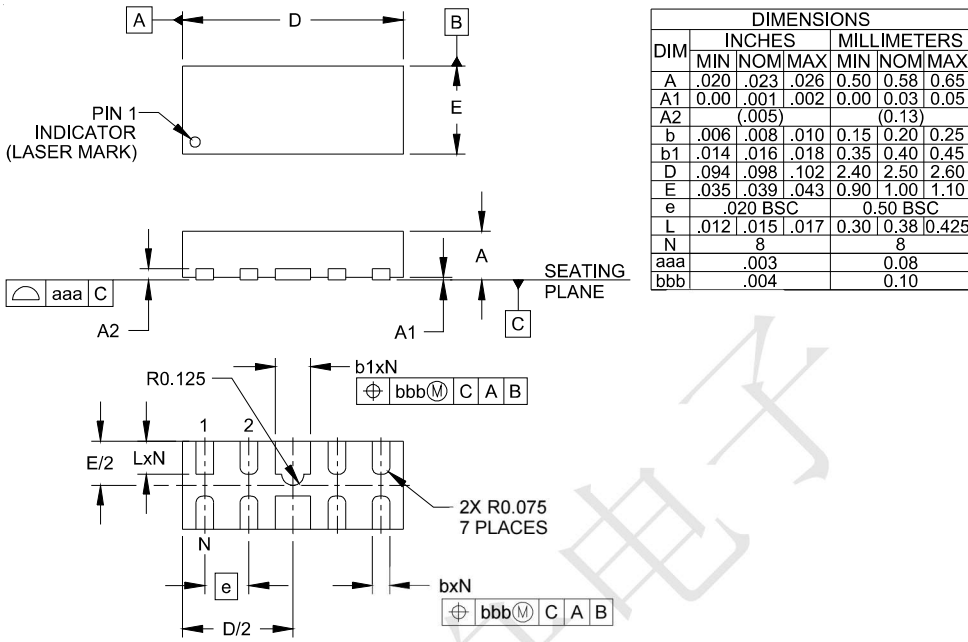


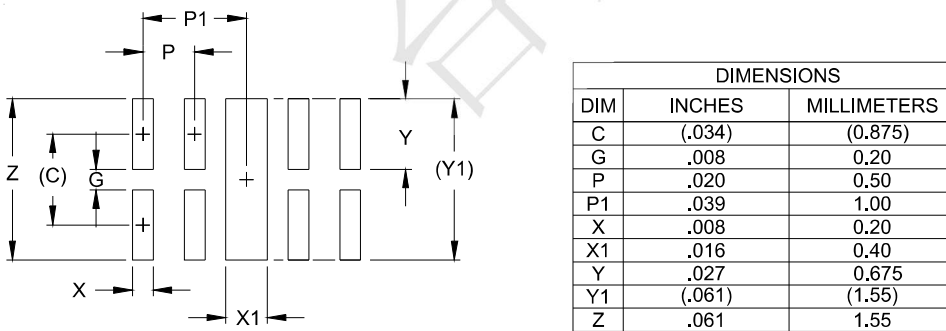
Fig3. Power Derating Curve



Outline Drawing - DFN2510-10



Land Pattern - DFN2510-10



Marking

or

