

Description

The HSBB02P15 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

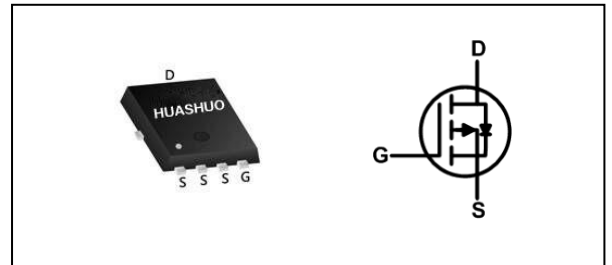
The HSBB02P15 meet the RoHS and Green Product requirement.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Product Summary

V_{DS}	-150	V
$R_{DS(ON),Max}$	780	m Ω
I_D	-2	A

PRPAK3*3 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $-V_{GS}$ @ $-10V^1$	-2	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $-V_{GS}$ @ $-10V^1$	-1.3	A
I_{DM}	Pulsed Drain Current ²	-8	A
EAS	Single Pulse Avalanche Energy ³	11.5	mJ
I_{AS}	Avalanche Current	4.8	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	7.7	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	16	$^\circ C/W$



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-150	---	---	V
ΔB _V DSS/ΔT _J	B _V DSS Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.12	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-1A	---	640	780	mΩ
		V _{GS} =-6V, I _D =-0.5A	---	710	950	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-2	-3	-4	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.8	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-150V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-120V, V _{GS} =0V, T _J =55°C	---	---	-10	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-1A	---	2	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	30	60	Ω
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-75V, V _{GS} =-10V, I _D =-1A	---	4.5	---	nC
Q _{gs}	Gate-Source Charge		---	0.7	---	
Q _{gd}	Gate-Drain Charge		---	1.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-75V, V _{GS} =-10V, R _G =10Ω, I _D =-1A	---	13	---	ns
T _r	Rise Time		---	8.8	---	
T _{d(off)}	Turn-Off Delay Time		---	17	---	
T _f	Fall Time		---	11.2	---	
C _{iss}	Input Capacitance	V _{DS} =-75V, V _{GS} =0V, f=1MHz	---	400	---	pF
C _{oss}	Output Capacitance		---	39	---	
C _{rss}	Reverse Transfer Capacitance		---	23	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	-2	A
I _{SM}	Pulsed Source Current ^{2,5}		---	---	-4	A
T _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =1A, di/dt=100A/μs	---	70	---	ns
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	113	---	nC
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=-50V, V_{GS}=-10V, L=1mH, R_G=25mΩ, I_{AS}=-4.8A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

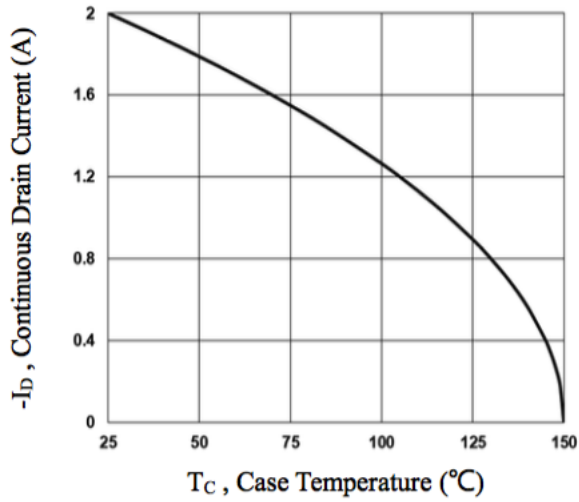


Fig.1 Continuous Drain Current vs. T_C

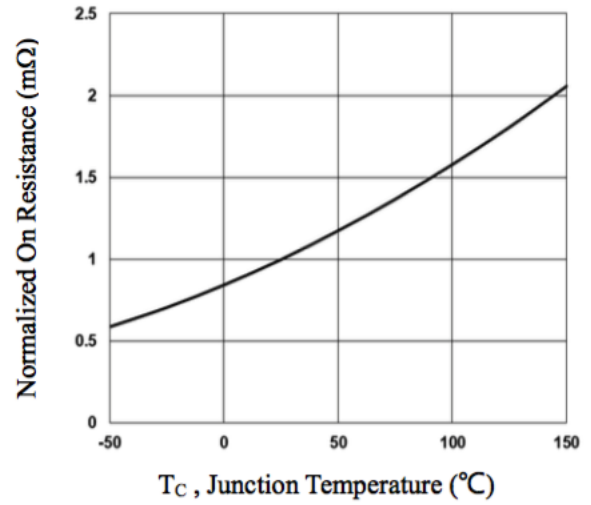


Fig.2 Continuous Drain Current vs. T_C

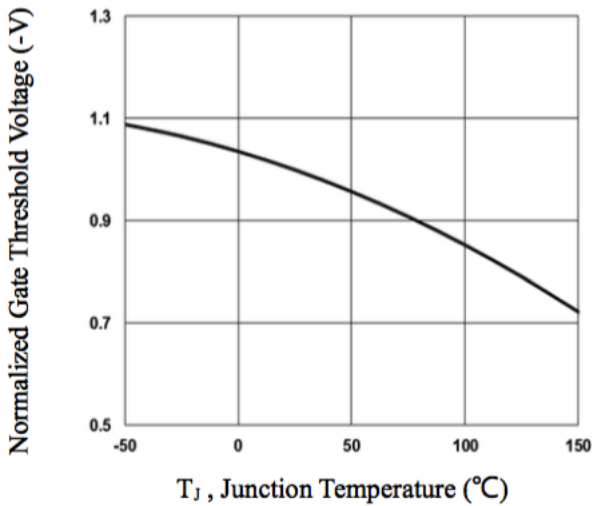


Fig.3 Normalized V_{th} vs. T_J

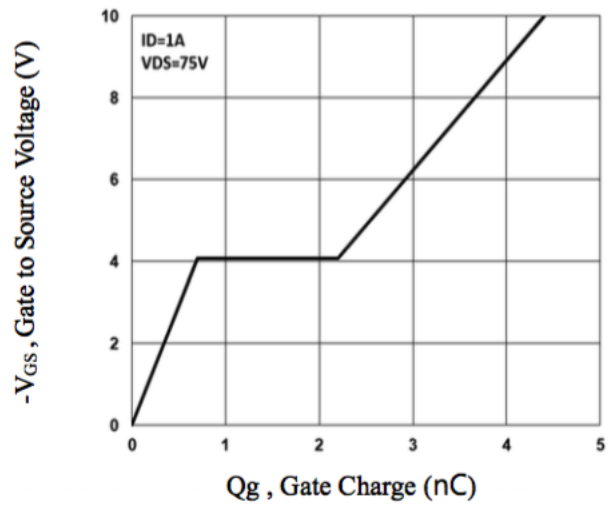


Fig.4 Gate Charge Waveform

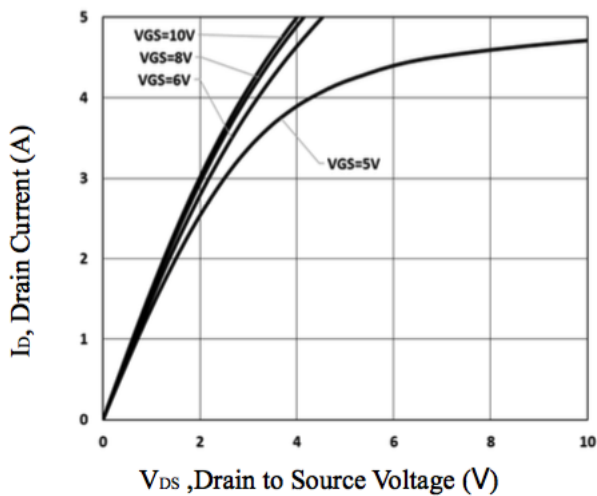


Fig.5 Typical Output Characteristics

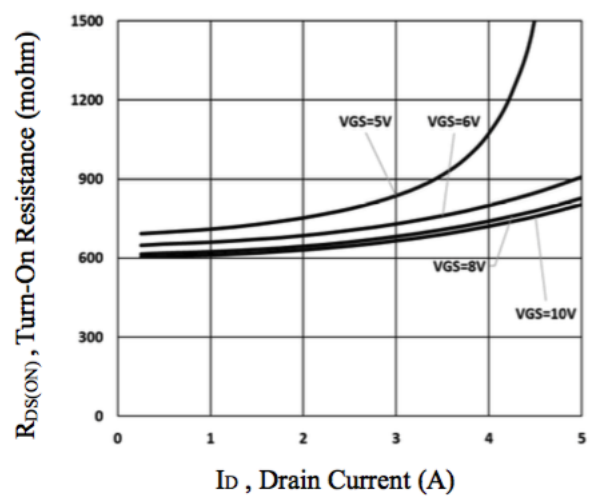


Fig.6 Turn-on Resistances vs. I_D



P-Ch 150V Fast Switching MOSFETs

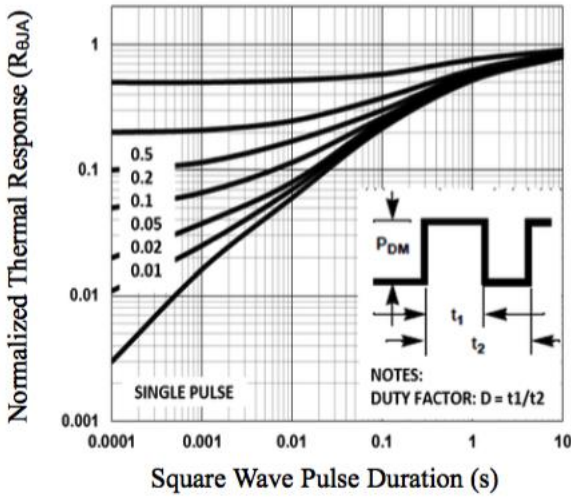


Fig.7 Normalized Transient Impedance

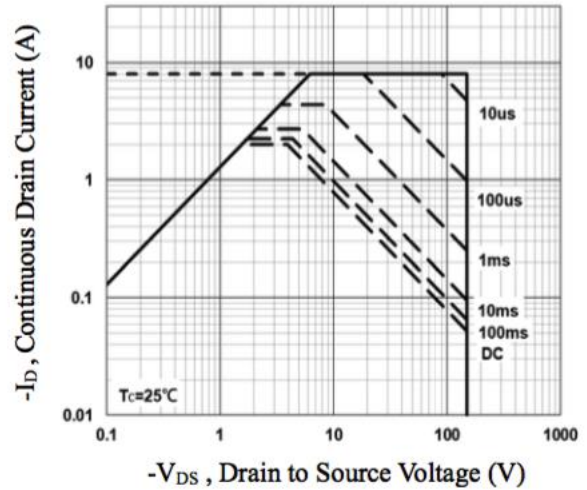


Fig.8 Maximum Safe Operation Area

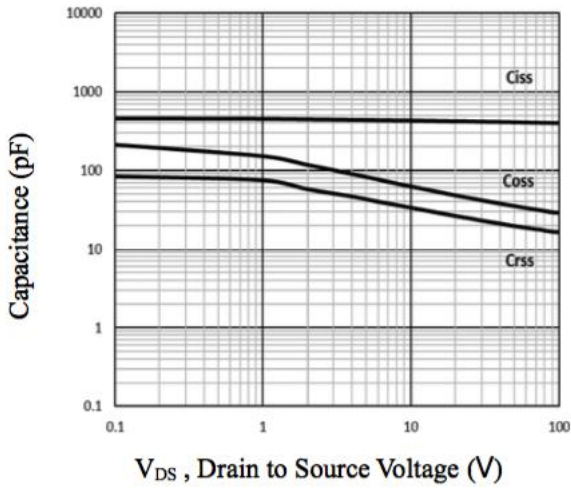


Fig.9 Capacitance Characteristics

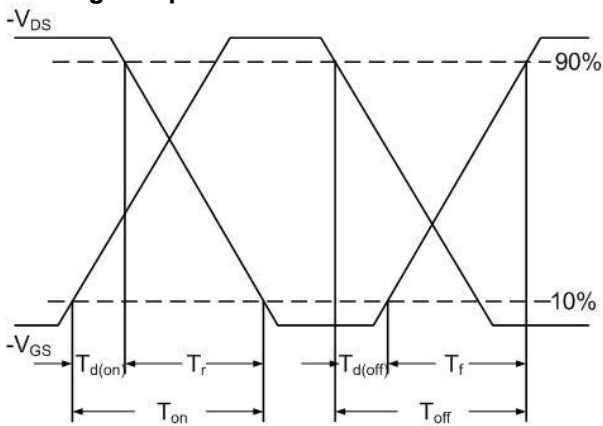


Fig.10 Switching Time Waveform

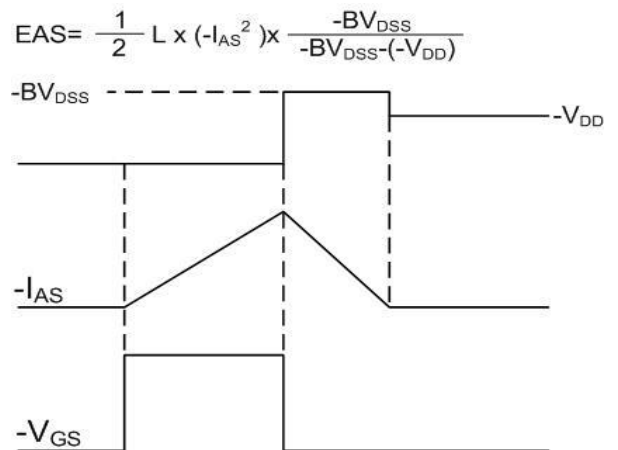
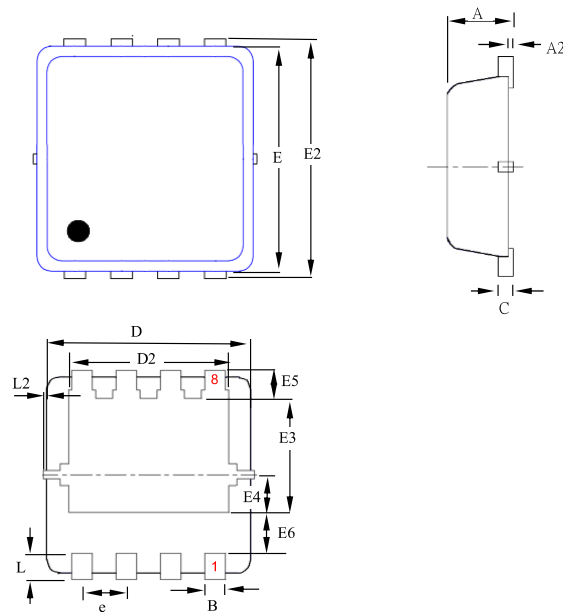


Fig.11 Unclamped Inductive Waveform

Ordering Information

Part Number	Package code	Packaging
HSBB02P15	PRPAK3*3	3000/Tape&Reel

PRPAK 3*3(E) Single Outline



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.80	0.90	0.028	0.031	0.035
A2	0.00	--	0.05	0.000	--	0.002
B	0.24	0.30	0.35	0.009	0.012	0.014
C	0.10	0.15	0.25	0.004	0.006	0.010
D	2.90	3.00	3.20	0.114	0.118	0.126
D2	2.15	2.35	2.59	0.085	0.093	0.102
E	2.90	3.00	3.12	0.114	0.118	0.123
E2	3.05	3.20	3.45	0.120	0.126	0.136
E3	1.55	1.75	1.95	0.061	0.069	0.077
E4	0.48	0.58	0.68	0.019	0.023	0.027
E5	0.28	0.43	0.58	0.011	0.017	0.023
E6	0.43	0.63	0.87	0.017	0.025	0.034
L	0.30	0.40	0.50	0.012	0.016	0.020
L2	0.00	--	0.10	0.000	--	0.004
e	--	0.65	--	--	0.026	--