

**CRYSTAL OSCILLATOR (Programmable)  
SPREAD SPECTRUM  
OUTPUT: CMOS**



Product Number  
**SG-9101CA: X1G005301xxxx00**  
**SG-9101CB: X1G005311xxxx00**  
**SG-9101CE: X1G005321xxxx00**  
**SG-9101CG: X1G005291xxxx00**

**SG-9101 series**

- Frequency range : 0.67 MHz to 170 MHz (1 ppm Step)
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE) or Standby ( $\overline{ST}$ )
- Configurable spread spectrum settings:  
 2 kinds of spread type, 6 kinds of spread width  
 4 kinds of modulation frequency, 3 kinds of spread profile
- PLL technology to enable short lead time
- Available field oscillator programmer "SG-Writer II"



**Specifications (characteristics)**

Item	Symbol	Specifications				Conditions/Remarks		
Supply voltage	V <sub>CC</sub>	1.80 V Typ.		2.50 V Typ.	3.30 V Typ.		-	
		1.62 V to 1.98 V	1.98 V to 2.20 V	2.20 V to 2.80 V	2.70 V to 3.63 V			
Output frequency range	f <sub>o</sub>	0.67 MHz to 170 MHz						
Storage temperature	T <sub>stg</sub>	-40 °C to +125 °C				Storage as single product.		
Operating temperature	T <sub>use</sub>	-40 °C to +85 °C						
		-40 °C to +105 °C						
Frequency tolerance <sup>*1</sup>	f <sub>tol</sub>	±50 × 10 <sup>-6</sup>				Average frequency of 1s gate time.		
Current consumption	I <sub>CC</sub>	3.4 mA Max.	3.5 mA Max.	3.6 mA Max.	3.7 mA Max.	T <sub>use</sub> = +105 °C	No load, f <sub>o</sub> = 20 MHz	
		2.9 mA Typ.		3.0 mA Typ.	3.2 mA Typ.	T <sub>use</sub> = +25 °C		
		5.7 mA Max.	6.0 mA Max.	6.9 mA Max.	8.3 mA Max.	T <sub>use</sub> = +105 °C	No load, f <sub>o</sub> = 170 MHz	
		4.9 mA Typ.		5.9 mA Typ.	7.0 mA Typ.	T <sub>use</sub> = +25 °C		
Output disable current	I <sub>dis</sub>	3.4 mA Max.	3.4 mA Max.	3.5 mA Max.	3.7 mA Max.	OE = GND, f <sub>o</sub> = 170 MHz		
Standby current	I <sub>std</sub>	0.9 µA Max.	1.0 µA Max.	1.5 µA Max.	2.5 µA Max.	T <sub>use</sub> = +105 °C	$\overline{ST}$ = GND	
		0.3 µA Typ.	0.4 µA Typ.	0.5 µA Typ.	1.1 µA Typ.	T <sub>use</sub> = +25 °C		
Symmetry	SYM	45 % to 55 %				50 % V <sub>CC</sub> Level		
Output voltage (DC characteristics)	V <sub>OH</sub>	90 % V <sub>CC</sub> Min.				I <sub>OH</sub> /I <sub>OL</sub> Conditions [mA]		
	V <sub>OL</sub>	10 % V <sub>CC</sub> Max.						
		Rise/Fall time		V <sub>CC</sub>	*A	*B	*C	*D
		Default (f <sub>o</sub> > 40 MHz),		I <sub>OH</sub>	-2.5	-3.5	-4.0	-5.0
		Fast		I <sub>OL</sub>	2.5	3.5	4.0	5.0
		Default (f <sub>o</sub> ≤ 40 MHz)		I <sub>OH</sub>	-1.5	-2.0	-2.5	-3.0
		Slow		I <sub>OL</sub>	1.5	2.0	2.5	3.0
				I <sub>OH</sub>	-1.0	-1.5	-2.0	-2.5
				I <sub>OL</sub>	1.0	1.5	2.0	2.5
		*A : 1.62 V to 1.98 V, *B : 1.98 V to 2.20 V, *C : 2.20 V to 2.80 V, *D : 2.70 V to 3.63 V						
Output load condition	L <sub>CMOS</sub>	15 pF Max.				-		
Input voltage	V <sub>IH</sub>	70 % V <sub>CC</sub> Min.				OE or $\overline{ST}$		
	V <sub>IL</sub>	30 % V <sub>CC</sub> Max.						
Rise time / Fall time	Default	tr/tf	3.0 ns Max.		f <sub>o</sub> > 40 MHz		20 % - 80 % V <sub>CC</sub> , L <sub>CMOS</sub> = 15 pF	
			6.0 ns Max.		f <sub>o</sub> ≤ 40 MHz			
			3.0 ns Max.		f <sub>o</sub> = 0.67 MHz to 170 MHz			
			10.0 ns Max.		f <sub>o</sub> = 0.67 MHz to 20 MHz			
Output disable time (OE)	tstp_oe	1 µs Max.				Measured from the time OE or $\overline{ST}$ pin crosses 30 % V <sub>CC</sub>		
Output disable time (ST)	tstp_st	1 µs Max.				Measured from the time OE pin crosses 70 % V <sub>CC</sub>		
Output enable time (OE)	tsta_oe	3 ms Max.				Measured from the time $\overline{ST}$ pin crosses 70 % V <sub>CC</sub>		
Output enable time (ST)	tsta_st	3 ms Max.				Measured from the time V <sub>CC</sub> reaches its rated minimum value, 1.62 V		
Start-up time	t <sub>str</sub>	3 ms Max.				Measured from the time V <sub>CC</sub> reaches its rated minimum value, 1.62 V		
Frequency aging	f <sub>age</sub>	This is included in frequency tolerance specification.				+25 °C, first year		

\*1 Frequency tolerance includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient, frequency / load coefficient and frequency aging (+25 °C, 1 year).

**Pin description**

Pin	Name	I/O type	Function	
1	OE	Input	Output enable	High <sup>*2</sup> : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), only output driver is disabled.
	$\overline{ST}$	Input	Standby	High <sup>*2</sup> : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I <sub>std</sub> .
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V <sub>CC</sub>	Power	Power supply	

\*2 Please do not use the OE/ST terminal in the open state.



Product Name

SG-9101CG 170.000000MHz C 20 P H A A A  
① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

- ① Model
- ② Package type
- ③ Frequency
- ④ Spread type
- ⑤ Spread width
- ⑥ Function
- ⑦ Operating temperature
- ⑧ Modulation frequency
- ⑨ Spread profile
- ⑩ Rise/Fall time

② Package type	
CG	2.5 mm × 2.0 mm
CE	3.2 mm × 2.5 mm
CB	5.0 mm × 3.2 mm
CA	7.0 mm × 5.0 mm

④ Spread type	
C	Center spread
D	Down spread

⑤ Spread width		
	Center spread	Down spread
02	±0.25 %	
05	±0.5 %	-0.5 %
07	±0.75 %	
10	±1.0 %	-1.0 %
15	±1.5 %	-1.5 %
20	±2.0 %	-2.0 %
30		-3.0 %
40		-4.0 %

⑧ Modulation frequency	
A	25.4 kHz (Default)
B	12.7 kHz
C	8.5 kHz
D	6.3 kHz

⑨ Spread profile	
A	Hershey-kiss (Default)
B	Sine-wave
C	Triangle

⑥ Function	
P	Output enable
S	Standby

⑩ Rise/Fall time	
A	Default
B	Fast
C	Slow

⑦ Operating temperature	
G	-40 °C to +85 °C
H	-40 °C to +105 °C

External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μF capacitor between V<sub>CC</sub> and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

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All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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	► Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.
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