RICOH

R1200x SERIES

STEP-UP DC/DC CONVERTER FOR OLED BACK LIGHT with SHUTDOWN FUNCTION

NO.EA-192-170925

OUTLINE

R1200x series are CMOS-based control type step-up DC/DC converter with low supply current ICs. Each of these ICs consists of a Nch MOSFET, NPN transistor, an oscillator, PWM comparator, a voltage reference unit, an error amplifier, a current limit circuit, an under voltage lockout circuit (UVLO), an over voltage protection circuit (OVP), and a soft start circuit. As the external components, an inductor, resistances or capacitors are necessary to make a constant output voltage of step-up DC/DC converter with the R1200x. At standby mode, the NPN transistor can separate the output from the input. During the situation of that, there are two versions. R1200xxxxA: the output of VouT is generated to 0V by the low resistance (with the auto discharge function). R1200xxxxB does not generate the output of VouT (without the auto discharge function).

The soft-start time (Typ. 1.5ms) and the maximum duty cycle (Typ. 91%) are set internally. For the protection functions of R1200x series are the current limit function of the Lx peak current, the OVP function for detection the over voltage of output and the UVLO function for protective miss-operation by the low voltage. (The threshold of OVP is selectable from 17V, 19V or 21V.)

Since the packages for these ICs are DFN1616-6, DFN(PLP)1820-6, SOT-23-6 and WLCSP-6-P1, therefore high density mounting of the ICs on boards is possible.

FEATURES

Supply Current	Τур. 500μΑ
Standby Current	Μax. 3μA
Input Voltage Range	2.3V to 5.5V
Feedback Voltage	1.0V (Externally adjustable)
Feedback Voltage Accuracy	±1.5%
• Temperature-Drift Coefficient of Feedback Volt	age±150ppm/°C
Oscillator Frequency	Typ. 1.2MHz
Maximum Duty Cycle	Typ. 91%
Switch ON Resistance	Typ. 1.35Ω
UVLO Detector Threshold	Typ. 2.0V
Soft-start Time	Typ. 1.5ms
Lx Current Limit Protection	Typ. 700mA
OVP Detector Threshold	17V, 19V, 21V
Switching Control	PWM
Built-in a rectifier NPN transistor, at standby meaning to the standard meaning and the standard meaning are standard meaning at the standard meaning at the standard meaning are standard meaning at the standard meaning at the standard meaning are standard meaning at the standard	ode, complete shutdown is possible.
Built-in Auto discharge function	A version
Packages	DFN1616-6, DFN(PLP)1820-6, SOT-23-6,
	WLCSP-6-P1
Ceramic capacitors are recommended	1μF

APPLICATION

- OLED power supply for portable equipment
- White LED Backlight for portable equipment

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SELECTION GUIDE

The OVP threshold voltage, auto discharge function, and the package for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1200Zxxx*-E2-F	WLCSP-6-P1	5,000 pcs	Yes	Yes
R1200Lxxx*-TR	DFN1616-6	5,000 pcs	Yes	Yes
R1200Kxxx*-TR	DFN(PLP)1820-6	5,000 pcs	Yes	Yes
R1200Nxxx*-TR-FE	SOT-23-6	3,000 pcs	Yes	Yes

xxx: Designation of OVP detector threshold

(001) 17V threshold of OVP

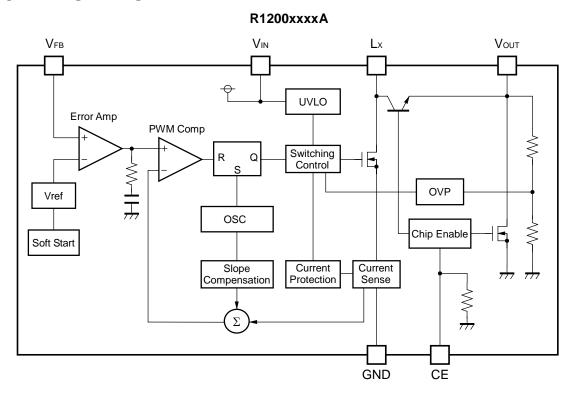
(002) 19V threshold of OVP

(003) 21V threshold of OVP

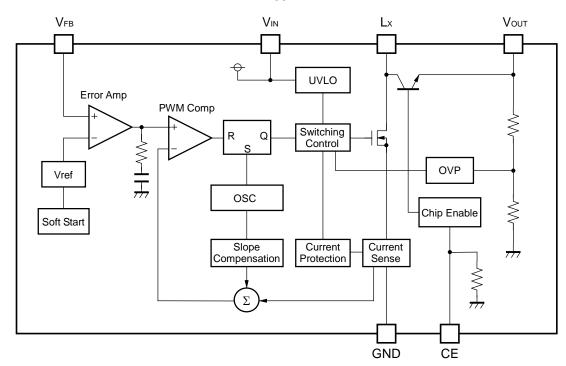
- * : The auto discharge function at off state are options as follows.
 - (A) with auto discharge function at off state
 - (B) without auto discharge function at off state

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BLOCK DIAGRAMS



R1200xxxxB



DFN1616-6

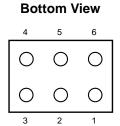
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PIN DESCRIPTIONS



Top View

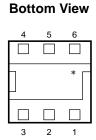
6 5 4



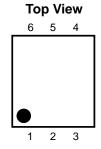
Top View

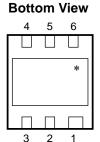
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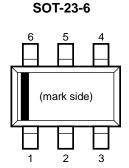
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DFN(PLP)1820-6







WLCSP-6-P1

Pin No Symbol Pin Description		Pin Description	
1	Lx	Switching Pin (Open Drain Output)	
2	VIN	Power Supply Input Pin	
3	V _{FB}	Feedback Pin	
4	CE	Chip Enable Pin ("H" Active)	
5	Vouт	Output Pin	
6	GND	Ground Pin	

• DFN1616-6, DFN(PLP)1820-6

Pin No	Symbol	Pin Description	
1	CE	Chip Enable Pin ("H" Active)	
2	V _{FB}	Feedback Pin	
3	Lx	Switching Pin (Open Drain Output)	
4	GND	Ground Pin	
5	V _{DD}	Input Pin	
6	Vouт	Output Pin	

st) Tab is GND level. (They are connected to the reverse side of this IC.)

The tab is better to be connected to the GND, but leaving it open is also acceptable.

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• SOT-23-6

Pin No	Symbol	Pin Description	
1	CE	Chip Enable Pin ("H" Active)	
2	Vouт	Output Pin	
3	V _{DD}	Input Pin	
4	Lx	Switching Pin (Open Drain Output)	
5	GND	Ground Pin	
6	V _{FB}	Feedback Pin	

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ABSOLUTE MAXIMUM RATINGS

(GND=0V)

					(GIAD-OA)
Symbol		Item		Rating	Unit
VIN	V _{IN} Pin Voltage			-0.3 to 6.5	V
Vce	CE Pin Voltage			-0.3 to V _{IN} +0.3	V
V _{FB}	V _{FB} Pin Voltage			-0.3 to V _{IN} +0.3	V
Vоит	Vоит Pin Voltage			-0.3 to 25.0	V
V _L X	Lx Pin Voltage			-0.3 to 25.0	V
ILX	Lx Pin Current			1000	mA
		Standard Test Land Pattern	WLCSP-6-P1	633	
P□	Power Dissipation*		DFN1616-6	2400	mW
		JEDEC STD. 51-7 Test Land Pattern	DFN(PLP)1820-6	2200	
		Land I attorn	SOT-23-6	660	
Tj	Junction Temperatur	e Range		-40 to 125	°C
Tstg	Storage Temperature	e Range		-55 to 125	°C

^{*)} For Power Dissipation, please refer to POWER DISSIPATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Rating	Unit
$V_{\scriptscriptstyle IN}$	Input Voltage	2.3 to 5.5	V
Та	Operating Temperature Range	-40 to 85	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such ratings by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.



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ELECTRICAL CHARACTERISTICS

• R1200x Ta=25°C

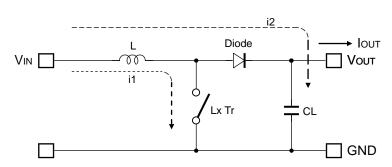
Symbol	Item	Condit	ions	Min.	Тур.	Max.	Unit
IDD	Supply Current	V _{IN} =5.5V, V _{FB} =0V, Lx at no load			0.5	1.0	mA
Istandby	Standby Current	VIN=5.5V, VCE=0V			0	3.0	μА
V _{UVLO1}	UVLO Detector Threshold	V _{IN} falling		1.9	2.0	2.1	V
V _{UVLO2}	UVLO Released Voltage	V _{IN} rising			Vuvlo1 +0.10	2.25	V
Vсен	CE Input Voltage "H"	VIN=5.5V		1.5			V
Vcel	CE Input Voltage "L"	VIN=2.3V				0.5	V
Rce	CE Pull Down Resistance	VIN=3.6V		600	1200	2200	kΩ
V _{FB}	V _{FB} Voltage Accuracy	VIN=3.6V		0.985	1.0	1.015	V
ΔV _{FB} / ΔTa	V _{FB} Voltage Temperature Coefficient	V _{IN} =3.6V, −40°C ≤ 7	ā ≤ 85°C		±150		ppm /°C
lfв	V _{FB} Input Current	VIN=5.5V, VFB=0V 01	5.5V	-0.1		0.1	μА
tstart	Soft-start Time	VIN=3.6V	V _{IN} =3.6V		1.5		ms
Ron	Switch ON Resistance	Vin=3.6V, Isw=100m	A		1.35		Ω
LXleak	Switch Leakage Current				0	3.0	μА
LXlim	Switch Current Limit	VIN=3.6V		400	700	1000	mA
V _{NPN}	NPN Vce Voltage	INPN=100mA			0.8		V
INPNOFF1	NPN Leakage Current 1	Vоит=23V				10	μА
INPNOFF2	NPN Leakage Current 2	Vоит=0V, VLX=5.5V				3.0	μА
fosc	Oscillator Frequency	VIN=3.6V, VOUT=VFB=	=0V	1.0	1.2	1.4	MHz
Maxduty	Maximum Duty Cycle	VIN=3.6V, VOUT=VFB=	=0V	86	91		%
			R1200x001x	16	17	18	
V _{OVP1}	OVP Detector Threshold	Vin=3.6V, Vout rising	R1200x002x	18	19	20	V
		Voornsing	R1200x003x	20	21	22	
V _{OVP2}	OVP Released Voltage	V _{IN} =3.6V, V _{OUT} falling			V _{OVP1} -1.1		V
Іріясна	Vоит Discharge Current	VIN=3.6V, VOUT=0.1	/ R1200xxxxA		0.7		mA
Іνоυт	OVP Sense Current	VIN=3.6V, VOUT=23V			6.0		μА

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OPERATING DESCRIPTIONS

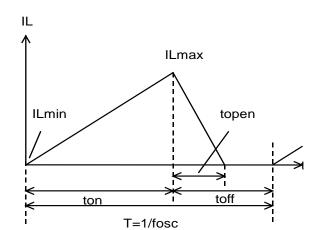
Operation of Step-Up DC/DC Converter and Output Current

<Basic Circuit>

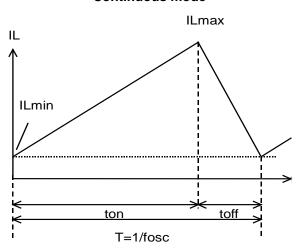


<Current through L>

Discontinuous mode



Continuous mode



There are two operation modes of the step-up PWM control-DC/DC converter. That is the continuous mode and discontinuous mode by the continuousness inductor.

When the transistor turns ON, the voltage of inductor L becomes equal to VIN voltage. The increase value of inductor current (i1) will be

$$\Delta i1 = V_{IN} \times ton / L$$
 Formula 1

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As the step-up circuit, during the OFF time (when the transistor turns OFF) the voltage is continually supply from the power supply. The decrease value of inductor current (i2) will be

$$\Delta i2 = (V_{OUT} - V_{IN}) \times topen / L$$
 Formula 2

At the PWM control-method, the inductor current become continuously when topen=toff, the DC/DC converter operate as the continuous mode.

In the continuous mode, the variation of current of i1 and i2 is same at regular condition.

$$V_{IN} \times ton / L = (V_{OUT} - V_{IN}) \times toff / L$$
 Formula 3

The duty at continuous mode will be

The average of inductor current at tf = toff will be

If the input voltage = output voltage, the lout will be

If the IouT value is large than above the calculated value (Formula 6), it will become the continuous mode, at this status, the peak current (ILmax) of inductor will be

$$IL_{max} = I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times ton / (2 \times L)....$$
Formula 7

$$IL_{max} = I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times T \times (V_{OUT} - V_{IN}) / (2 \times L \times V_{OUT}).....Formula~8$$

The peak current value is larger than the lout value. In case of this, selecting the condition of the input and the output and the external components by considering of ILmax value.

The explanation above is based on the ideal calculation, and the loss caused by Lx switch and the external components are not included.

The actual maximum output current will be between 50% and 80% by the above calculations. Especially, when the IL is large or V_{IN} is low, the loss of V_{IN} is generated with on resistance of the switch. Moreover, it is necessary to consider Vf of the diode (approximately 0.8V) about V_{OUT}.



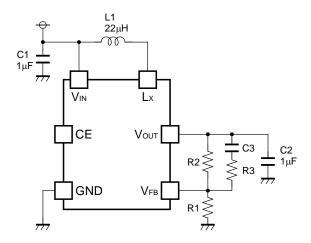
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Shutdown

- At standby mode, the output is completely separated from the input and shutdown by the NPN transistor of internal IC. However, the leakage current is generated when the Lx pin voltage is equal or more than V_{IN} pin voltage at standby mode.
- R1200xxxxA (with auto discharge function): In the term of standby mode, the switch is turned ON between Vout to GND and the Vout capacitor is discharged.
- R1200xxxxB (without auto discharge function): The built-in switch for discharge does not turn on, but the OVP sense resistors between Vout and GND exists as same as A version.
- · However, the both version (A/B) has the OVP sense resistance (4 to 5MΩ) between Vout and GND (refer to OVP sense current (Ivout) on ELECTRICAL CHARACTERISTICS table) and the current flows through from Vout to GND.

APPLICATION INFORMATION

Typical Applications



Selection of Inductors

The peak current of the inductor at normal mode can be estimated as the next formula when the efficiency is 80%.

ILmax=1.25 x Iout x Vout / Vin + 0.5 x Vin x (Vout - Vin) / (L x Vout x fosc)

In the case of start-up or dimming control by CE pin, inductor transient current flows, and the peak current of it must be equal or less than the current limit of the IC. The peak current should not beyond the rated current of the inductor.

The recommended inductance value is 4.7 μ H – 22 μ H.

Table 1 Peak current value in each condition

	Con	dition		
Vin (V)	Vout (V)	lout (mA)	L (μH)	ILmax (mA)
3	14	20	10	215
3	14	20	22	160
3	21	20	10	280
3	21	20	22	225



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Table 2 Recommended inductors

L (μH)	Part No.	Rated Current (mA)	Size (mm)
10	LQH32CN100K53	450	3.2 x 2.5 x 1.55
10	LQH2MC100K02	225	2.0 x 1.6 x 0.9
10	VLF3010A-100	490	2.8 x 2.6 x 0.9
10	VLS252010-100	520	2.5 x 2.0 x 1.0
22	LQH32CN220K53	250	3.2 x 2.5 x 1.55
22	LQH2MC220K02	185	2.0 x 1.6 x 0.9
22	VLF3010A-220	330	2.8 x 2.6 x 0.9
4.7	LQH32CN4R7M53	650	3.2 x 2.5 x 1.55

Selection of Capacitors

Set $1\mu F$ or more value bypass capacitor C1 between V_{IN} pin and GND pin as close as possible. Set $1\mu F - 4.7\mu F$ or more capacitor C2 between V_{OUT} and GND pin.

Table 3 Recommended components

	Rated voltage(V)	Part No.
C1	6.3	CM105B105K06
C2	25	GRM21BR11E105K
C3	25	22pF
R1		For Vo∪⊤ Setting
R2		For Vou⊤ Setting
R3		2kΩ

• External Components Setting

· If the spike noise of V_{OUT} may be large, the spike noise may be picked into V_{FB} pin and make the operation unstable. In this case, use a R3 of the resistance value in the range from $1k\Omega$ to $5k\Omega$ to reduce a noise level of V_{FB} .

The Method of Output Voltage Setting

· The output voltage can be calculated with divider resistors (R1 and R2) values as the following formula:

Output Voltage =
$$V_{FB} \times (R1 + R2) / R1$$

• The total value of R1 and R2 should be equal or less than $300 k\Omega$. Make the V_{IN} and GND line sufficient. The large current flows through the V_{IN} and GND line due to the switching. If this impedance (V_{IN} and GND line) is high, the internal voltage of the IC may shift by the switching current, and the operating may become unstable. Moreover, when the built-in Lx switch is turn OFF, the spike noise caused by the inductor may be generated. As a result of this, recommendation voltage rating of capacitor (C2) value is equal 1.5 times larger or more than the setting output voltage.

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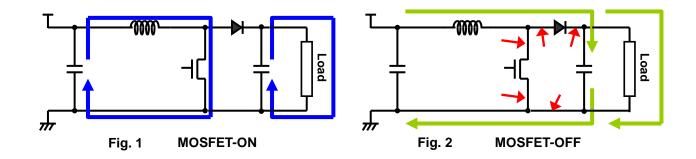
TECHNICAL NOTES

Current Path on PCB

The current paths in an application circuit are shown in Fig. 1 and 2. A current flows through the paths shown in Fig. 1 at the time of MOSFET-ON, and shown in Fig. 2 at the time of MOSFET-OFF. In the paths pointed with red arrows in Fig. 2, current flows just in MOSFET-ON period or just in MOSFET-OFF period. Parasitic impedance/inductance and the capacitance of these paths influence stability of the system and cause noise outbreak. So please minimize this side effect. In addition, please shorten the wiring of other current paths shown in Fig. 1 and 2 except for the paths of LED load.

Layout Guide for PCB

- Please shorten the wiring of the input capacitor (C1) between V_{IN} pin and GND pin of IC. The GND pin should be connected to the strong GND plane.
- · The area of Lx land pattern should be smaller.
- · Please put output capacitor (C2) close to the Vout pin.
- · Please make the GND side of output capacitor (C2) close to the GND pin of IC.

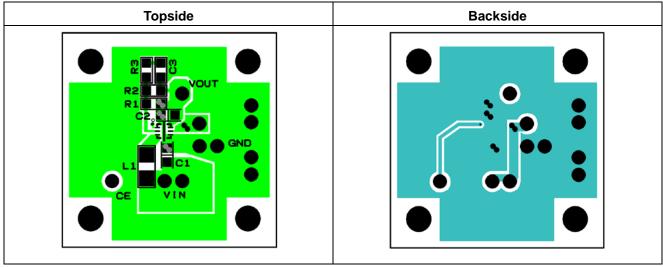


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PCB Layout

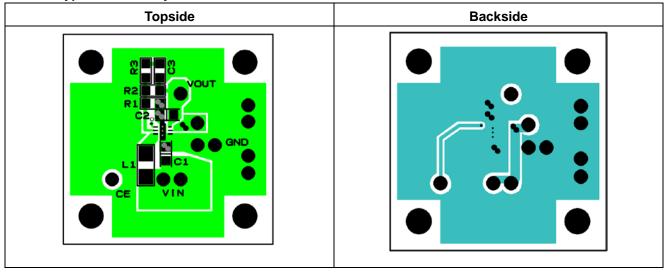
· PKG: DFN1616-6pin

R1200L Typical Board Layout



- PKG:DFN(PLP)1820-6pin

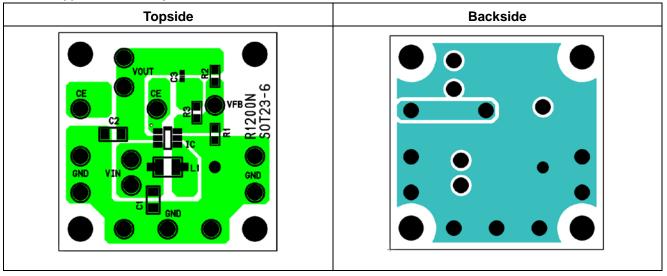
R1200K Typical Board Layout



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• PKG:SOT-23-6pin

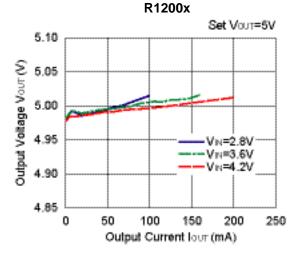
R1200N Typical Board Layout

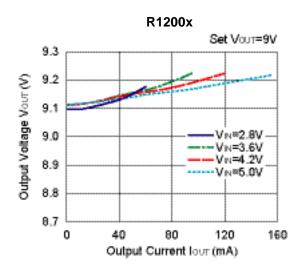


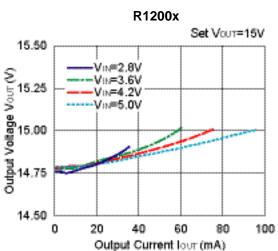
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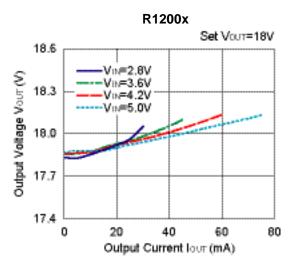
TYPICAL CHARACTERISTICS

1) Output Voltage vs. Output Current (L=22μH)

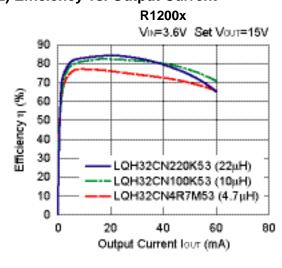


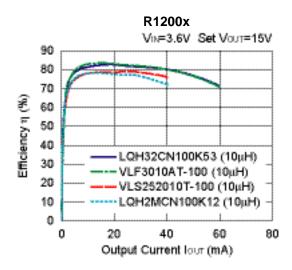




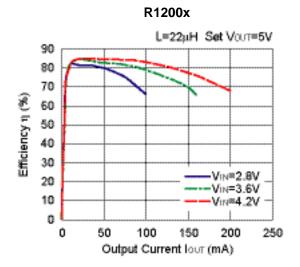


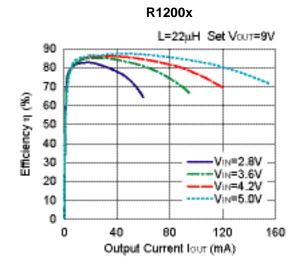
2) Efficiency vs. Output Current

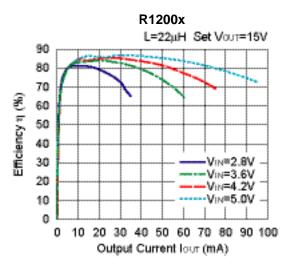


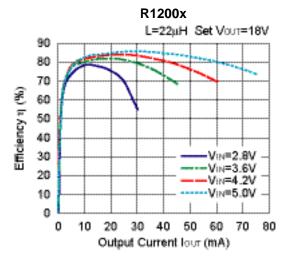


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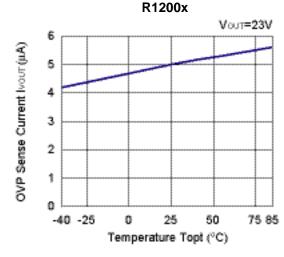


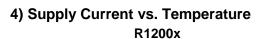


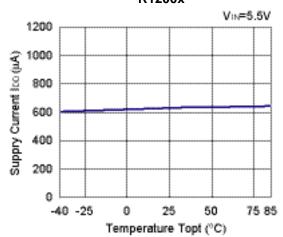




3) OVP Sense Current vs. Temperature

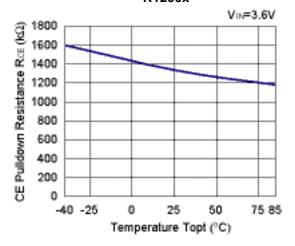




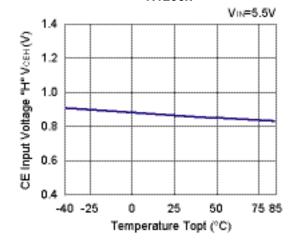


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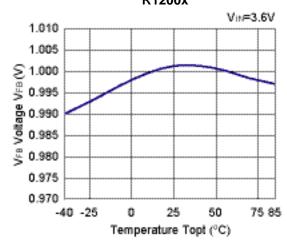
5) CE Pulldown Resistance vs. Temperature R1200x



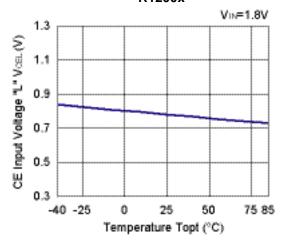
7) CE Input Voltage "H" vs. Temperature R1200x



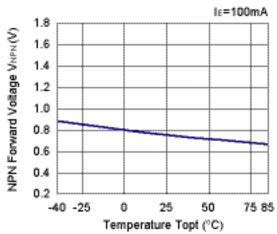
9) V_{FB} Voltage vs. Temperature R1200x



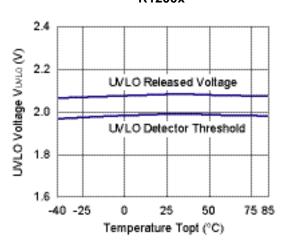
6) CE Input Voltage "L" vs. Temperature R1200x



8) NPN VCE Voltage vs. Temperature R1200x

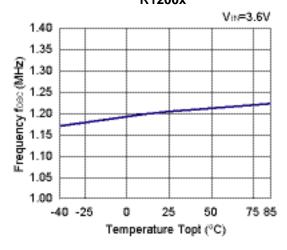


10) UVLO Detect / Released Voltage vs. Temperature R1200x

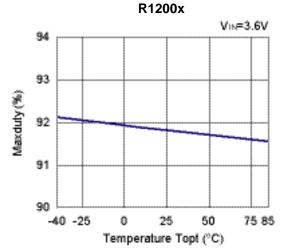


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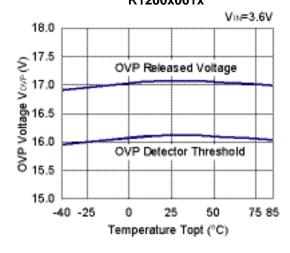
11) Oscillator Frequency vs. Temperature R1200x



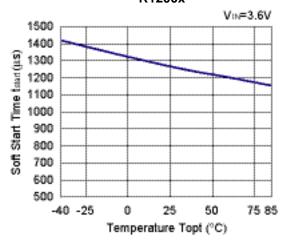
12) Maxduty vs. Temperature



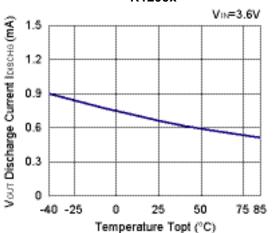
13) OVP Detect / Released Voltage vs. Temperature R1200x001x



14) Soft-start Time vs. Temperature R1200x

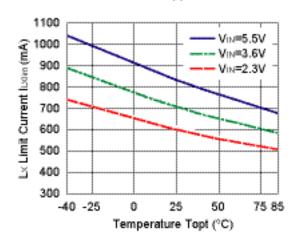


15) Vouτ Discharge Current vs. Temperature R1200x

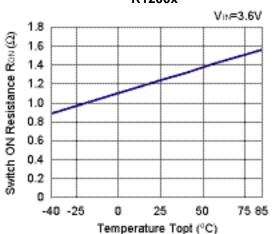


NO.EA-192-170925

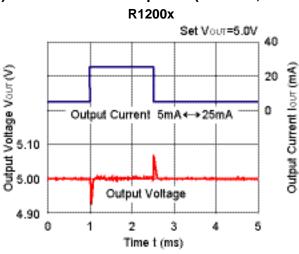
16) Lx Limit Current vs. Temperature R1200x

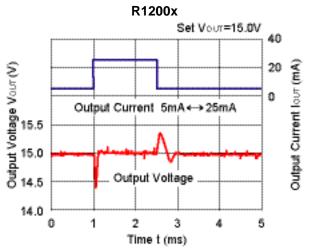


17) Switch ON Resistance vs. Temperature R1200x

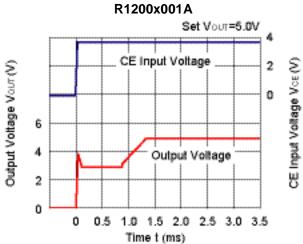


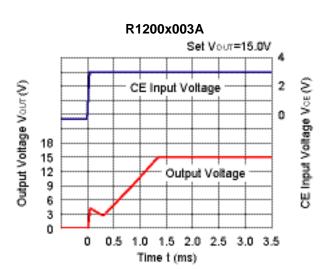
18) Load Transient Response (V_{IN}=3.6V, Ioυτ=5mA↔25mA, tr=tf=0.5μs)





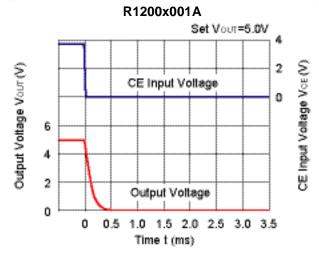
19) Start-up Waveform (VIN=3.6V, IOUT=20mA)

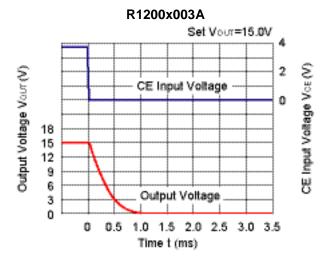




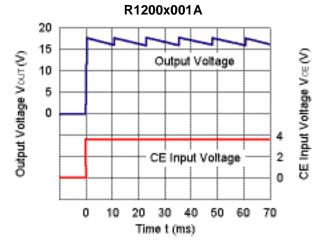
NO.EA-192-170925

20) Shut-down Waveform (VIN=3.6V, IOUT=20mA)





21) OVP Waveform (V_{FB}=0V)



The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

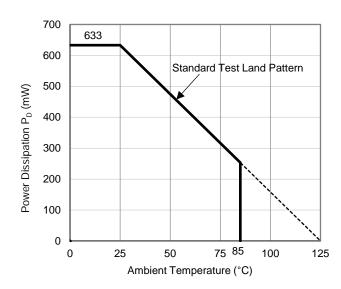
Measurement Conditions

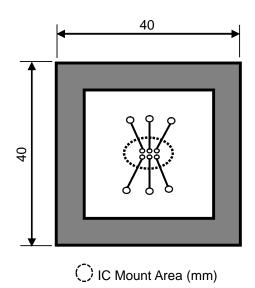
	Standard Test Land Pattern
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Double-Sided Board)
Board Dimensions	40 mm × 40 mm × 1.6 mm
Copper Ratio	Top Side: Approx. 50%
	Bottom Side: Approx. 50%
Through-holes	-

Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

	Standard Test Land Pattern
Power Dissipation	633 mW
Thermal Resistance	θja = (125 - 25°C) / 0.633 W = 158°C/W

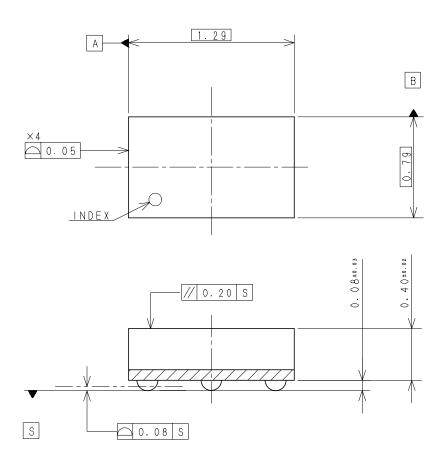


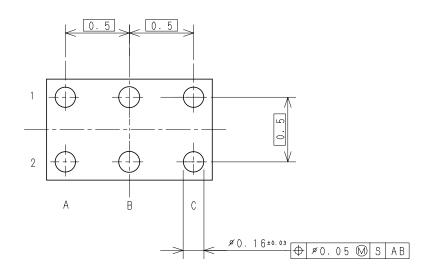


Power Dissipation vs. Ambient Temperature

Measurement Board Pattern

i





WLCSP-6-P1 Package Dimensions (Unit: mm)

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

Measurement Conditions

Item	Measurement Conditions (JEDEC STD. 51-7)		
Environment	Mounting on Board (Wind Velocity = 0 m/s)		
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)		
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm		
Copper Ratio	1st Layer: Less than 95% of 50 mm Square 2nd, 3rd, 4th Layers: Approx. 100% of 50 mm Square		
Through-holes	φ 0.2 mm × 15 pcs		

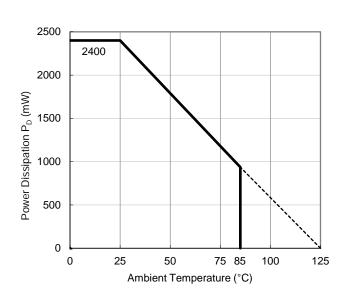
Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

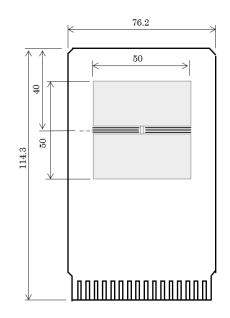
Item	Measurement Result
Power Dissipation	2400 mW
Thermal Resistance (θja)	θja = 41°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 11°C/W

 θ ja: Junction-to-ambient thermal resistance.

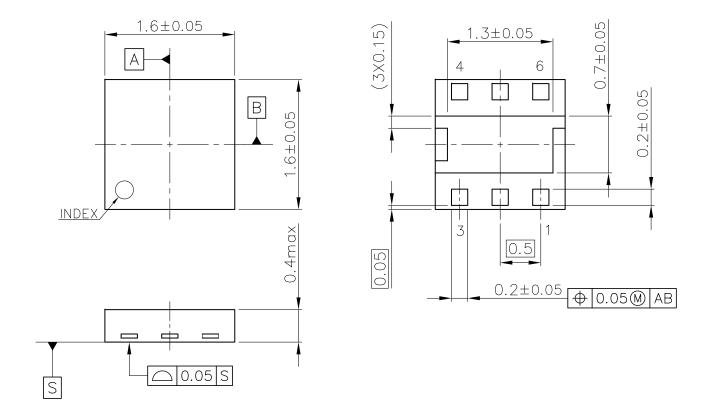
ψjt: Junction-to-top of package thermal characterization parameter.



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



DFN1616-6 Package Dimensions (Unit: mm)

i

^{*} The tab on the bottom of the package shown by blue circle is a substrate potential (GND). It is recommended that this tab be connected to the ground plane on the board but it is possible to leave the tab floating.

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

Measurement Conditions

Item	Measurement Conditions (JEDEC STD. 51-7)		
Environment	Mounting on Board (Wind Velocity = 0 m/s)		
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)		
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm		
Copper Ratio	1st Layer: Less than 95% of 50 mm Square 2nd, 3rd, 4th Layers: Approx. 100% of 50 mm Square		
Through-holes	φ 0.2 mm × 34 pcs		

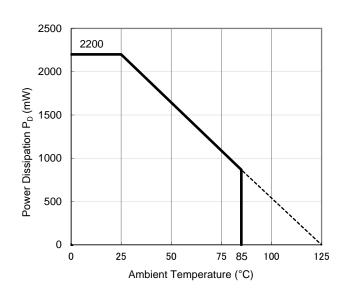
Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

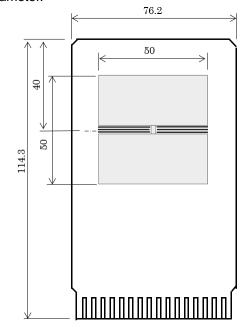
Item	Measurement Result
Power Dissipation	2200 mW
Thermal Resistance (θja)	θja = 45°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 18°C/W

 θ ja: Junction-to-ambient thermal resistance.

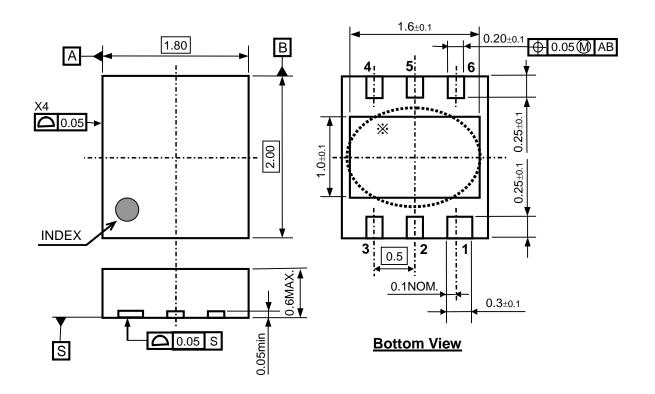
ψjt: Junction-to-top of package thermal characterization parameter.



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



DFN(PLP)1820-6 Package Dimensions (Unit: mm)

i

^{*} The tab on the bottom of the package is substrate level (GND). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left floating.

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

Measurement Conditions

Item	Measurement Conditions (JEDEC STD. 51-7)		
Environment	Mounting on Board (Wind Velocity = 0 m/s)		
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)		
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm		
Copper Ratio	1st Layer : Less than 95% of 50 mm Square 2nd, 3rd, 4th Layers: Approx. 100% of 50 mm Square		
Through-holes	φ 0.3 mm × 7 pcs		

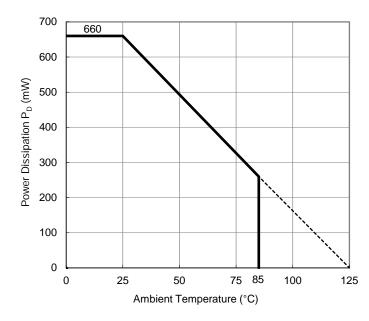
Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

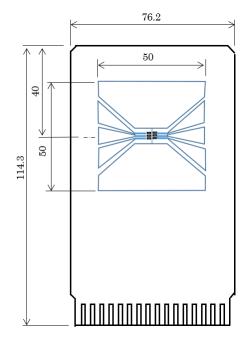
Item	Measurement Result
Power Dissipation	660 mW
Thermal Resistance (θja)	θja = 150°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 51°C/W

 θ ja: Junction-to-ambient thermal resistance.

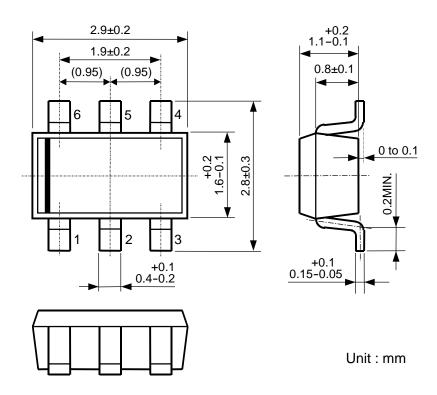
ψjt: Junction-to-top of package thermal characterization parameter



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



SOT-23-6 Package Dimensions



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